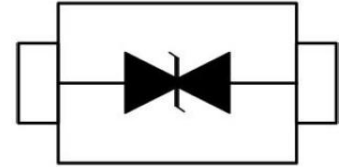


APPLICATIONS

Serial ATA.
Desktops, Servers and Notebooks.
Cellular Phones.
MDDI Ports.
USB2.0 Power and Data Line Protection.
Display Ports
Digital Visual Interfaces (DVI).



FEATURES

Protects one data or power line.
Ultra low leakage: nA level.
Low clamping voltage.
2-pin leadless package.
Complies with following standards:
IEC 61000-4-2 (ESD): $\pm 30\text{kV}$ (Air)
IEC 61000-4-2 (ESD): $\pm 30\text{kV}$ (Contact)
IEC61000-4-4 (EFT) 40A (5/50ns)



SOD523

MACHANICAL DATA

Package: SOD-523.
Lead Finish: Matte Tin.
Case Material: “Green” Molding Compound.
Terminal Connections: See Diagram Below.
Marking Information: See Below.
Tape & Reel
Reel Size : 7 inch.
Quantity per reel:3000pcs
Marking code: According to the physical

DEVICE CHARACTERISTICS

Absolute Maximum Ratings ($T_A=25^{\circ}\text{C}$ unless otherwise specified)			
Parameter	Symbol	Value	Unit
Peak Pulse Power (8/20 μs)	Ppp	400	W
ESD per IEC 61000-4-2 (Air)	VESD	± 30	kV
ESD per IEC 61000-4-2 (Contact)		± 30	
Operating Temperature Range	TJ	-55to +125	$^{\circ}\text{C}$
Storage Temperature Range	Tstg	-55to +150	$^{\circ}\text{C}$

ELECTRICAL CHARACTERISTICS ($T_A=25^{\circ}\text{C}$ unless otherwise specified)

ELECTRICAL CHARACTERISTICS ($T_{\text{amb}}=25^{\circ}\text{C}$)						
Symbol	Parameter	Test Condition	Min	Typ	Max	Units
VRWM	Reverse Working Voltage				5.0	V
VBR	Reverse Breakdown Voltage	$I_T = 1\text{mA}$	5.8		7.8	V
IR	Reverse Leakage Current	$VRWM = 5\text{V}$			1.0	μA
VC	Clamping Voltage	$I_{PP} = 20\text{A}$, $t_p = 8/20 \mu\text{s}$		15	20	V
CJ	Junction Capacitance	$VR = 0\text{V}$, $f = 1\text{MHz}$		30	40	pF

ELECTRICAL CHARACTERISTICS

① Surge waveform: 8/20 μs

VR : Stand-off Voltage -- Maximum voltage that can be applied
 VBR: Breakdown Voltage

VC: Clamping Voltage -- Peak voltage measured across the suppressor at a specified I_{pp}

IR: Reverse Leakage Current.

TYPICAL CHARACTERISTICS(TA=25°C unless otherwise Specified)

FIG.1: V- I curve characteristics (Bi-directional)

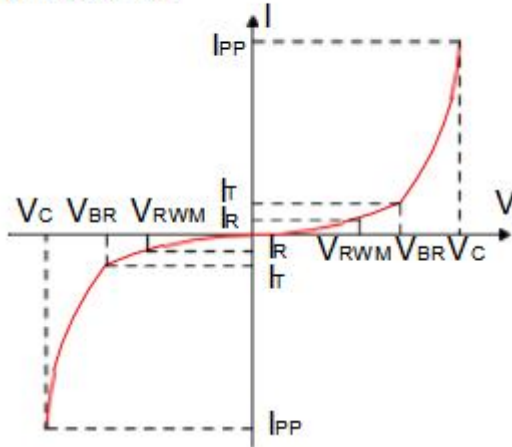
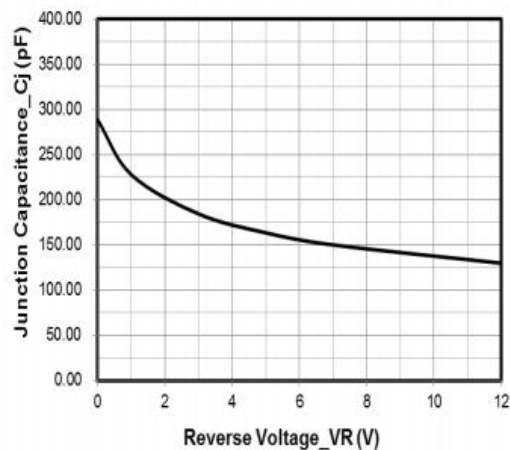
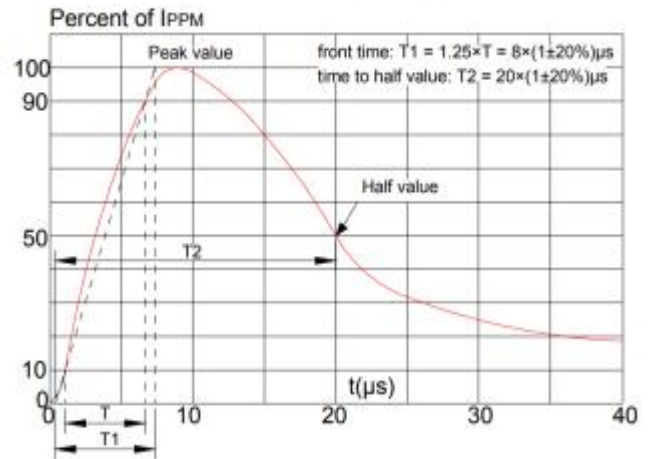
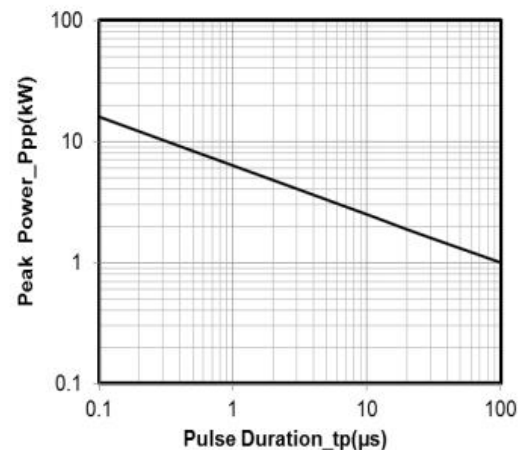


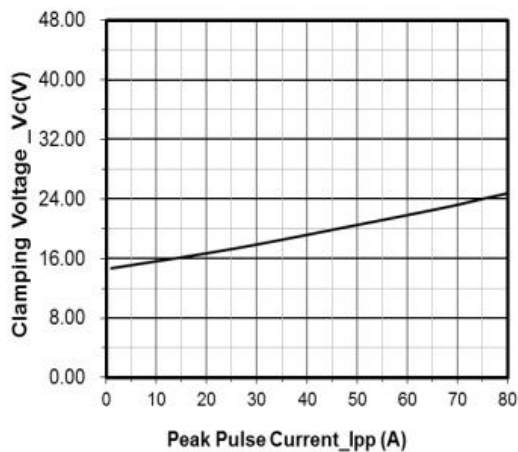
FIG.2: Pulse waveform (8/20μs)



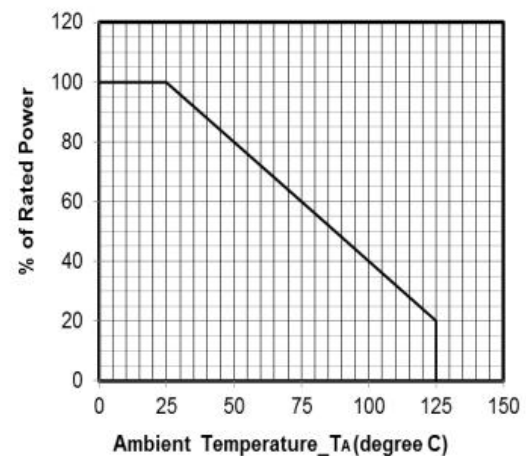
Junction Capacitance vs. Reverse Voltage



Peak Pulse Power vs. Pulse Time

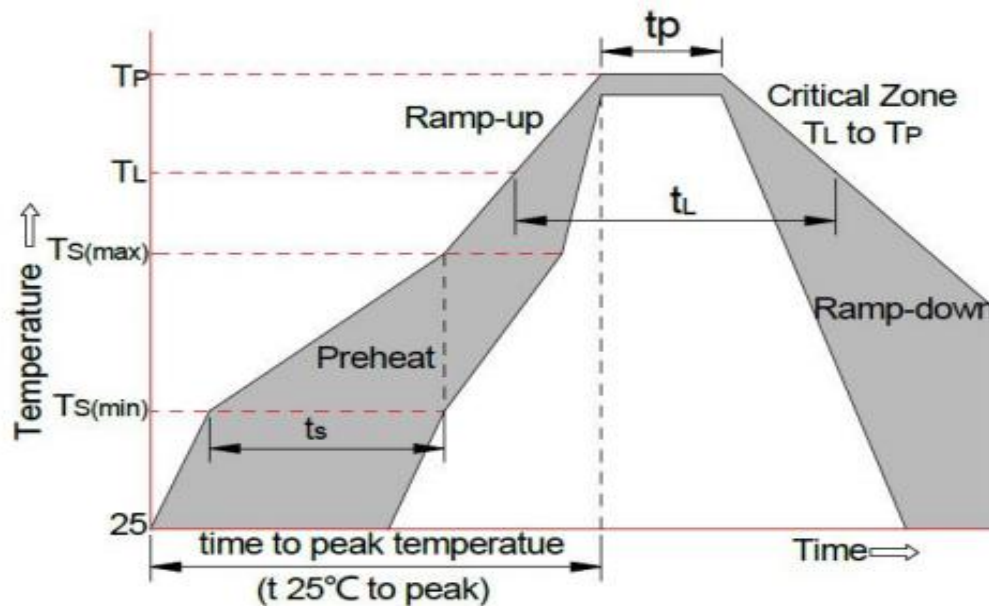


Clamping Voltage vs. Peak Pulse Current



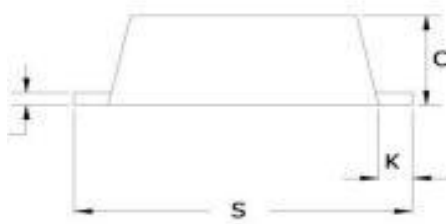
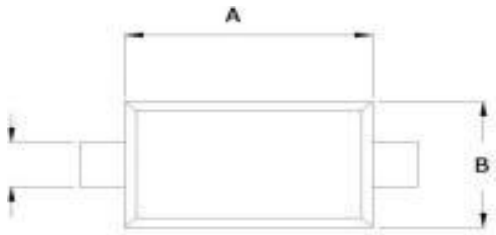
Power Derating Curve

SOLDERING PARAMETERS

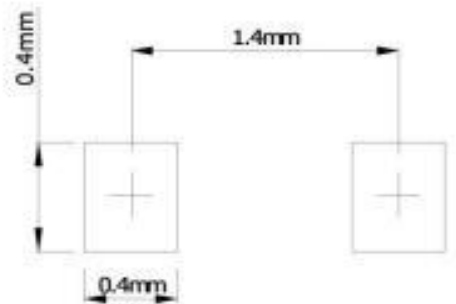


Reflow Condition		Pb-Free assembly (see FIG.5)
Pre Heat	-Temperature Min ($T_{s(min)}$)	+150 °C
	-Temperature Max($T_{s(max)}$)	+200 °C
	-Time (Min to Max) (t_s)	60-180 secs.
Average ramp up rate (Liquid us Temp (T_L) to peak)		3 °C/sec. Max
$T_{s(max)}$ to T_L - Ramp-up Rate		3 °C/sec. Max
Reflow	-Temperature(T_L)(Liquid us)	+217 °C
	-Temperature(t_L)	60-150 secs.
Peak Temp (T_P)		+260(+0/-5) °C
Time within 5 °C of actual Peak Temp (t_p)		30 secs. Max
Ramp-down Rate		6 °C/sec. Max
xTime 25 °C to Peak Temp (T_P)		8 min. Max
Do not exceed		+260 °C

SOD-523 PACKAGE OUTLINE DRAWING

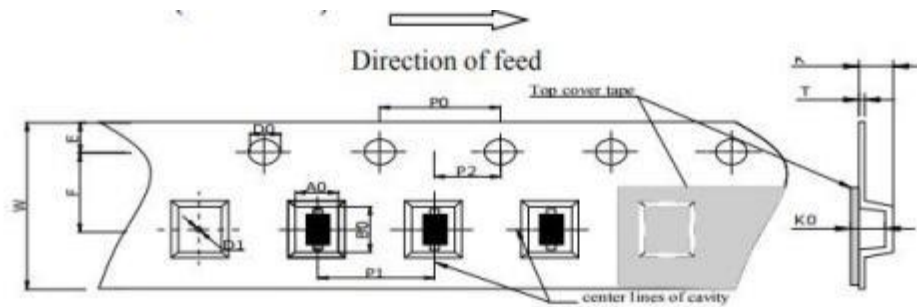


SOD-523



Dim	Millimeters		Inches	
	Min	Max	Min	Max
A	1.10	1.30	0.043	0.051
B	0.70	0.90	0.045	0.053
C	0.50	0.70	0.031	0.043
D	0.25	0.35	0.004	0.012
J	0.07	0.20	0.0028	0.0079
K	0.15	0.25	0.006	0.010
S	1.50	1.70	0.059	0.067

Product Orientation (continued)



Package	Chip Size	Pocket Size B0×A0×K0(mm)	Tape Width	Reel Diameter	Quantity Per Reel	P0	P1
SOD-523	1.60×0.8×0.6	1.75×0.95×0.75	8mm	178mm(7")	3000	4mm	4mm
D0	D1	E	F	K	T	W	
1.5mm	0.5mm	1.75mm	3.5mm	0.70mm	0.2mm	8mm	

The LEADER is a minimum of 100 components in length and it consists of empty cavities with sealed cover tape. The TRAILER is a minimum of 100 components in length and it consists of empty cavities with sealed cover tape.