IDO-SOM1126B-S1 核心板规格书

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核心板规格书

深圳触觉智能科技有限公司

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文档修订历史

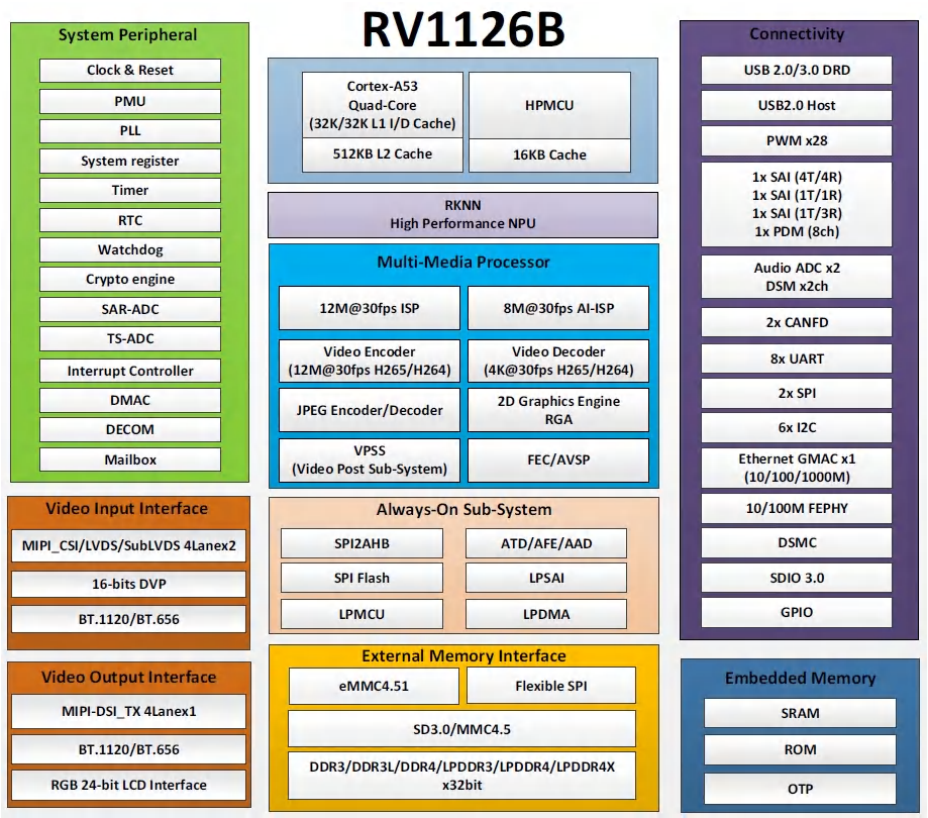
版本	PCBA 版本	修订内容	修订	审核	日期
V1.0	S1A		GZH	IDO	2025/10/29
V1.0	S1A	增加核心板重量	LYT	IDO	2026/07/08

1 产品介绍

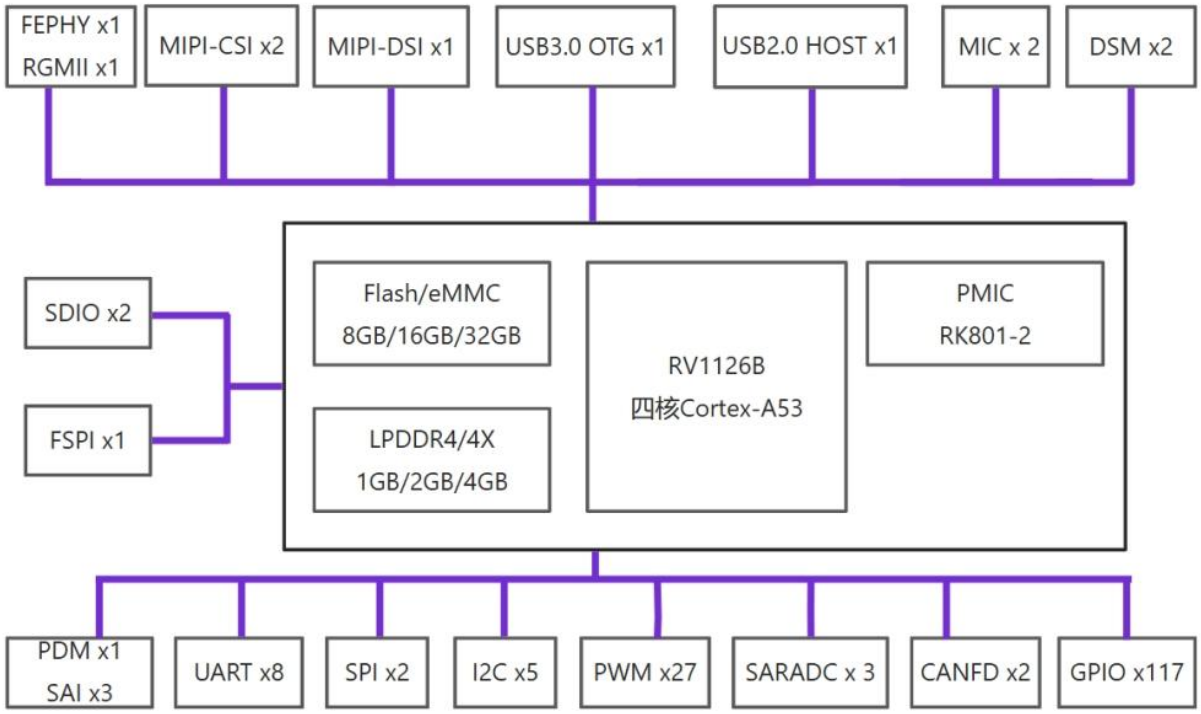
1.1 产品概述

IDO-SOM1126B-S1 是一款基于瑞芯微高性能 AIOT 平台 RV1126B 设计的核心板，在 40x40mm 的小体积上集成了 RV1126B SoC, PMIC，LPDDR4(X) ， eMMC。

RV1126B 是一款用于机器视觉应用的高性能视觉处理系统芯片，集成了 4 个 Cortex-A53 及独立的 NEON 协处理器，支持 4K@30fps 的 H.264/H.265 解码器，还支持 12M@30fps 的 H.264/H.265 编码器。引入了新一代完全基于硬件的最大 12M 像素 ISP（图像信号处理器），实现了多种算法加速器，如 HDR、3A、LSC、3DNR、2DNR、锐化、去雾、鱼眼校正、伽马校正、特征点检测等。同时还引入了最大 800 万像素的 AI-ISP 作为传统 ISP 的补充，提供卓越的空间降噪性能和强大的图像增强效果。 内置 3TOPS NPU，支持 INT4/8/16/FP16 混合运算，支持 TensorFlow、PyTorch、 TFLite、Caffe、ONNX 等深度学习框架 。RV1126B SoC 内部组成，如下图所示：



IDO-SOM1126B-S1 核心板进行了严格的电源完整性和信号完整性仿真设计，通过各项电磁兼容、温度冲击、高温高湿老化、长时间存储压力等测试， 稳定可靠，批量供货。用户仅需设计外围电路即可快速实现项目的稳定量产，IDO-SOM1126B-S1 模块逻辑框图，如下图所示：

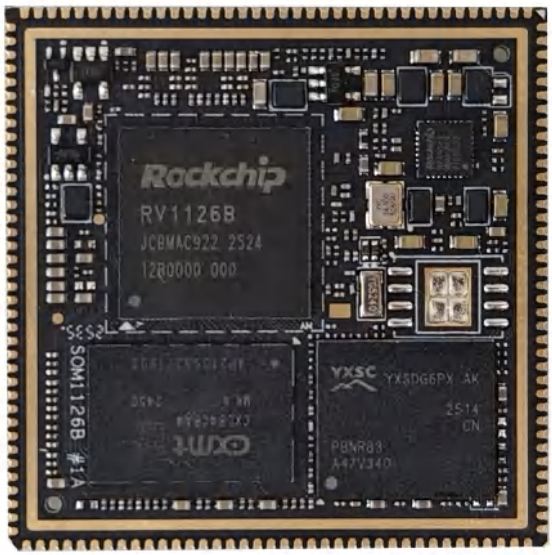


1.2 产品特点

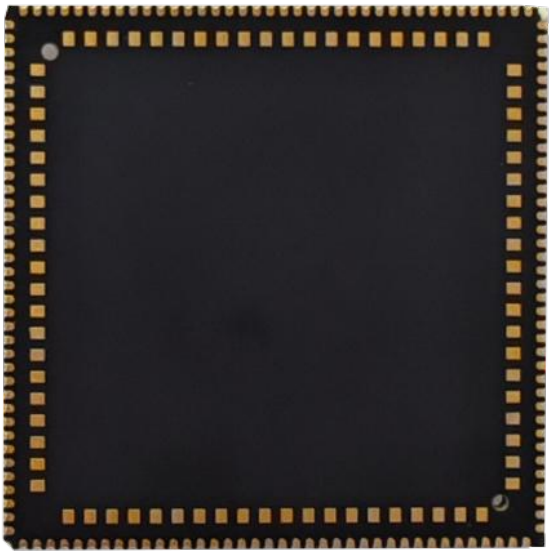
1. 处理器采用 Quad A53 CPU 14nm 制程工艺，主频高达 1.5GHz；
2. 内置 3T RKNN AI 算力；
3. 2 种屏幕显示接口：MIPI DSI、RGB；
4. 丰富的总线接口：1×GMAC、2×CAN FD、1×USB3.0、2×USB2.0、8×UART、27×PWM、3×SPI、5×I2C 等；
5. 核心板支持 100%全国产；
6. 40mm X 40mm 超小尺寸 LCC+LGA 封装 232Pin，10 层板沉金工艺。

1.3 产品图片

IDO-SOM1126B-S1 核心板正面，，如下图所示：



IDO-SOM1126B-S1 核心板背面，如下图所示：



1.4 适用场景

IDO-SOM1126B-S1 适用于人脸识别、闸机门禁、智能安防、智能网络摄像头等行业领域。

2 参数规格

2.1 基本参数

基本参数，如下表所示：

基本参数	
SOC	RockChip RK1126B
CPU	四核 64 位 ARM Cortex-A53，集成 NEON 和 FPU
NPU	3TOPS NPU，支持 INT4/INT8/INT16/FP16 混合运算
ISP	1200 万像素 ISP，800 万像素的 AI-ISP，集成了多种算法加速器，如 HDR、3A、LSC、3DNR、2DNR、锐化、去雾、鱼眼校正、伽马校正、特征点检测等
编解码	编码：12M@30fps H.264 解码：4K@30fps H.264
内存	LPDDR4/4X（1GB/2GB/4GB 可选）
存储	eMMC（8GB/16GB/32GB/64GB 可选）
硬件参数	
以太网	集成 GMAC 以太网控制器，最高支持 1 路千兆以太网（1000 Mbps）
视频输出	视频输出： 1 × MIPI_DPHY_TX(支持 V1.2 版本，总共 4Lane，支持 1920×1080@60fps)

	1 × LCDC TX(支持并行 24bit RGB 模式 1920×1080@60fps、16bit BT1120 模式 1920×1080@60fps、8bit BT656 模式 720×576@60fps)
视频输入	2 × MIPI CSI RX（支持 MIPI V1.2 版本；2 × 4 Lanes 或 4 × 2 Lanes）、 1 × DVP（8/10/12/16-bit，BT.601/BT.656 and BT.1120）
音频接口	3× SAI 1 × PDM 2 x 数字 DAC 3 x ADC 2 x DSM
USB	1 × USB3.0 OTG 1 × USB2.0 OTG 1 × USB2.0 HOST
扩展接口	2 × SDIO 8 × UART 2 × SPI 2 × CANFD 5 × I2C 27 × PWM 3 × SARADC

	117 × GPIO
其他	
主板尺寸	40mm × 40mm
接口类型	232Pin 间距 1mm 邮票孔
PCB 规格	板厚 1.3mm ,10 层板 高 Tg 材质，沉金工艺
重量	7.1g

2.2 工作环境

核心板工作环境，如下表所示：

工作环境	
工作温度	-20℃~+80℃
工作湿度	5%~90% RH 非冷凝
存储温度	-40℃～+85℃

2.3 系统支持

核心板支持系统，如下表所示：

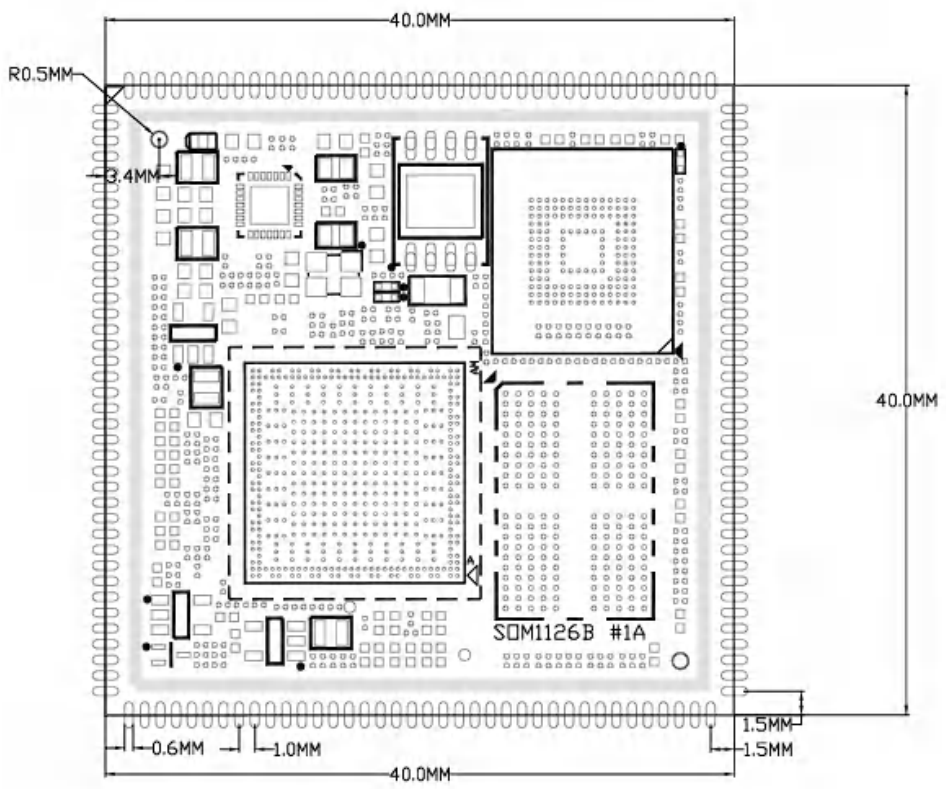
序号	操作系统	支持	说明
1	Debian	•	/

2	Buildroot	•	/

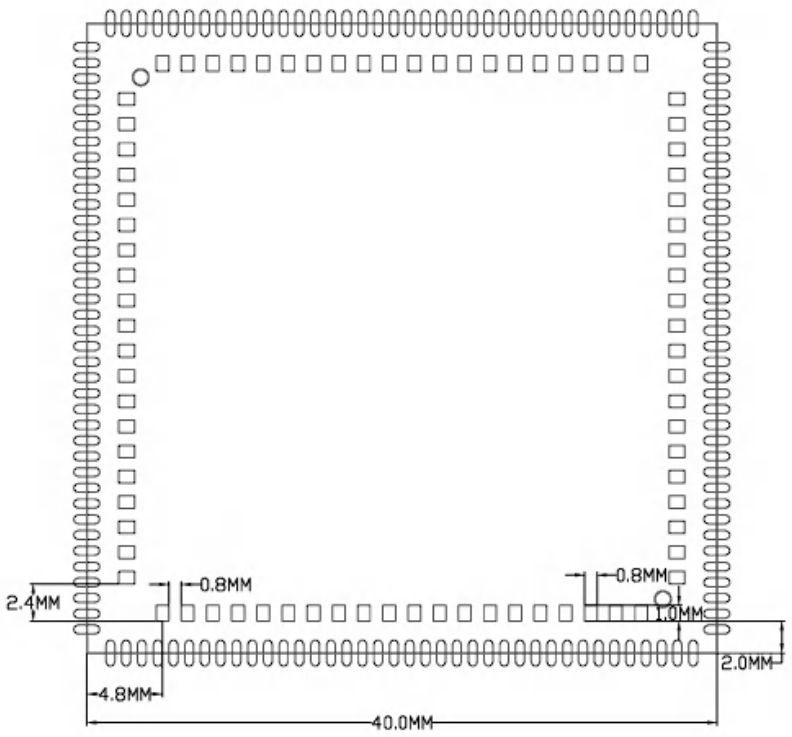
3、PCB 尺寸和电气参数

3.1 PCB 尺寸

IDO-SOM1126B-S1 核心板尺寸如下图所示：



正面



背面

3.2 电气参数

3.2.1 主电源输入

主电源输入，如下表所示：

电源名称	最小电压	标称值	最大电压	峰值电流	桌面静态电流	关机电流
VCC5V0_SYS_S5_IN	4.6V	5.0V	5.4V	339mA	163mA	＜1mA

3.2.2 IO 电源输入

电源名称	最小电压	最大电压	峰值电流	备注

VCCIO3_IN	1.75V	3.4V	<50mA	VCCIO3 电源域电压
VCCIO5_IN	1.75V	3.4V	<50mA	VCCIO5 电源域电压
VCCIO6_IN	1.75V	3.4V	<50mA	VCCIO6 电源域电压

3.2.3 电源输出

电源输出，如下表所示：

电源名称	最小电压	标称值	最大电压	限制电流
VCC_1V8_OUT_S0	1.75V	1.8V	1.85V	100mA/待机掉电
VCC_3V3_OUT_S0	3.2V	3.3V	3.4V	100mA/待机掉电
VCC_3V3_OUT_S3	3.2V	3.3V	3.4V	100mA/待机保持

4、采购型号

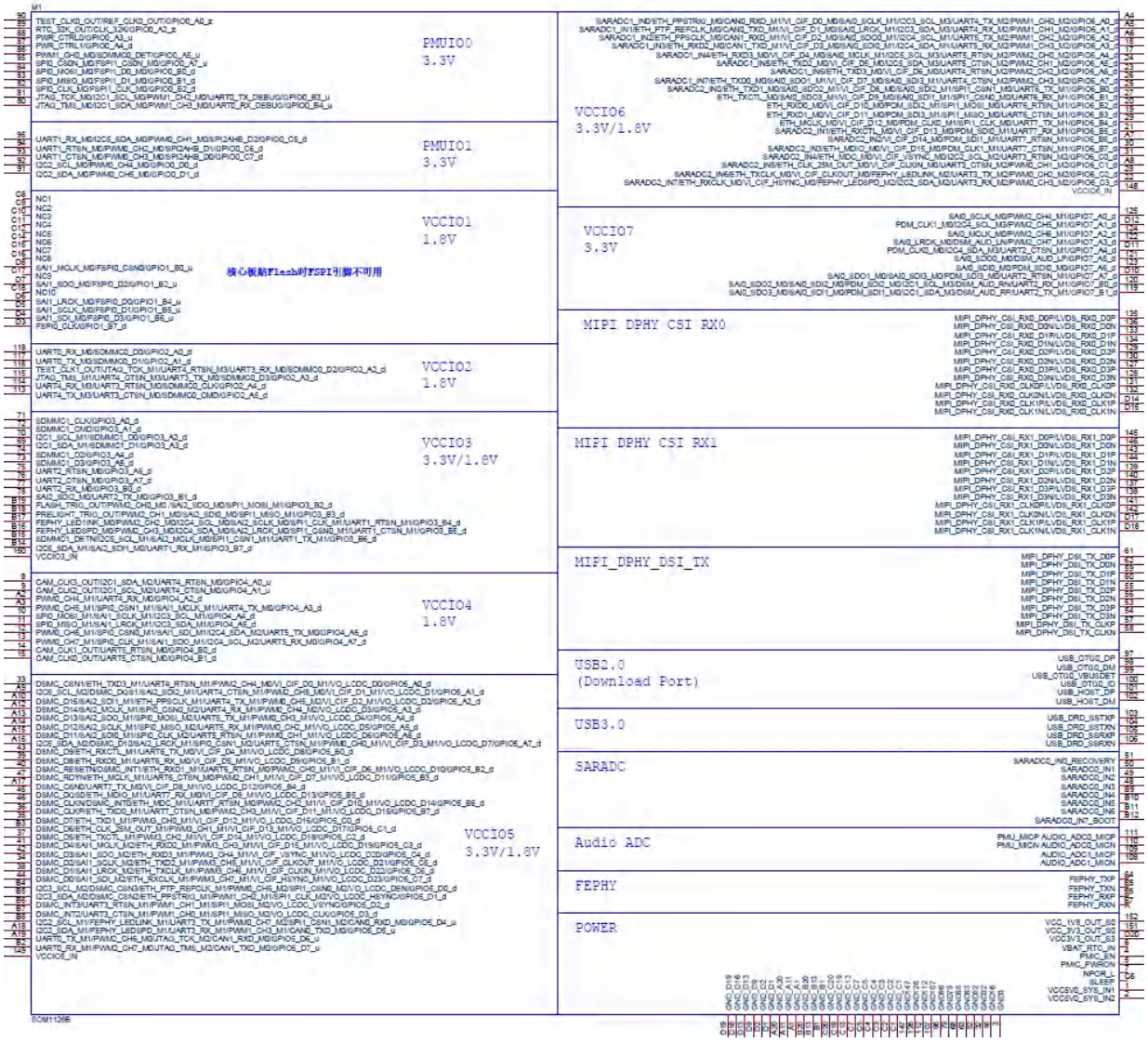
采购型号，如下表所示：

采购型号	LPDDR4/4x	eMMC	标称工作温度
IDO-SOM1126B-S1-D1E8-C	1GB	8GB	-20℃ ~ +80℃
IDO-SOM1126B-S1-D2E8-C	2GB	8GB	-20℃ ~ +80℃

IDO-SOM1126B-S1-D2E32-C	2GB	32GB	-20℃ ~ +80℃
IDO-SOM1126B-S1-D4E32-C	4GB	32GB	-20℃ ~ +80℃

5、引脚定义说明

5.1 核心板引脚示意图



5.2 核心板引脚列表

Num	Pin Name	BGA Ball	Signal Name	Mode
1	VCC5V0_SYS_IN1		VCC5V0_SYS_IN1	

2	VCC5V0_SYS_IN2		VCC5V0_SYS_IN2	
3	GND		GND	
4	PMIC_EN		PMIC_EN	
5	PMIC_PWRON		PMIC_PWRON	
6	VBAT_RTC_OUT		VBAT_RTC_OUT	
7	NPOR_L		NPOR_L	
8	CAM_CLK3_OUT / I2C1_SDA_M2 / UART4_RTSN_M0 / GPIO4_A0	AG31	CAM_CLK3_OUT	
			I2C1_SDA_M2	
			UART4_RTSN_M0	
			GPIO4_A0	
9	CAM_CLK2_OUT / I2C1_SCL_M2 / UART4_CTSN_M0 / GPIO4_A1	AG32	CAM_CLK2_OUT	
			I2C1_SCL_M2	
			UART4_CTSN_M0	
			GPIO4_A1	
10	SPI0_MOSI_M1 / SAI1_SCLK_M1 / I2C3_SCL_M1 / GPIO4_A4	1V22	SPI0_MOSI_M1	
			SAI1_SCLK_M1	
			I2C3_SCL_M1	
			GPIO4_A4	

11	SPI0_MISO_M1 / SAI1_LRCK_M1 / I2C3_SDA_M1 / GPIO4_A5	AF32	SPI0_MISO_M1	
			SAI1_LRCK_M1	
			I2C3_SDA_M1	
			GPIO4_A5	
12	PWM0_CH6_M1 / SPI0_CSN0_M1 / SAI1_SDI_M1 / I2C4_SDA_M2 / UART5_TX_M0 / GPIO4_A6	1T21	PWM0_CH6_M1	
			SPI0_CSN0_M1	
			SAI1_SDI_M1	
			I2C4_SDA_M2	
			UART5_TX_M0	
			GPIO4_A6	
13	PWM0_CH7_M1 / SPI0_CLK_M1 / SAI1_SDO_M1 / I2C4_SCL_M2 / UART5_RX_M0 / GPIO4_A7	1T22	PWM0_CH7_M1	
			SPI0_CLK_M1	
			SAI1_SDO_M1	
			I2C4_SCL_M2	
			UART5_RX_M0	
			GPIO4_A7	
14	CAM_CLK1_OUT / UART5_RTSN_M0 / GPIO4_B0	AF31	CAM_CLK1_OUT	
			UART5_RTSN_M0	

			GPIO4_B0	
15	CAM_CLKO_OUT / UART5_CTSN_M0 / GPIO4_B1	AE31	CAM_CLKO_OUT	
			UART5_CTSN_M0	
			GPIO4_B1	
16	GND		GND	
17	SARADC1_IN4 / ETH_RXD3_M0 / VI_CIF_D4_M0 / SAI0_MCLK_M1 / I2C5_SCL_M3 / UART5_RTSN_M2 / PWM2_CH0_M2 / GPIO6_A4	AB31	SARADC1_IN4	
			ETH_RXD3_M0	
			VI_CIF_D4_M0	
			SAI0_MCLK_M1	
			I2C5_SCL_M3	
			UART5_RTSN_M2	
			PWM2_CH0_M2	
			GPIO6_A4	
18	SARADC1_IN3 / ETH_RXD2_M0 / CAN1_TXD_M1 / VI_CIF_D3_M0 / SAI0_SDI0_M1 / I2C4_SDA_M1 / UART5_RX_M2 / PWM1_CH3_M2 / GPIO6_A3	AC31	SARADC1_IN3	
			ETH_RXD2_M0	
			CAN1_TXD_M1	
			VI_CIF_D3_M0	
			SAI0_SDI0_M1	

			I2C4_SDA_M1	
			UART5_RX_M2	
			PWM1_CH3_M2	
			GPIO6_A3	
19	ETH_RXD1_M0 / VI_CIF_D11_M0 / PDM_SDI3_M1 / SPI1_MISO_M0 / UART6_CTSN_M1 / GPIO6_B3	1P23	ETH_RXD1_M0	
			VI_CIF_D11_M0	
			PDM_SDI3_M1	
			SPI1_MISO_M0	
			UART6_CTSN_M1	
			GPIO6_B3	
20	ETH_RXD0_M0 / VI_CIF_D10_M0 / PDM_SDI2_M1 / SPI1_MOSI_M0 / UART6_RTSN_M1 / GPIO6_B2	1M22	ETH_RXD0_M0	
			VI_CIF_D10_M0	
			PDM_SDI2_M1	
			SPI1_MOSI_M0	
			UART6_RTSN_M1	
			GPIO6_B2	
21	SARADC2_IN1 / ETH_RXCTL_M0 / VI_CIF_D13_M0 / PDM_SDI0_M1 / UART7_RX_M1 / GPIO6_B5	1M22	SARADC2_IN1	
			ETH_RXCTL_M0	

			VI_CIF_D13_M0	
			PDM_SDI0_M1	
			UART7_RX_M1	
			GPIO6_B5	
22	SARADC2_IN7 / ETH_RXCLK_M0 / VI_CIF_HSYNC_M0 / FEPHY_LEDSPD_M2 / I2C2_SDA_M2 / UART3_RX_M2 / PWM0_CH3_M2 / GPIO6_C3	V31	SARADC2_IN7	
			ETH_RXCLK_M0	
			VI_CIF_HSYNC_M0	
			FEPHY_LEDSPD_M2	
			I2C2_SDA_M2	
			UART3_RX_M2	
			PWM0_CH3_M2	
			GPIO6_C3	
23	SARADC1_IN6 / ETH_TXD3_M0 / VI_CIF_D6_M0 / UART4_RTSN_M2 / PWM2_CH2_M2 / GPIO6_A6	AA32	SARADC1_IN6	
			ETH_TXD3_M0	
			VI_CIF_D6_M0	
			UART4_RTSN_M2	
			PWM2_CH2_M2	
			GPIO6_A6	

24	SARADC1_IN5 / ETH_TXD2_M0 / VI_CIF_D5_M0 / I2C5_SDA_M3 / UART5_CTSN_M2 / PWM2_CH1_M2 / GPIO6_A5	AA31	SARADC1_IN5	
			ETH_TXD2_M0	
			VI_CIF_D5_M0	
			I2C5_SDA_M3	
			UART5_CTSN_M2	
			PWM2_CH1_M2	
			GPIO6_A5	
25	SARADC2_IN0 / ETH_TXD1_M0 / SAI0_SDO2_M1 / VI_CIF_D8_M0 / SAI0_SDI2_M1 / SPI1_CSN1_M0 / UART6_TX_M1 / GPIO6_B0	W31	SARADC2_IN0	
			ETH_TXD1_M0	
			SAI0_SDO2_M1	
			VI_CIF_D8_M0	
			SAI0_SDI2_M1	
			SPI1_CSN1_M0	
			UART6_TX_M1	
			GPIO6_B0	
26	SARADC1_IN7 / ETH_TXD0_M0 / SAI0_SDO1_M1 / VI_CIF_D7_M0 / SAI0_SDI3_M1 / UART4_CTSN_M2 / PWM2_CH3_M2 / GPIO6_A7	Y32	SARADC1_IN7	
			ETH_TXD0_M0	
			SAI0_SDO1_M1	

			VI_CIF_D7_M0	
			SAI0_SDI3_M1	
			UART4_CTSN_M2	
			PWM2_CH3_M2	
			GPIO6_A7	
27	ETH_TXCTL_M0 / SAI0_SDO3_M1 / VI_CIF_D9_M0 / SAI0_SDI1_M1 / SPI1_CSN0_M0 / UART6_RX_M1 / GPIO6_B1	1P21	ETH_TXCTL_M0	
			SAI0_SDO3_M1	
			VI_CIF_D9_M0	
			SAI0_SDI1_M1	
			SPI1_CSN0_M0	
			UART6_RX_M1	
			GPIO6_B1	
28	SARADC2_IN6 / ETH_TXCLK_M0 / VI_CIF_CLKOUT_M0 / FEPHY_LEDLINK_M2 / UART3_TX_M2 / PWM0_CH2_M2 / GPIO6_C2	U31	SARADC2_IN6	
			ETH_TXCLK_M0	
			VI_CIF_CLKOUT_M0	
			FEPHY_LEDLINK_M2	
			UART3_TX_M2	
			PWM0_CH2_M2	

			GPIO6_C2	
29	ETH_MCLK_M0 / VI_CIF_D12_M0 / PDM_CLK0_M1 / SPI1_CLK_M0 / UART7_TX_M1 / GPIO6_B4	1M21	ETH_MCLK_M0	
			VI_CIF_D12_M0	
			PDM_CLK0_M1	
			SPI1_CLK_M0	
			UART7_TX_M1	
			GPIO6_B4	
30	SARADC2_IN3 / ETH_MDIO_M0 / VI_CIF_D15_M0 / PDM_CLK1_M1 / UART7_CTSN_M1 / GPIO6_B7	1K21	SARADC2_IN3	
			ETH_MDIO_M0	
			VI_CIF_D15_M0	
			PDM_CLK1_M1	
			UART7_CTSN_M1	
			GPIO6_B7	
31	SARADC2_IN4 / ETH_MDC_M0 / VI_CIF_VSYNC_M0 / I2C2_SCL_M2 / UART3_RTSN_M2 / PWM0_CH0_M2 / GPIO6_C0	V32	SARADC2_IN4	
			ETH_MDC_M0	
			VI_CIF_VSYNC_M0	
			I2C2_SCL_M2	
			UART3_RTSN_M2	

			PWM0_CH0_M2	
			GPIO6_C0	
32	GND		GND	
33	DSMC_CSN1 / ETH_TXD3_M1 / UART4_RTSN_M1 / PWM2_CH4_M0 / VI_CIF_D0_M1 / VO_LCDC_D0 / GPIO5_A0	R31	DSMC_CSN1	
			ETH_TXD3_M1	
			UART4_RTSN_M1	
			PWM2_CH4_M0	
			VI_CIF_D0_M1	
			VO_LCDC_D0	
			GPIO5_A0	
34	DSMC_D2 / SAI1_SCLK_M2 / ETH_TXD2_M1 / PWM3_CH5_M1 / VI_CIF_CLKOUT_M1 / VO_LCDC_D21 / GPIO5_C5	1D23	DSMC_D2	
			SAI1_SCLK_M2	
			ETH_TXD2_M1	
			PWM3_CH5_M1	
			VI_CIF_CLKOUT_M1	
			VO_LCDC_D21	
			GPIO5_C5	
35	DSMC_D7 / ETH_TXD1_M1 / PWM3_CH0_M1 / VI_CIF_D12_M1 /	J32	DSMC_D7	

	VO_LCDC_D16 / GPIO5_C0		ETH_TXD1_M1	
			PWM3_CH0_M1	
			VI_CIF_D12_M1	
			VO_LCDC_D16	
			GPIO5_C0	
36	DSMC_CLKP / ETH_TXD0_M1 / UART7_CTSN_M0 / PWM2_CH3_M1 / VI_CIF_D11_M1 / VO_LCDC_D15 / GPIO5_B7	H32	DSMC_CLKP	
			ETH_TXD0_M1	
			UART7_CTSN_M0	
			PWM2_CH3_M1	
			VI_CIF_D11_M1	
			VO_LCDC_D15	
			GPIO5_B7	
37	DSMC_D5 / ETH_TXCTL_M1 / PWM3_CH2_M1 / VI_CIF_D14_M1 / VO_LCDC_D18 / GPIO5_C2	1F23	DSMC_D5	
			ETH_TXCTL_M1	
			PWM3_CH2_M1	
			VI_CIF_D14_M1	
			VO_LCDC_D18	
			GPIO5_C2	

38	DSMC_D1 / SAI1_LRCK_M2 / ETH_TXCLK_M1 / PWM3_CH6_M1 / VI_CIF_CLKIN_M1 / VO_LCDC_D22 / GPIO5_C6	1D22	DSMC_D1	
			SAI1_LRCK_M2	
			ETH_TXCLK_M1	
			PWM3_CH6_M1	
			VI_CIF_CLKIN_M1	
			VO_LCDC_D22	
			GPIO5_C6	
39	DSMC_D8 / ETH_RXD0_M1 / UART6_RX_M0 / VI_CIF_D5_M1 / VO_LCDC_D9 / GPIO5_B1	1K23	DSMC_D8	
			ETH_RXD0_M1	
			UART6_RX_M0	
			VI_CIF_D5_M1	
			VO_LCDC_D9	
			GPIO5_B1	
40	DSMC_RESETN / DSMC_INT1 / ETH_RXD1_M1 / UART6_RTSN_M0 / PWM2_CH0_M1 / VI_CIF_D6_M1 / VO_LCDC_D10 / GPIO5_B2	1H23	DSMC_RESETN	
			DSMC_INT1	
			ETH_RXD1_M1	
			UART6_RTSN_M0	
			PWM2_CH0_M1	

			VI_CIF_D6_M1	
			VO_LCDC_D10	
			GPIO5_B2	
41	DSMC_D4 / SAI1_MCLK_M2 / ETH_RXD2_M1 / PWM3_CH3_M1 / VI_CIF_D15_M1 / VO_LCDC_D19 / GPIO5_C3	G31	DSMC_D4	
			SAI1_MCLK_M2	
			ETH_RXD2_M1	
			PWM3_CH3_M1	
			VI_CIF_D15_M1	
			VO_LCDC_D19	
			GPIO5_C3	
42	DSMC_D3 / SAI1_SDO_M2 / ETH_RXD3_M1 / PWM3_CH4_M1 / VI_CIF_VSYNC_M1 / VO_LCDC_D20 / GPIO5_C4	F31	DSMC_D3	
			SAI1_SDO_M2	
			ETH_RXD3_M1	
			PWM3_CH4_M1	
			VI_CIF_VSYNC_M1	
			VO_LCDC_D20	
			GPIO5_C4	
43	DSMC_D9 / ETH_RXCTL_M1 / UART6_TX_M0 / VI_CIF_D4_M1 /	K31	DSMC_D9	

	VO_LCDC_D8 / GPIO5_B0		ETH_RXCTL_M1	
			UART6_TX_M0	
			VI_CIF_D4_M1	
			VO_LCDC_D8	
			GPIO5_B0	
44	DSMC_D0 / SAI1_SDI_M2 / ETH_RXCLK_M1 / PWM3_CH7_M1 / VI_CIF_HSYNC_M1 / VO_LCDC_D23 / GPIO5_C7	1D21	DSMC_D0	
			SAI1_SDI_M2	
			ETH_RXCLK_M1	
			PWM3_CH7_M1	
			VI_CIF_HSYNC_M1	
			VO_LCDC_D23	
			GPIO5_C7	
45	DSMC_DQS0 / ETH_MDIO_M1 / UART7_RX_M0 / VI_CIF_D9_M1 / VO_LCDC_D13 / GPIO5_B5	1H21	DSMC_DQS0	
			ETH_MDIO_M1	
			UART7_RX_M0	
			VI_CIF_D9_M1	
			VO_LCDC_D13	
			GPIO5_B5	

46	DSMC_CLKN / DSMC_INT0 / ETH_MDC_M1 / UART7_RTSN_M0 / PWM2_CH2_M1 / VI_CIF_D10_M1 / VO_LCDC_D14 / GPIO5_B6	J31	DSMC_CLKN	
			DSMC_INT0	
			ETH_MDC_M1	
			UART7_RTSN_M0	
			PWM2_CH2_M1	
			VI_CIF_D10_M1	
			VO_LCDC_D14	
			GPIO5_B6	
47	DSMC_RDYN / ETH_MCLK_M1 / UART6_CTSN_M0 / PWM2_CH1_M1 / VI_CIF_D7_M1 / VO_LCDC_D11 / GPIO5_B3	2E13	DSMC_RDYN	
			ETH_MCLK_M1	
			UART6_CTSN_M0	
			PWM2_CH1_M1	
			VI_CIF_D7_M1	
			VO_LCDC_D11	
			GPIO5_B3	
48	SARADC0_IN3	1B18	SARADC0_IN3	
49	SARADC0_IN2	1A18	SARADC0_IN2	
50	SARADC0_IN1	1C18	SARADC0_IN1	

51	SARADC0_IN0_RECOVERY		SARADC0_IN0_RECOVERY	
52	GND		GND	
53	MIPI_DPHY_DSI_TX_D3P	C32	MIPI_DPHY_DSI_TX_D3P	
54	MIPI_DPHY_DSI_TX_D3N	B32	MIPI_DPHY_DSI_TX_D3N	
55	MIPI_DPHY_DSI_TX_D2P	A31	MIPI_DPHY_DSI_TX_D2P	
56	MIPI_DPHY_DSI_TX_D2N	B31	MIPI_DPHY_DSI_TX_D2N	
57	MIPI_DPHY_DSI_TX_CLKP	B30	MIPI_DPHY_DSI_TX_CLKP	
58	MIPI_DPHY_DSI_TX_CLKN	B29	MIPI_DPHY_DSI_TX_CLKN	
59	MIPI_DPHY_DSI_TX_D1P	A29	MIPI_DPHY_DSI_TX_D1P	
60	MIPI_DPHY_DSI_TX_D1N	A28	MIPI_DPHY_DSI_TX_D1N	
61	MIPI_DPHY_DSI_TX_D0P	B28	MIPI_DPHY_DSI_TX_D0P	
62	MIPI_DPHY_DSI_TX_D0N	B27	MIPI_DPHY_DSI_TX_D0N	
63	GND		GND	

64	FEPHY_TXP	A26	FEPHY_TXP	
65	FEPHY_TXN	A25	FEPHY_TXN	
66	FEPHY_RXP	B25	FEPHY_RXP	
67	FEPHY_RXN	B24	FEPHY_RXN	
68	GND		GND	
69	I2C1_SDA_M1 / SDMMC1_D1 / GPIO3_A3	A23	I2C1_SDA_M1	
			SDMMC1_D1	
			GPIO3_A3	
70	I2C1_SCL_M1 / SDMMC1_D0 / GPIO3_A2	A22	I2C1_SCL_M1	
			SDMMC1_D0	
			GPIO3_A2	
71	SDMMC1_CLK / GPIO3_A0	B22	SDMMC1_CLK	
			GPIO3_A0	
72	SDMMC1_CMD / GPIO3_A1	B21	SDMMC1_CMD	
			GPIO3_A1	
73	SDMMC1_D3 / GPIO3_A5	B20	SDMMC1_D3	
			GPIO3_A5	
74	SDMMC1_D2 / GPIO3_A4	A20	SDMMC1_D2	

			GPIO3_A4	
75	UART2_RTSN_M0 / GPIO3_A6	B19	UART2_RTSN_M0	
			GPIO3_A6	
76	UART2_CTSN_M0 / GPIO3_A7	B18	UART2_CTSN_M0	
			GPIO3_A7	
77	UART2_RX_M0 / GPIO3_B0	B17	UART2_RX_M0	
			GPIO3_B0	
78	UART2_TX_M0 / GPIO3_B1	A17	UART2_TX_M0	
			GPIO3_B1	
79	GND		GND	
80	JTAG_TMS_M0 / I2C1_SDA_M0 / PWM1_CH3_M0 / UART0_RX_DEBUG / GPIO0_B4	AL9	JTAG_TMS_M0	
			I2C1_SDA_M0	
			PWM1_CH3_M0	
			UART0_RX_DEBUG	
			GPIO0_B4	
81	JTAG_TCK_M0 / I2C1_SCL_M0 / PWM1_CH2_M0 / UART0_TX_DEBUG / GPIO0_B3	AM10	JTAG_TCK_M0	
			I2C1_SCL_M0	
			PWM1_CH2_M0	

			UART0_TX_DEBUG	
			GPIO0_B3	
82	SPI0_CLK_M0 / FSPI1_CLK_M0 / GPIO0_B2	1AC2	SPI0_CLK_M0	
			FSPI1_CLK_M0	
			GPIO0_B2	
83	SPI0_MISO_M0 / FSPI1_D1_M0 / GPIO0_B1	1AA4	SPI0_MISO_M0	
			FSPI1_D1_M0	
			GPIO0_B1	
84	SPI0_MOSI_M0 / FSPI1_D0_M0 / GPIO0_B0	1AB4	SPI0_MOSI_M0	
			FSPI1_D0_M0	
			GPIO0_B0	
85	SPI0_CSN0_M0 / FSPI1_CSN0_M0 / GPIO0_A7	1AC4	SPI0_CSN0_M0	
			FSPI1_CSN0_M0	
			GPIO0_A7	
86	PWM1_CH0_M0 / SDMMC0_DET / GPIO0_A5	AL10	PWM1_CH0_M0	
			SDMMC0_DET	
			GPIO0_A5	
87	PWR_CTRL1 / GPIO0_A4	1AB6	PWR_CTRL1	

			GPIO0_A4	
88	PWR_CTRL0 / GPIO0_A3	AM11	PWR_CTRL0	
			GPIO0_A3	
89	RTC_32K_OUT / CLK_32K / GPIO0_A2	AL12	RTC_32K_OUT	
			CLK_32K	
			GPIO0_A2	
90	TEST_CLK0_OUT / REF_CLK0_OUT / GPIO0_A0	AM13	TEST_CLK0_OUT	
			REF_CLK0_OUT	
			GPIO0_A0	
91	I2C2_SDA_M0 / PWM0_CH5_M0 / GPIO0_D1	1AC6	I2C2_SDA_M0	
			PWM0_CH5_M0	
			GPIO0_D1	
92	I2C2_SCL_M0 / PWM0_CH4_M0 / GPIO0_D0	1AA8	I2C2_SCL_M0	
			PWM0_CH4_M0	
			GPIO0_D0	
93	UART1_CTSN_M0 / PWM0_CH3_M0 / SPI2AHB_D0 / GPIO0_C7	1AB8	UART1_CTSN_M0	
			PWM0_CH3_M0	
			SPI2AHB_D0	

			GPIO0_C7	
94	UART1_RTSN_M0 / PWM0_CH2_M0 / SPI2AHB_D1 / GPIO0_C6	1AB10	UART1_RTSN_M0	
			PWM0_CH2_M0	
			SPI2AHB_D1	
			GPIO0_C6	
95	UART1_RX_M0 / I2C5_SDA_M0 / PWM0_CH1_M0 / GPIO0_C5	1AA10	UART1_RX_M0	
			I2C5_SDA_M0	
			PWM0_CH1_M0	
			GPIO0_C5	
96	GND		GND	
97	USB_OTG0_DP	AL2	USB_OTG0_DP	
98	USB_OTG0_DM	AM2	USB_OTG0_DM	
99	USB_OTG0_VBUSDET	1AA2	USB_OTG0_VBUSDET	
100	USB_OTG0_ID	1AB2	USB_OTG0_ID	
101	USB_HOST_DP	AL4	USB_HOST_DP	
102	USB_HOST_DM	AL3	USB_HOST_DM	
103	USB_DRD_SSTXP	AM4	USB_DRD_SSTXP	
104	USB_DRD_SSTXN	AM5	USB_DRD_SSTXN	

105	USB_DRD_SSRXP	AL6	USB_DRD_SSRXP	
106	USB_DRD_SSRXN	AL7	USB_DRD_SSRXN	
107	GND		GND	
108	AUDIO_ADC1_MICN	AL18	AUDIO_ADC1_MICN	
109	AUDIO_ADC1_MICP	AL17	AUDIO_ADC1_MICP	
110	PMU_MICN AUDIO_ADC0_MICN	AM17	PMU_MICN AUDIO_ADC0_MICN	
111	PMU_MICP AUDIO_ADC0_MICP	AM16	PMU_MICP AUDIO_ADC0_MICP	
112	GND		GND	
113	UART4_TX_M3 /UART3_CTSN_M0/SDMMC0_CMD/ GPIO2_A5	1AC18	UART4_TX_M3 GPIO2_A5	
114	UART4_RX_M3 / /UART3_RTSN_M0/SDMMC0_CLK/GPIO2_A4	1AC20	UART4_RX_M3 GPIO2_A4	
115	JTAG_TMS_M1/UART4_CTSN_M3/UART3_TX_M0/SDMMC0_D3/GPIO2_A3_d	1AA18	UART3_TX_M0 GPIO2_A3	
116	TEST_CLK1_OUT/JTAG_TCK_M1/UART4_RTSN_M3/UART3_RX_M0/SDMMC0_D2/ GPIO2_A2_d	1AB18	UART3_RX_M0 GPIO2_A2	
117	I2C0_SCL_M1/UART0_TX_M0/SDMMC0_D1/GPIO2_A1_d	1AA20	UART0_TX_M0	

			GPIO2_A1	
118	I2C0_SDA_M1/UART0_RX_M0/SDMMC0_D0/GPIO2_A0_d	1AB20	UART0_RX_M0	
			GPIO2_A0	
119	SAI0_SDO3_M0/SAI0_SDI1_M0/PDM_SDI1_M0/I2C1_SDA_M3/DSM_AUD_RP/UART2_TX_M1/GPIO7_B1_d	1AC14	SAI0_SDO3_M0	
			UART2_TX_M1	
			GPIO7_B1	
120	SAI0_SDO2_M0/SAI0_SDI2_M0/PDM_SDI2_M0/I2C1_SCL_M3/DSM_AUD_RN/UART2_RX_M1/GPIO7_B0_d	1AB14	SAI0_SDO2_M0	
			UART2_RX_M1	
			GPIO7_B0	
121	SAI0_SDO0_M0 / DSM_AUD_LP / GPIO7_A5	1AC22	SAI0_SDO0_M0	
			DSM_AUD_LP	
			GPIO7_A5	
122	SAI0_LRCK_M0 / DSM_AUD_LN / GPIO7_A3	1AB22	SAI0_LRCK_M0	
			DSM_AUD_LN	
			GPIO7_A3	
123	SAI0_SDI0_M0 / PDM_SDI0_M0 / GPIO7_A6	1AB16	SAI0_SDI0_M0	
			PDM_SDI0_M0	
			GPIO7_A6	

124	SAI0_MCLK_M0 / GPIO7_A2	1AA16	SAI0_MCLK_M0	
			GPIO7_A2	
125	SAI0_SCLK_M0 / GPIO7_A0	1Y21	SAI0_SCLK_M0	
			GPIO7_A0	
126	GND		GND	
127	MIPI_DPHY_CSI_RX0_D3P	AL20	MIPI_DPHY_CSI_RX0_D3P	
128	MIPI_DPHY_CSI_RX0_D3N	AL21	MIPI_DPHY_CSI_RX0_D3N	
129	MIPI_DPHY_CSI_RX0_D2P	AM19	MIPI_DPHY_CSI_RX0_D2P	
130	MIPI_DPHY_CSI_RX0_D2N	AM20	MIPI_DPHY_CSI_RX0_D2N	
131	MIPI_DPHY_CSI_RX0_CLK0P	AM23	MIPI_DPHY_CSI_RX0_CLK0P	
132	MIPI_DPHY_CSI_RX0_CLK0N	AL23	MIPI_DPHY_CSI_RX0_CLK0N	
133	MIPI_DPHY_CSI_RX0_D1P	AL24	MIPI_DPHY_CSI_RX0_D1P	
134	MIPI_DPHY_CSI_RX0_D1N	AL25	MIPI_DPHY_CSI_RX0_D1N	
135	MIPI_DPHY_CSI_RX0_D0P	AM25	MIPI_DPHY_CSI_RX0_D0P	

136	MIPI_DPHY_CSI_RX0_D0N	AM26	MIPI_DPHY_CSI_RX0_D0N	
137	MIPI_DPHY_CSI_RX1_D3P	AM28	MIPI_DPHY_CSI_RX1_D3P	
138	MIPI_DPHY_CSI_RX1_D3N	AM29	MIPI_DPHY_CSI_RX1_D3N	
139	MIPI_DPHY_CSI_RX1_D2P	AL27	MIPI_DPHY_CSI_RX1_D2P	
140	MIPI_DPHY_CSI_RX1_D2N	AL28	MIPI_DPHY_CSI_RX1_D2N	
141	MIPI_DPHY_CSI_RX1_CLK0P	AL31	MIPI_DPHY_CSI_RX1_CLK0P	
142	MIPI_DPHY_CSI_RX1_CLK0N	AM31	MIPI_DPHY_CSI_RX1_CLK0N	
143	MIPI_DPHY_CSI_RX1_D1P	AL32	MIPI_DPHY_CSI_RX1_D1P	
144	MIPI_DPHY_CSI_RX1_D1N	AK32	MIPI_DPHY_CSI_RX1_D1N	
145	MIPI_DPHY_CSI_RX1_D0P	AJ32	MIPI_DPHY_CSI_RX1_D0P	
146	MIPI_DPHY_CSI_RX1_D0N	AJ31	MIPI_DPHY_CSI_RX1_D0N	
147	GND		GND	
148	VCCIO6_IN	2G12	VCCIO6_IN	

149	VCCIO5_IN	2E12/2F12	VCCIO5_IN	
150	VCCIO3_IN	2B8	VCCIO3_IN	
151	VCC_3V3_OUT_S0		VCC_3V3_OUT_S0	
152	VCC_1V8_OUT		VCC_1V8_OUT	
A1	GND		GND	
A2	PWM0_CH4_M1 / UART4_RX_M0 / GPIO4_A2	1Y23	PWM0_CH4_M1	
			UART4_RX_M0	
			GPIO4_A2	
A3	PWM0_CH5_M1 / SPI0_CSN1_M1 / SAI1_MCLK_M1 / UART4_TX_M0 / GPIO4_A3	1V21	PWM0_CH5_M1	
			SPI0_CSN1_M1	
			SAI1_MCLK_M1	
			UART4_TX_M0	
			GPIO4_A3	
A4	SARADC1_IN0 / ETH_PPSTRIG_M0 / CAN0_RXD_M1 / VI_CIF_D0_M0 / SAI0_SCLK_M1 / I2C3_SCL_M3 / UART4_TX_M2 / PWM1_CH0_M2 / GPIO6_A0	1T23	SARADC1_IN0	
			ETH_PPSTRIG_M0	
			CAN0_RXD_M1	
			VI_CIF_D0_M0	

			SAI0_SCLK_M1	
			I2C3_SCL_M3	
			UART4_TX_M2	
			PWM1_CH0_M2	
			GPIO6_A0	
A5	SARADC1_IN1 / ETH_PTP_REFCLK_M0 / CAN0_TXD_M1 / VI_CIF_D1_M0 / SAI0_LRCK_M1 / I2C3_SDA_M3 / UART4_RX_M2 / PWM1_CH1_M2 / GPIO6_A1	AD32	SARADC1_IN1	
			ETH_PTP_REFCLK_M0	
			CAN0_TXD_M1	
			VI_CIF_D1_M0	
			SAI0_LRCK_M1	
			I2C3_SDA_M3	
			UART4_RX_M2	
			PWM1_CH1_M2	
			GPIO6_A1	
A6	SARADC1_IN2 / ETH_PPSCLK_M0 / CAN1_RXD_M1 / VI_CIF_D2_M0 / SAI0_SDO0_M1 / I2C4_SCL_M1 / UART5_TX_M2 / PWM1_CH2_M2 / GPIO6_A2	AC32	SARADC1_IN2	
			ETH_PPSCLK_M0	
			CAN1_RXD_M1	
			VI_CIF_D2_M0	

			SAI0_SDO0_M1	
			I2C4_SCL_M1	
			UART5_TX_M2	
			PWM1_CH2_M2	
			GPIO6_A2	
A7	SARADC2_IN2 / VI_CIF_D14_M0 / PDM_SDI1_M1 / UART7_RTSN_M1 / GPIO6_B6	1M23	SARADC2_IN2	
			VI_CIF_D14_M0	
			PDM_SDI1_M1	
			UART7_RTSN_M1	
			GPIO6_B6	
A8	SARADC2_IN5 / ETH_CLK_25M_OUT_M0 / VI_CIF_CLKIN_M0 / UART3_CTSN_M2 / PWM0_CH1_M2 / GPIO6_C1	U32	SARADC2_IN5	
			ETH_CLK_25M_OUT_M0	
			VI_CIF_CLKIN_M0	
			UART3_CTSN_M2	
			PWM0_CH1_M2	
			GPIO6_C1	
A9	I2C5_SCL_M2 / DSMC_DQS1 / SAI2_SDI2_M1 / UART4_CTSN_M1 /	R32	I2C5_SCL_M2	

	PWM2_CH5_M0 / VI_CIF_D1_M1 / VO_LCDC_D1 / GPIO5_A1		DSMC_DQS1	
			SAI2_SDI2_M1	
			UART4_CTSN_M1	
			PWM2_CH5_M0	
			VI_CIF_D1_M1	
			VO_LCDC_D1	
			GPIO5_A1	
A10	DSMC_D15 / SAI2_SDI1_M1 / ETH_PPSCLK_M1 / UART4_TX_M1 / PWM0_CH5_M2 / VI_CIF_D2_M1 / VO_LCDC_D2 / GPIO5_A2	P32	DSMC_D15	
			SAI2_SDI1_M1	
			ETH_PPSCLK_M1	
			UART4_TX_M1	
			PWM0_CH5_M2	
			VI_CIF_D2_M1	
			VO_LCDC_D2	
			GPIO5_A2	
A11	GND		GND	
A12	DSMC_D14 / SAI2_MCLK_M1 / SPI0_CSN0_M2 / UART4_RX_M1 / PWM0_CH4_M2 / VO_LCDC_D3 / GPIO5_A3	P31	DSMC_D14	
			SAI2_MCLK_M1	

			SPI0_CSN0_M2	
			UART4_RX_M1	
			PWM0_CH4_M2	
			VO_LCDC_D3	
			GPIO5_A3	
A13	DSMC_D13 / SAI2_SDO_M1 / SPI0_MOSI_M2 / UART5_TX_M1 / PWM0_CH3_M1 / VO_LCDC_D4 / GPIO5_A4	N31	DSMC_D13	
			SAI2_SDO_M1	
			SPI0_MOSI_M2	
			UART5_TX_M1	
			PWM0_CH3_M1	
			VO_LCDC_D4	
			GPIO5_A4	
A14	DSMC_D12 / SAI2_SCLK_M1 / SPI0_MISO_M2 / UART5_RX_M1 / PWM0_CH2_M1 / VO_LCDC_D5 / GPIO5_A5	M32	DSMC_D12	
			SAI2_SCLK_M1	
			SPI0_MISO_M2	
			UART5_RX_M1	
			PWM0_CH2_M1	
			VO_LCDC_D5	

			GPIO5_A5	
A15	DSMC_D11 / SAI2_SDIO_M1 / SPIO_CLK_M2 / UART5_RTSN_M1 / PWMO_CH1_M1 / VO_LCDC_D6 / GPIO5_A6	L32	DSMC_D11	
			SAI2_SDIO_M1	
			SPIO_CLK_M2	
			UART5_RTSN_M1	
			PWMO_CH1_M1	
			VO_LCDC_D6	
			GPIO5_A6	
A16	I2C5_SDA_M2 / DSMC_D10 / SAI2_LRCK_M1 / SPIO_CSN1_M2 / UART5_CTSN_M1 / PWMO_CHO_M1 / VI_CIF_D3_M1 / VO_LCDC_D7 / GPIO5_A7	L31	I2C5_SDA_M2	
			DSMC_D10	
			SAI2_LRCK_M1	
			SPIO_CSN1_M2	
			UART5_CTSN_M1	
			PWMO_CHO_M1	
			VI_CIF_D3_M1	
			VO_LCDC_D7	
			GPIO5_A7	
A17	DSMC_CSNO / UART7_TX_MO / VI_CIF_D8_M1 / VO_LCDC_D12 /	1H22	DSMC_CSNO	

	GPIO5_B4		UART7_TX_M0	
			VI_CIF_D8_M1	
			VO_LCDC_D12	
			GPIO5_B4	
A18	I2C2_SDA_M1 / FEPHY_LEDSPD_M1 / UART3_RX_M1 / PWM1_CH3_M1 / CAN0_TXD_M0 / GPIO5_D5	1C22	I2C2_SDA_M1	
			FEPHY_LEDSPD_M1	
			UART3_RX_M1	
			PWM1_CH3_M1	
			CAN0_TXD_M0	
			GPIO5_D5	
A19	UART0_TX_M1 / PWM2_CH6_M0 / JTAG_TCK_M2 / CAN1_RXD_M0 / GPIO5_D6	1A22	UART0_TX_M1	
			PWM2_CH6_M0	
			JTAG_TCK_M2	
			CAN1_RXD_M0	
			GPIO5_D6	
A20	GND		GND	
B1	GND		GND	
B2	UART0_RX_M1 / PWM2_CH7_M0 / JTAG_TMS_M2 / CAN1_TXD_M0 /	1B22	UART0_RX_M1	

	GPIO5_D7		PWM2_CH7_M0	
			JTAG_TMS_M2	
			CAN1_TXD_M0	
			GPIO5_D7	
B3	DSMC_D6 / ETH_CLK_25M_OUT_M1 / PWM3_CH1_M1 / VI_CIF_D13_M1 / VO_LCDC_D17 / GPIO5_C1	1F22	DSMC_D6	
			ETH_CLK_25M_OUT_M1	
			PWM3_CH1_M1	
			VI_CIF_D13_M1	
			VO_LCDC_D17	
			GPIO5_C1	
B4	I2C3_SCL_M2 / DSMC_CSN3 / ETH_PTP_REFCLK_M1 / PWM0_CH6_M2 / SPI1_CSN0_M2 / VO_LCDC_DEN / GPIO5_D0	F32	I2C3_SCL_M2	
			DSMC_CSN3	
			ETH_PTP_REFCLK_M1	
			PWM0_CH6_M2	
			SPI1_CSN0_M2	
			VO_LCDC_DEN	
			GPIO5_D0	

B5	I2C3_SDA_M2 / DSMC_CSN2 / ETH_PPSTRIG_M1 / PWM1_CH2_M1 / SPI1_CLK_M2 / VO_LCDC_HSYNC / GPIO5_D1	E31	I2C3_SDA_M2	
			DSMC_CSN2	
			ETH_PPSTRIG_M1	
			PWM1_CH2_M1	
			SPI1_CLK_M2	
			VO_LCDC_HSYNC	
			GPIO5_D1	
B6	DSMC_INT3 / UART3_RTSN_M1 / PWM1_CH1_M1 / SPI1_MOSI_M2 / VO_LCDC_VSYNC / GPIO5_D2	E32	DSMC_INT3	
			UART3_RTSN_M1	
			PWM1_CH1_M1	
			SPI1_MOSI_M2	
			VO_LCDC_VSYNC	
			GPIO5_D2	
B7	DSMC_INT2 / UART3_CTSN_M1 / PWM1_CH0_M1 / SPI1_MISO_M2 / VO_LCDC_CLK / GPIO5_D3	D31	DSMC_INT2	
			UART3_CTSN_M1	
			PWM1_CH0_M1	
			SPI1_MISO_M2	
			VO_LCDC_CLK	

			GPIO5_D3	
B8	I2C2_SCL_M1 / FEPHY_LEDLINK_M1 / UART3_TX_M1 / PWM0_CH7_M2 / SPI1_CSN1_M2 / CAN0_RXD_M0 / GPIO5_D4	2D13	I2C2_SCL_M1	
			FEPHY_LEDLINK_M1	
			UART3_TX_M1	
			PWM0_CH7_M2	
			SPI1_CSN1_M2	
			CAN0_RXD_M0	
			GPIO5_D4	
B9	SARADC0_IN4	2B11	SARADC0_IN4	
B10	SARADC0_IN5	1C16	SARADC0_IN5	
B11	SARADC0_IN6	2A10	SARADC0_IN6	
B12	SARADC0_IN7_BOOT	1A20	SARADC0_IN7_BOOT	
B13	GND		GND	
B14	I2C5_SDA_M1 / SAI2_SDI1_M0 / UART1_RX_M1 / GPIO3_B7	A16	I2C5_SDA_M1	
			SAI2_SDI1_M0	
			UART1_RX_M1	
			GPIO3_B7	
B15	SDMMC1_DETN / I2C5_SCL_M1 / SAI2_MCLK_M0 / SPI1_CSN1_M1 /	B16	SDMMC1_DETN	

	UART1_TX_M1 / GPIO3_B6		I2C5_SCL_M1	
			SAI2_MCLK_M0	
			SPI1_CSN1_M1	
			UART1_TX_M1	
			GPIO3_B6	
B16	FEPHY_LEDSPD_M0 / PWM2_CH3_M0 / I2C4_SDA_M0 / SAI2_LRCK_M0 / SPI1_CSN0_M1 / UART1_CTSN_M1 / GPIO3_B5	1A16	FEPHY_LEDSPD_M0	
			PWM2_CH3_M0	
			I2C4_SDA_M0	
			SAI2_LRCK_M0	
			SPI1_CSN0_M1	
			UART1_CTSN_M1	
			GPIO3_B5	
B17	FEPHY_LED1INK_M0 / PWM2_CH2_M0 / I2C4_SCL_M0 / SAI2_SCLK_M0 / SPI1_CLK_M1 / UART1_RTSN_M1 / GPIO3_B4	1A14	FEPHY_LED1INK_M0	
			PWM2_CH2_M0	
			I2C4_SCL_M0	
			SAI2_SCLK_M0	
			SPI1_CLK_M1	
			UART1_RTSN_M1	

			GPIO3_B4	
B18	PRELIGHT_TRIG_OUT / PWM2_CH1_M0 / SAI2_SDI0_M0 / SPI1_MISO_M1 / GPIO3_B3	1B14	PRELIGHT_TRIG_OUT	
			PWM2_CH1_M0	
			SAI2_SDI0_M0	
			SPI1_MISO_M1	
			GPIO3_B3	
B19	FLASH_TRIG_OUT / PWM2_CH0_M0 / SAI2_SDO_M0 / SPI1_MOSI_M1 / GPIO3_B2	1B16	FLASH_TRIG_OUT	
			PWM2_CH0_M0	
			SAI2_SDO_M0	
			SPI1_MOSI_M1	
			GPIO3_B2	
B20	GND		GND	
C1	GND		GND	
C2	GND		GND	
C3	GND		GND	
C4	GND		GND	
C5	GND		GND	
C6	I2C3_SDA_M0 / PWM0_CH6_M0 / PWR_CTRL2 / GPIO0_C1	1AC10	I2C3_SDA_M0	

			PWM0_CH6_M0	
			PWR_CTRL2	
			GPIO0_C1	
C8	NC1		NC1	
C9	NC2		NC2	
C10	NC3		NC3	
C11	NC4		NC4	
C12	NC5		NC5	
C13	GND		GND	
C14	NC6		NC6	
C15	NC7		NC7	
C16	NC8		NC8	
C17	NC9		NC9	
C18	NC10		NC10	
C19	GND		GND	
C20	GND		GND	
D1	GND		GND	
D2	GND		GND	

D3	FSPI0_CLK / GPIO1_B7	AG2	FSPI0_CLK	
			GPIO1_B7	
D4	SAI1_SDI_M0 / FSPI0_D3 / GPIO1_B6	1T1	SAI1_SDI_M0	
			FSPI0_D3	
			GPIO1_B6	
D5	SAI1_SCLK_M0 / FSPI0_D1 / GPIO1_B5	1P3	SAI1_SCLK_M0	
			FSPI0_D1	
			GPIO1_B5	
D6	SAI1_LRCK_M0 / FSPI0_D0 / GPIO1_B4	1T2	SAI1_LRCK_M0	
			FSPI0_D0	
			GPIO1_B4	
D7	SAI1_SDO_M0 / FSPI0_D2 / GPIO1_B2	1T3	SAI1_SDO_M0	
			FSPI0_D2	
			GPIO1_B2	
D8	SAI1_MCLK_M0 / FSPI0_CSN0 / GPIO1_B0	1V1	SAI1_MCLK_M0	
			FSPI0_CSN0	
			GPIO1_B0	
D9	GND		GND	

D10	SAI0_SDO1_M0 / SAI0_SDI3_M0 / PDM_SDI3_M0 / UART2_RTSN_M1 / GPIO7_A7	1AA14	SAI0_SDO1_M0	
			SAI0_SDI3_M0	
			PDM_SDI3_M0	
			UART2_RTSN_M1	
			GPIO7_A7	
D11	PDM_CLK0_M0 / I2C4_SDA_M3 / UART2_CTSN_M1 / GPIO7_A4	1AC16	PDM_CLK0_M0	
			I2C4_SDA_M3	
			UART2_CTSN_M1	
			GPIO7_A4	
D12	PDM_CLK1_M0 / I2C4_SCL_M3 / PWM2_CH5_M1 / GPIO7_A1	1AA22	PDM_CLK1_M0	
			I2C4_SCL_M3	
			PWM2_CH5_M1	
			GPIO7_A1	
D13	GND		GND	
D14	MIPI_CSI_RX0_CLK1P	AL22	MIPI_CSI_RX0_CLK1P	
D15	MIPI_CSI_RX0_CLK1N	AM22	MIPI_CSI_RX0_CLK1N	
D16	GND		GND	

D17	MIPI_CSI_RX1_CLK1P	AL29	MIPI_CSI_RX1_CLK1P	
D18	MIPI_CSI_RX1_CLK1N	AL30	MIPI_CSI_RX1_CLK1N	
D19	GND		GND	
D20	VCC3V3_OUT_S3		VCC3V3_OUT_S3	

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