

# IDO-SOM7609-S1 核心板规格书

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# IDO-SOM7609-S1

## 核心板规格书

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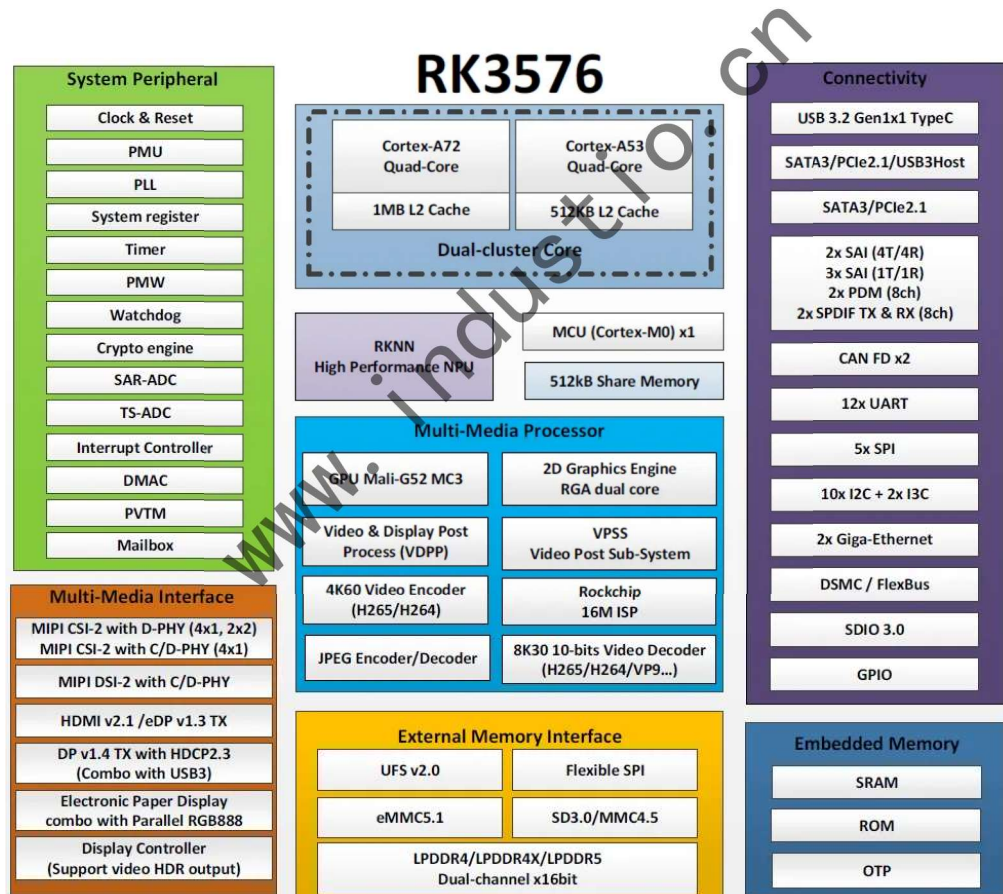
### 文档修订历史

| 版本   | PCBA版本 | 修订内容 | 修订  | 审核  | 日期         |
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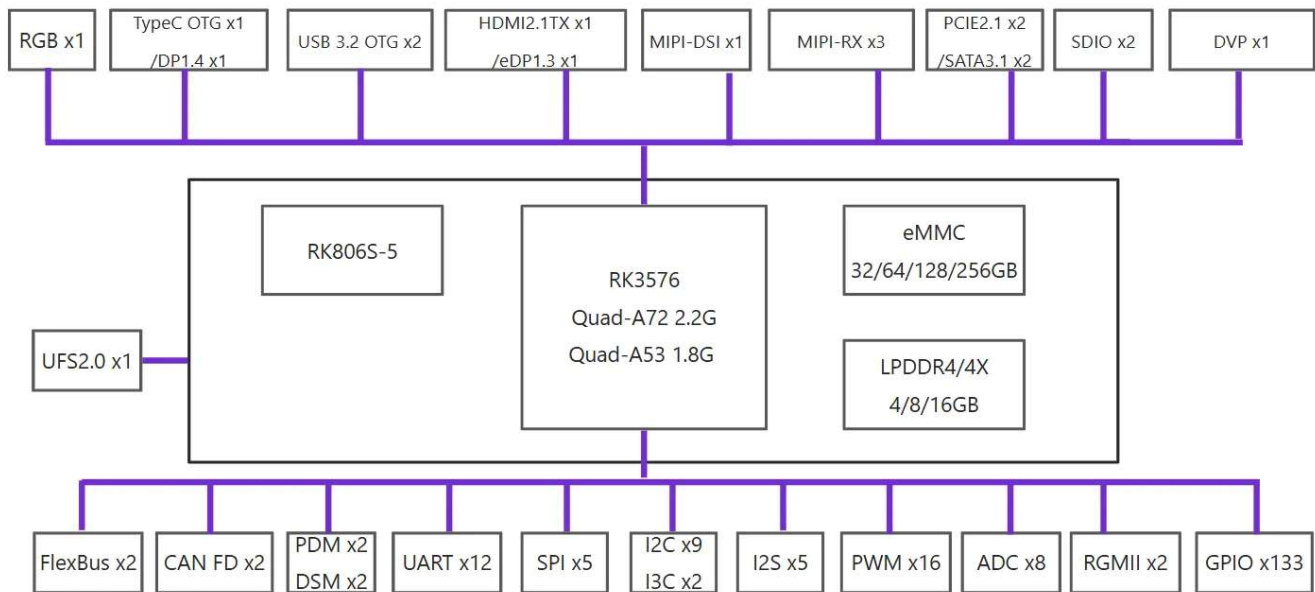
### 1、产品概述

IDO-SOM7609-S1是一款基于瑞芯微第二代8nm高性能AIOT平台RK3576设计的核心板，在40.5x40.5mm的小体积上集成了RK3576 SoC, PMIC, LPDDR4(X), eMMC。

RK3576 是一颗高性能低功耗处理器芯片，集成了 4 个 Cortex-A72 和 4 个 Cortex-A53 及独立的NEON 协处理器；适用于 ARM PC、边缘计算、个人移动互联网设备及其它多媒体产品。RK3576 内置了多种功能强大的嵌入式硬件引擎，为高端应用提供了优异的性能，支持 4K@120fps 的H.265、VP9、AVS2 和 AV1 解码器，支持 4k@60fps 的 H.264 解码器；还支持 4K@60fps 的 H.264 和 H.265编码器，高质量的 JPEG 编码器/解码器，专门的图像预处理器和后处理器。内置 3D GPU，能够完全兼容 OpenGL ES1.1/2.0/3.2、OpenCL 2.0 和 Vulkan 1.1。带有 MMU 的特殊 2D硬件引擎将最大限度地提高显示性能，并提供流畅的操作体验。引入了新一代完全基于硬件的最大 16M 像素 ISP（图像信号处理器），实现了多种算法加速器，如HDR、3A、CAC、3DNR、2DNR、锐化、去雾、增强、鱼眼校正、伽马校正等。内嵌的 NPU 支持 INT4/INT8/INT16/FP16/BF16/TF32 混合运算。此外，凭借其强大的兼容性，可以轻松转换基于 TensorFlow/MXNet/PyTorch/Caffe 等一系列框架的网络模型。



IDO-SOM7609-S1核心板进行了严格的电源完整性和信号完整性仿真设计，通过各项电磁兼容、温度冲击、高温高湿老化、长时间存储压力等测试，稳定可靠，批量供货。用户仅需设计外围电路即可快速实现项目的稳定量产，IDO-SOM7609-S1模块逻辑框图，如下图所示：



## 1.1 产品特点

1. 处理器采用Quad A72 + Quad A53 CPU 8nm先进制程工艺，主频高达2.2GHz；
2. 内置6T RKNN AI 算力；
3. 5种屏幕显示接口：HDMI、eDP、MIPI DSI、DP、RGB；
4. 丰富的总线接口：2×GMAC、2×CAN FD、PCIE/USB3.2、2×USB2.0、12×UART、16×PWM、5×SPI、9×I2C等；
5. 核心板支持100%全国产；
6. 4.05cm X 4.05cm超小尺寸LCC+LGA封装300Pin，10层板沉金工艺。

## 1.2 产品图片

IDO-SOM7609-S1核心板正面，如下图所示：



IDO-SOM7609-S1核心板背面，如下图所示：



### 1.3 适用场景

IDO-SOM7609-S1采用瑞芯微第二代8nm高性能AIOT平台RK3576/RK3576J，可广泛应用在边缘计算、大模型本地化、智慧商显、云终端产品、工控主机、汽车电子等行业领域。

## 2、硬件参数规格

### 2.1 基本参数

基本参数如下图所示：

| 基本参数 |                                                          |
|------|----------------------------------------------------------|
| SOC  | RockChip RK3576/RK3576J                                  |
| CPU  | Quad-core Cortex-A72 and quad-core Cortex-A53，主频高达2.2GHz |



|      |                                                                                                                                                                                                                                                                                                                                                                                                                                      |
|------|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| GPU  | <ol style="list-style-type: none"> <li>1. ARM Mali G52 MC3 GPU</li> <li>2. OpenGL ES 1.1,2.0, and 3.2</li> <li>3. Vulkan 1.1</li> <li>4. OpenCL 2.0 Full Profile</li> <li>5. 2D Graphics Engine (RGA)</li> </ol>                                                                                                                                                                                                                     |
| NPU  | 6TOPS INT8, 支持INT4/INT8/INT16/FP16/TF32混合运算                                                                                                                                                                                                                                                                                                                                                                                          |
| ISP  | <ol style="list-style-type: none"> <li>1. ISP V3.9</li> <li>2. 16M Pixel ISP with HDR (up to 120dB)</li> </ol>                                                                                                                                                                                                                                                                                                                       |
| VPU  | <p>视频解码:</p> <ol style="list-style-type: none"> <li>1. H.265/HEVC/AVS2/VP9/AV1, 8K@30fps or 4K@120fps</li> <li>2. H.264/AVC/MJPEG, 4K@60fps</li> </ol> <p>视频编码:</p> <ol style="list-style-type: none"> <li>1. H.265/H.264, 4K@60fps</li> <li>2. MJPEG, 4K@60fps</li> </ol>                                                                                                                                                           |
| 内存   | 4GB/8GB/16GB LPDDR4/4x 4266Mbps                                                                                                                                                                                                                                                                                                                                                                                                      |
| 存储   | 32GB/64GB/128GB/256GB eMMC (V5.1)                                                                                                                                                                                                                                                                                                                                                                                                    |
| 硬件参数 |                                                                                                                                                                                                                                                                                                                                                                                                                                      |
| 视频输入 | <p>2 × MIPI DPHY CSI (支持MIPI V1.2 版本; 1 × 4 Lanes 或 2 × 2 Lanes) 、<br/>1 × MIPI DCPHY CSI RX (DPHY 支持V2.0 版本支持4Lane/2Lane/1Lane模<br/>式; CPHY 支持V1.1 版本支持0/1/2 Trio模式)</p> <p>1 × DVP (8/10/12/16-bit, BT.601/BT.656 and BT.1120)</p>                                                                                                                                                                                                 |
| 视频输出 | <p>1 × HDMI2.1(4096×2160@120Hz)/eDP1.3(4096×2160@60Hz支持<br/>1Lane/2Lane/4Lane 模式)</p> <p>1 × DP1.4 (4096×2160@120Hz)</p> <p>1 × EBC 输出接口 (支持 E-ink EPD (Electronic Paper Display),<br/>2560×1920)</p> <p>1 × MIPI_DCPHY_TX(支持V2.0 版本支持0/1/2/3 Lane模式; C-PHY 支持V1.1<br/>版本支持0/1/2 Trio模式; 2560×1600@60Hz)</p> <p>1 × LCDC TX(支持并行24bit RGB 模式1920×1080@60Hz、16bit BT1120 模<br/>式1920×1080@60Hz、8bit BT656 模式720×576@60Hz 以及MCU模式)</p> |

|           |                                                                                                                                                                                                                                                                         |
|-----------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| 音频        | <p>2 × SAI (4T/4R)、3 × SAI (1T/1R), 支持I2S/TDM/PCM 模式, 支持高达192KHz 的采样率</p> <p>2 × SPDIF TX &amp; RX (8ch;最大支持24bits 解析度)</p> <p>2 × PDM (最高8 channels, 音频分辨率16~24 位, 采样率达192KHz, 支持PDM 主接收模式)</p> <p>2 × DSM (支持双倍数据速率接口; 支持8 线和16 线串行传输模式; DSMC_CLKP/N 最高速率为100MHz)</p> |
| UFS       | 1 × UFS2.0总线                                                                                                                                                                                                                                                            |
| USB       | <p>1 × USB3.2 Gen1 OTG0 (与DP1.4复用)</p> <p>1 × USB3.2 Gen1 OTG1 (与PCIe 2.1/SATA 3.1复用)</p>                                                                                                                                                                               |
| PCIe/SATA | <p>1 × PCIe2.1/SATA3.1 (1 lane)</p> <p>1 × PCIe2.1/SATA3.1/USB3.2 Gen1 (1 lane)</p>                                                                                                                                                                                     |
| 网络        | 2 × RGMII 支持2路千兆以太网                                                                                                                                                                                                                                                     |
| 扩展接口      | <p>2 × FlexBus数据总线</p> <p>12 × UART</p> <p>5 × SPI, 支持主从模式</p> <p>2 × CAN FD</p> <p>9 × I2C</p> <p>2 × I3C</p> <p>2 × SDIO v3.0</p> <p>16 × PWM, 支持红外输入, 时钟计数</p> <p>8 × ADC , 1MS/s , 12bits</p> <p>133 × GPIO</p>                                                     |
| 其他        |                                                                                                                                                                                                                                                                         |
| 核心板尺寸     | 40.5mm X 40.5mm X 4mm                                                                                                                                                                                                                                                   |
| 接口类型      | 300Pin LCC (邮票孔152PIN) +LGA (背面焊盘148PIN) 封装                                                                                                                                                                                                                             |
| PCB规格     | 1.5mm 10层高密板, 沉金工艺                                                                                                                                                                                                                                                      |

## 2.2 工作环境

工作环境如下表所示：



| 工作环境 |                                                     |
|------|-----------------------------------------------------|
| 工作温度 | 0℃~+70℃ [ RK3576 商业级 ]<br>-40℃~+85℃ [ RK3576J 工业级 ] |
| 存储温度 | -40℃ ~ +85℃                                         |
| 存储湿度 | 0%~90% RH (无凝露)                                     |

## 2.3 系统支持

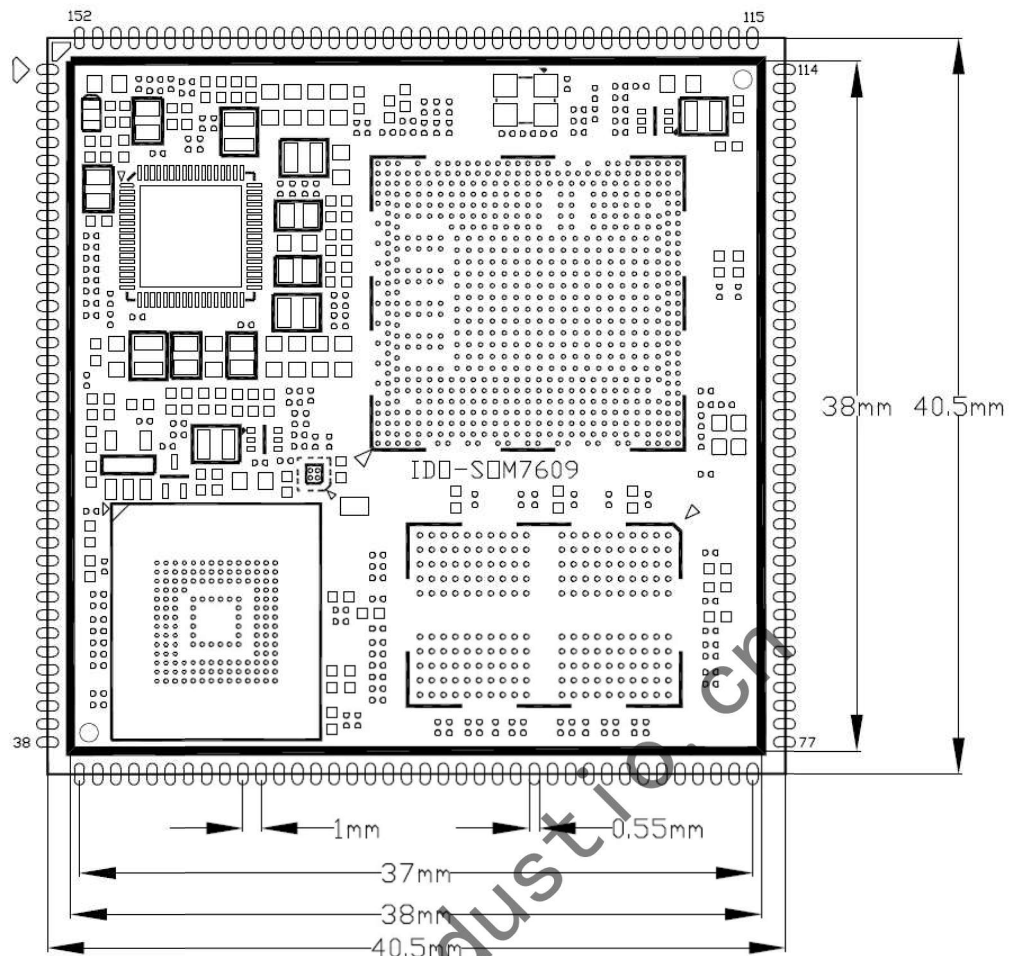
系统支持如下表所示：

| 序号 | 操作系统        | 支持 | 说明 |
|----|-------------|----|----|
| 1  | Android     | ✓  | /  |
| 2  | Debian      | ✓  | /  |
| 3  | Buildroot   | ✓  | /  |
| 4  | Ubuntu      | ✓  | /  |
| 5  | OpenHarmony | ✓  | /  |

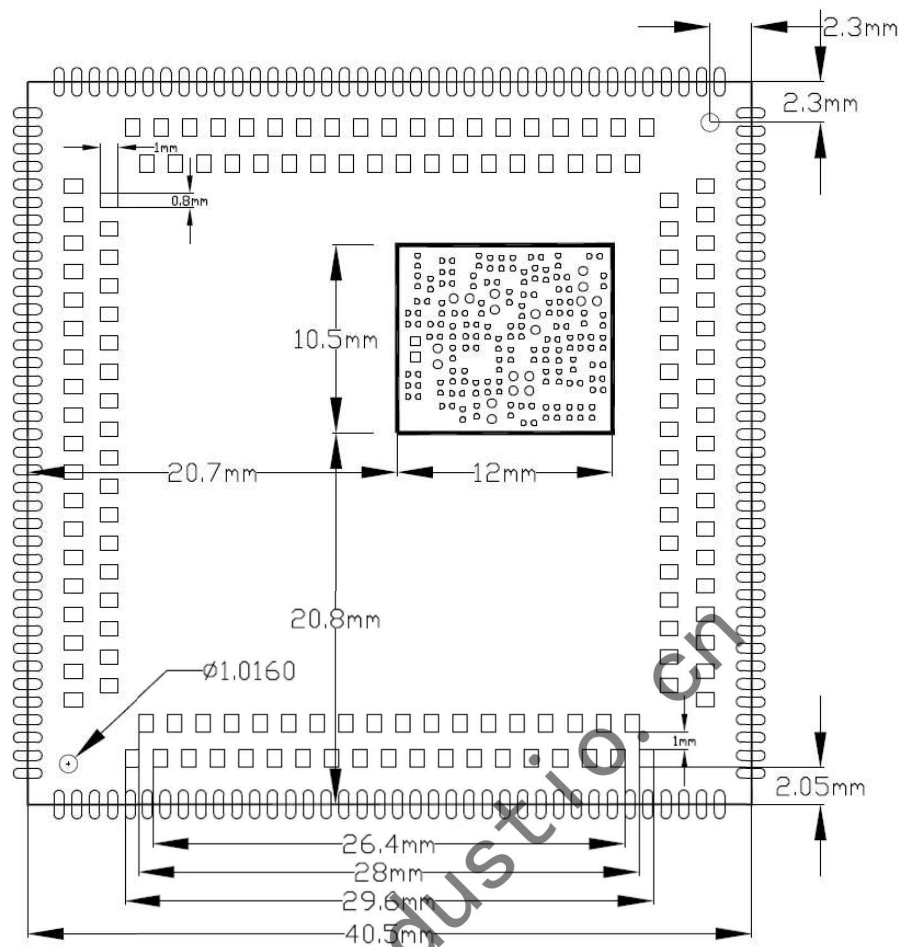
## 3、PCB 尺寸和电气参数

### 3.1 PCB尺寸

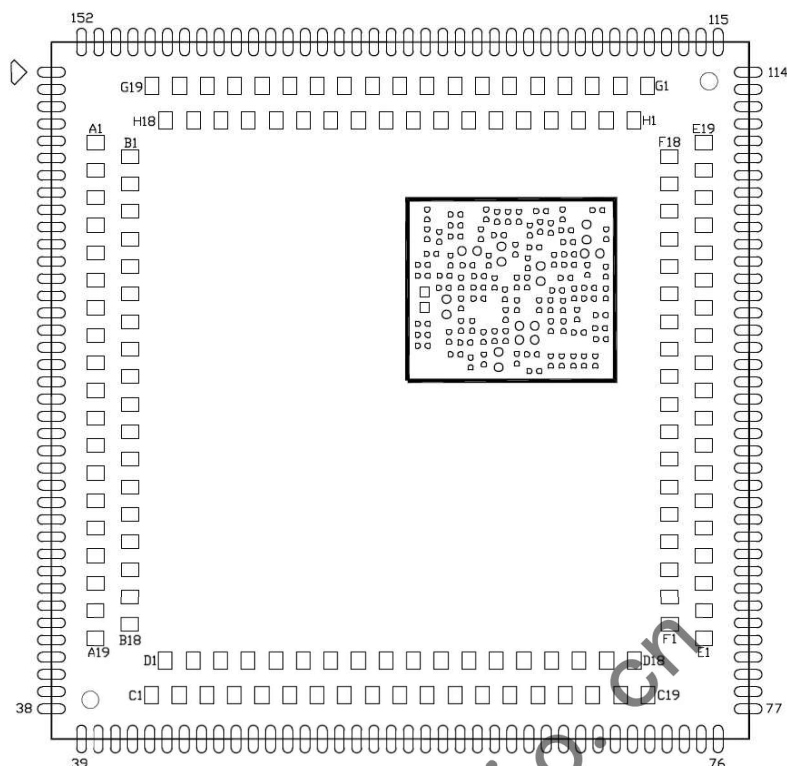
IDO-SOM7609-S1核心板正面尺寸，如下表所示：



IDO-SOM7609-S1核心板背面尺寸，如下图所示：



IDO-SOM7609-S1核心板PIN-OUT图，如下图所示：



## 3.2 电气参数

### 3.2.1 主电源输入

电源输入如下表所示：

| 电源名称                 | 最小电压 | 标称值  | 最大电压 | 峰值电流 | 待机电流 | 关机电流 |
|----------------------|------|------|------|------|------|------|
| VCC5V0_S<br>YS_S5_IN | 4.5V | 5.0V | 5.5V | 1.3A | 5mA  | <1mA |

### 3.2.2 IO电源输入

| 电源名称      | 最小电压  | 最大电压 | 峰值电流  | 备注          |
|-----------|-------|------|-------|-------------|
| VCCIO2_IN | 1.75V | 3.4V | <50mA | VCCIO2电源域电压 |
| VCCIO4_IN | 1.75V | 3.4V | <50mA | VCCIO4电源域电压 |

|           |       |      |       |             |
|-----------|-------|------|-------|-------------|
| VCCIO5_IN | 1.75V | 3.4V | <50mA | VCCIO5电源域电压 |
|-----------|-------|------|-------|-------------|

### 3.2.3 电源输出

电源输出如下表所示：

| 电源名称               | 最小电压  | 标称值  | 最大电压  | 限制电流       |
|--------------------|-------|------|-------|------------|
| VCC_1V8_S3_OU<br>T | 1.75V | 1.8V | 1.85V | 100mA/待机保持 |
| VCC_3V3_S3_OU<br>T | 3.2V  | 3.3V | 3.4V  | 100mA/待机保持 |
| VCC_1V8_S0_OU<br>T | 1.75V | 1.8V | 1.85V | 100mA/待机掉电 |
| VCC_3V3_S0_OU<br>T | 3.2V  | 3.3V | 3.4V  | 100mA/待机掉电 |

## 4、采购型号

采购型号如下表所示：

| 采购型号                  | LPDDR4x | eMMC  | 标称工作温度      |
|-----------------------|---------|-------|-------------|
| IDO-SOM7609-S1-D4E64  | 4GB     | 64GB  | 0℃ ~ +70℃   |
| IDO-SOM7609-S1-D8E64  | 8GB     | 64GB  | 0℃ ~ +70℃   |
| IDO-SOM7609-S1-D8E128 | 8GB     | 128GB | 0℃ ~ +70℃   |
| IDO-SOM7609J-S1-D4E32 | 4GB     | 32GB  | -40℃ ~ +85℃ |
| IDO-SOM7609J-S1-D8E64 | 8GB     | 64GB  | -40℃ ~ +85℃ |

## 5、引脚定义说明

### 5.1 核心板引脚示意图

## 5.2 核心板引脚列表

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|   |                                                                                                                            |      |                              |         |      |  |
|---|----------------------------------------------------------------------------------------------------------------------------|------|------------------------------|---------|------|--|
| 2 | VCC5V0_SYS_S5_IN2                                                                                                          |      | VCC5V0_SYS_S5_I<br>N2        |         |      |  |
| 3 | GND0                                                                                                                       |      | GND0                         |         |      |  |
| 4 | GND1                                                                                                                       |      | GND1                         |         |      |  |
| 5 | SARADC_VIN1_KEY/REC<br>OVERY                                                                                               | 1A22 | SARADC_VIN1_KE<br>Y/RECOVERY |         |      |  |
| 6 | GND2                                                                                                                       |      | GND2                         |         |      |  |
| 7 | ETH1_RXD2_M1/SDMMC<br>1_D0_M0/SAI3_SCLK_M1<br>/I2C9_SDA_M1/SPI1_CLK<br>_M0/PCIE1_CLKREQN_M<br>1/PWM1_CH0_M1/GPIO1_<br>B4_d | A28  | ETH1_RXD2_M1                 |         | 1.8V |  |
|   |                                                                                                                            |      | SDMMC1_D0_M0                 | Default |      |  |
|   |                                                                                                                            |      | SAI3_SCLK_M1                 |         |      |  |
|   |                                                                                                                            |      | I2C9_SDA_M1                  |         |      |  |
|   |                                                                                                                            |      | SPI1_CLK_M0                  |         |      |  |
|   |                                                                                                                            |      | PCIE1_CLKREQN_<br>M1         |         |      |  |
|   |                                                                                                                            |      | PWM1_CH0_M1                  |         |      |  |
|   |                                                                                                                            |      | GPIO1_B4_d                   |         |      |  |
| 8 | ETH1_RXD3_M1/SDMMC<br>1_D1_M0/SAI3_LRCK_M1<br>/I2C9_SCL_M1/SPI1_MO<br>SI_M0/PWM1_CH1_M1/G<br>PIO1_B5_d                     | B27  | ETH1_RXD3_M1                 |         | 1.8V |  |
|   |                                                                                                                            |      | SDMMC1_D1_M0                 | Default |      |  |
|   |                                                                                                                            |      | SAI3_LRCK_M1                 |         |      |  |
|   |                                                                                                                            |      | I2C9_SCL_M1                  |         |      |  |
|   |                                                                                                                            |      | SPI1_MOSI_M0                 |         |      |  |
|   |                                                                                                                            |      | PWM1_CH1_M1                  |         |      |  |
|   |                                                                                                                            |      | GPIO1_B5_d                   |         |      |  |
| 9 | ETH1_TXD2_M1/SDMMC1<br>_D3_M0/SAI3_SDI_M1/U<br>ART3_RTSN_M2/SPI1_C<br>SN0_M0/GPIO1_B7_d                                    | A27  | ETH1_TXD2_M1                 |         | 1.8V |  |
|   |                                                                                                                            |      | SDMMC1_D3_M0                 | Default |      |  |
|   |                                                                                                                            |      | SAI3_SDI_M1                  |         |      |  |
|   |                                                                                                                            |      |                              |         |      |  |

|    |                                                                                               |      |                  |         |      |  |
|----|-----------------------------------------------------------------------------------------------|------|------------------|---------|------|--|
|    |                                                                                               |      | UART3_RTSN_M2    |         |      |  |
|    |                                                                                               |      | SPI1_CSN0_M0     |         |      |  |
|    |                                                                                               |      | GPIO1_B7_d       |         |      |  |
| 10 | ETH1_RXCLK_M1/SDMMC1_D2_M0/SAI3_SDO_M1/UART3_CTSN_M2/SPI1_MISO_M0/PCIE0_CLKREQN_M1/GPIO1_B6_d | 1A23 | ETH1_RXCLK_M1    |         | 1.8V |  |
|    |                                                                                               |      | SDMMC1_D2_M0     | Default |      |  |
|    |                                                                                               |      | SAI3_SDO_M1      |         |      |  |
|    |                                                                                               |      | UART3_CTSN_M2    |         |      |  |
|    |                                                                                               |      | SPI1_MISO_M0     |         |      |  |
|    |                                                                                               |      | PCIE0_CLKREQN_M1 |         |      |  |
|    |                                                                                               |      | GPIO1_B6_d       |         |      |  |
| 11 | ETH1_TXD3_M1/SDMMC1_CMD_M0/PDM0_SDI2_M2/UART3_TX_M2/SPI1_CSN1_M0/PWM0_CH0_M1/GPIO1_C0_d       | B26  | ETH1_TXD3_M1     |         | 1.8V |  |
|    |                                                                                               |      | SDMMC1_CMD_M0    | Default |      |  |
|    |                                                                                               |      | PDM0_SDI2_M2     |         |      |  |
|    |                                                                                               |      | UART3_TX_M2      |         |      |  |
|    |                                                                                               |      | SPI1_CSN1_M0     |         |      |  |
|    |                                                                                               |      | PWM0_CH0_M1      |         |      |  |
|    |                                                                                               |      | GPIO1_C0_d       |         |      |  |
| 12 | ETH1_TXCLK_M1/SDMMC1_CLK_M0/SAI3_MCLK_M1/PDM0_CLK0_M2/UART3_RX_M2/GPIO1_C1_d                  | 1B22 | ETH1_TXCLK_M1    |         | 1.8V |  |
|    |                                                                                               |      | SDMMC1_CLK_M0    | Default |      |  |
|    |                                                                                               |      | SAI3_MCLK_M1     |         |      |  |
|    |                                                                                               |      | PDM0_CLK0_M2     |         |      |  |
|    |                                                                                               |      | UART3_RX_M2      |         |      |  |
|    |                                                                                               |      | GPIO1_C1_d       |         |      |  |
| 13 | GND3                                                                                          |      | GND3             |         |      |  |

|    |                                                                                                                                     |     |               |         |           |  |
|----|-------------------------------------------------------------------------------------------------------------------------------------|-----|---------------|---------|-----------|--|
| 14 | SDMMC0_D0/FSPI1_D0_M0/DSM_AUD_LP_M0/UART0_RX_M1/UART7_RX_M2/I2C8_SCL_M0/SPI0_MOSI_M1/CAN0_RX_M0/PWM2_CH2_M0/GPIO2_A0_d              | B24 | SDMMC0_D0     | Default | 1.8V/3.3V |  |
|    |                                                                                                                                     |     | FSPI1_D0_M0   |         |           |  |
|    |                                                                                                                                     |     | DSM_AUD_LP_M0 |         |           |  |
|    |                                                                                                                                     |     | UART0_RX_M1   |         |           |  |
|    |                                                                                                                                     |     | UART7_RX_M2   |         |           |  |
|    |                                                                                                                                     |     | I2C8_SCL_M0   |         |           |  |
|    |                                                                                                                                     |     | SPI0_MOSI_M1  |         |           |  |
|    |                                                                                                                                     |     | CAN0_RX_M0    |         |           |  |
|    |                                                                                                                                     |     | PWM2_CH2_M0   |         |           |  |
|    |                                                                                                                                     |     | GPIO2_A0_d    |         |           |  |
|    |                                                                                                                                     |     |               |         |           |  |
| 15 | SDMMC0_D1/FSPI1_D1_M0/DSM_AUD_LN_M0/SAI3_MCLK_M3/UART0_TX_M1/UART7_TX_M2/I2C8_SDA_M0/SPI0_MISO_M1/CAN0_TX_M0/PWM2_CH3_M0/GPIO2_A1_d | B25 | SDMMC0_D1     | Default | 1.8V/3.3V |  |
|    |                                                                                                                                     |     | FSPI1_D1_M0   |         |           |  |
|    |                                                                                                                                     |     | DSM_AUD_LN_M0 |         |           |  |
|    |                                                                                                                                     |     | SAI3_MCLK_M3  |         |           |  |
|    |                                                                                                                                     |     | UART0_TX_M1   |         |           |  |
|    |                                                                                                                                     |     | UART7_TX_M2   |         |           |  |
|    |                                                                                                                                     |     | I2C8_SDA_M0   |         |           |  |
|    |                                                                                                                                     |     | SPI0_MISO_M1  |         |           |  |
|    |                                                                                                                                     |     | CAN0_TX_M0    |         |           |  |
|    |                                                                                                                                     |     | PWM2_CH3_M0   |         |           |  |
|    |                                                                                                                                     |     | GPIO2_A1_d    |         |           |  |
| 16 | SDMMC0_D2/FSPI1_D2_M0/DSM_AUD_RP_M0/SAI3_LRCK_M3/JTAG_TCK_M0/UART5_RTSN_M2/SPI0_CSN1_M1/CAN1_R                                      | A23 | SDMMC0_D2     | Default | 1.8V/3.3V |  |
|    |                                                                                                                                     |     | FSPI1_D2_M0   |         |           |  |
|    |                                                                                                                                     |     | DSM_AUD_RP_M0 |         |           |  |
|    |                                                                                                                                     |     | SAI3_LRCK_M3  |         |           |  |
|    |                                                                                                                                     |     |               |         |           |  |

|    |                                                                                                                                 |      |               |         |               |  |
|----|---------------------------------------------------------------------------------------------------------------------------------|------|---------------|---------|---------------|--|
|    | X_M0/I3C1_SCL_M1/GPI<br>O2_A2_d                                                                                                 |      | JTAG_TCK_M0   |         |               |  |
|    |                                                                                                                                 |      | UART5_RTSN_M2 |         |               |  |
|    |                                                                                                                                 |      | SPI0_CSN1_M1  |         |               |  |
|    |                                                                                                                                 |      | CAN1_RX_M0    |         |               |  |
|    |                                                                                                                                 |      | I3C1_SCL_M1   |         |               |  |
|    |                                                                                                                                 |      | GPIO2_A2_d    |         |               |  |
| 17 | SDMMC0_D3/FSPI1_D3_<br>M0/DSM_AUD_RN_M0/S<br>AI3_SDI_M3/JTAG_TMS_<br>M0/UART5_CTSN_M2/C<br>AN1_TX_M0/I3C1_SDA_M<br>1/GPIO2_A3_d | B23  | SDMMC0_D3     | Default | 1.8V/3.3<br>V |  |
|    |                                                                                                                                 |      | FSPI1_D3_M0   |         |               |  |
|    |                                                                                                                                 |      | DSM_AUD_RN_M0 |         |               |  |
|    |                                                                                                                                 |      | SAI3_SDI_M3   |         |               |  |
|    |                                                                                                                                 |      | JTAG_TMS_M0   |         |               |  |
|    |                                                                                                                                 |      | UART5_CTSN_M2 |         |               |  |
|    |                                                                                                                                 |      | CAN1_TX_M0    |         |               |  |
|    |                                                                                                                                 |      | I3C1_SDA_M1   |         |               |  |
|    |                                                                                                                                 |      | GPIO2_A3_d    |         |               |  |
| 18 | SDMMC0_CMD/FSPI1_C<br>SN0_M0/SAI3_SDO_M3/<br>UART5_RX_M2/I2C5_SD<br>A_M0/SPI0_CSN0_M1/P<br>WM2_CH4_M0/GPIO2_A<br>4_d            | 1A21 | SDMMC0_CMD    | Default | 1.8V/3.3<br>V |  |
|    |                                                                                                                                 |      | FSPI1_CSN0_M0 |         |               |  |
|    |                                                                                                                                 |      | SAI3_SDO_M3   |         |               |  |
|    |                                                                                                                                 |      | UART5_RX_M2   |         |               |  |
|    |                                                                                                                                 |      | I2C5_SDA_M0   |         |               |  |
|    |                                                                                                                                 |      | SPI0_CSN0_M1  |         |               |  |
|    |                                                                                                                                 |      | PWM2_CH4_M0   |         |               |  |
|    |                                                                                                                                 |      | GPIO2_A4_d    |         |               |  |
| 19 | SDMMC0_CLK/FSPI1_CL<br>K_M0/SAI3_SCLK_M3/T<br>EST_CLK_OUT/UART5_T                                                               | 1B21 | SDMMC0_CLK    | Default | 1.8V/3.3<br>V |  |
|    |                                                                                                                                 |      | FSPI1_CLK_M0  |         |               |  |
|    |                                                                                                                                 |      |               |         |               |  |

|    |                                                                                                     |      |                |         |                       |  |
|----|-----------------------------------------------------------------------------------------------------|------|----------------|---------|-----------------------|--|
|    | X_M2/I2C5_SCL_M0/SPI0_CLK_M1/I3C1_SDA_PU_M1/GPIO2_A5_d                                              |      | SAI3_SCLK_M3   |         |                       |  |
|    |                                                                                                     |      | TEST_CLK_OUT   |         |                       |  |
|    |                                                                                                     |      | UART5_TX_M2    |         |                       |  |
|    |                                                                                                     |      | I2C5_SCL_M0    |         |                       |  |
|    |                                                                                                     |      | GPIO2_A5_d     |         |                       |  |
|    |                                                                                                     |      | SPI0_CLK_M1    |         |                       |  |
|    |                                                                                                     |      | I3C1_SDA_PU_M1 |         |                       |  |
|    |                                                                                                     |      | GPIO2_A5_d     |         |                       |  |
| 20 | GND11                                                                                               |      | GND11          |         |                       |  |
| 21 | ETH1_TXCTL_M0/SAI4_SDI_M3/UART4_TX_M0/I2C6_SCL_M2/PWM2_CH0_M2/GPIO2_D0_d                            | B16  | ETH1_TXCTL_M0  | Default | VCCIO4<br>(1.8V/3.3V) |  |
|    |                                                                                                     |      | SAI4_SDI_M3    |         |                       |  |
|    |                                                                                                     |      | UART4_TX_M0    |         |                       |  |
|    |                                                                                                     |      | I2C6_SCL_M2    |         |                       |  |
|    |                                                                                                     |      | PWM2_CH0_M2    |         |                       |  |
|    |                                                                                                     |      | GPIO2_D0_d     |         |                       |  |
| 22 | ETH1_TXD0_M0/SAI4_SCLK_M3/UART4_CTSN_M0/I2C5_SCL_M2/PWM1_CH5_M2/GPIO2_C6_d                          | 1A14 | ETH1_TXD0_M0   | Default | VCCIO4<br>(1.8V/3.3V) |  |
|    |                                                                                                     |      | SAI4_SCLK_M3   |         |                       |  |
|    |                                                                                                     |      | UART4_CTSN_M0  |         |                       |  |
|    |                                                                                                     |      | I2C5_SCL_M2    |         |                       |  |
|    |                                                                                                     |      | PWM1_CH5_M2    |         |                       |  |
|    |                                                                                                     |      | GPIO2_C6_d     |         |                       |  |
| 23 | VI_CIF_D2/ETH1_TXD2_M0/SAI2_LRCK_M1/PDM1_SDI3_M0/UART11_RTSN_M1/SPI1_MISO_M1/PWM0_CH0_M2/GPIO2_C3_d | A15  | VI_CIF_D2      |         | VCCIO4<br>(1.8V/3.3V) |  |
|    |                                                                                                     |      | ETH1_TXD2_M0   | Default |                       |  |
|    |                                                                                                     |      | SAI2_LRCK_M1   |         |                       |  |
|    |                                                                                                     |      | PDM1_SDI3_M0   |         |                       |  |
|    |                                                                                                     |      |                |         |                       |  |

|    |                                                                                                  |      |                |         |                       |  |
|----|--------------------------------------------------------------------------------------------------|------|----------------|---------|-----------------------|--|
|    |                                                                                                  |      | UART11_RTSN_M1 |         |                       |  |
|    |                                                                                                  |      | SPI1_MISO_M1   |         |                       |  |
|    |                                                                                                  |      | PWM0_CH0_M2    |         |                       |  |
|    |                                                                                                  |      | GPIO2_C3_d     |         |                       |  |
| 24 | ETH1_TXD1_M0/SAI4_LRCK_M3/UART4_RTSN_M0/I2C5_SDA_M2/PWM0_CH1_M2/GPIO2_C7_d                       | B15  | ETH1_TXD1_M0   | Default | VCCIO4<br>(1.8V/3.3V) |  |
|    |                                                                                                  |      | SAI4_LRCK_M3   |         |                       |  |
|    |                                                                                                  |      | UART4_RTSN_M0  |         |                       |  |
|    |                                                                                                  |      | I2C5_SDA_M2    |         |                       |  |
|    |                                                                                                  |      | PWM0_CH1_M2    |         |                       |  |
|    |                                                                                                  |      | GPIO2_C7_d     |         |                       |  |
| 25 | VI_CIF_D1/ETH1_TXD3_M0/SAI2_SDO_M1/PDM1_SDI0_M0/UART11_TX_M1/SPI1_CSN0_M1/PWM1_CH3_M2/GPIO2_C4_d | 1A13 | VI_CIF_D1      |         | VCCIO4<br>(1.8V/3.3V) |  |
|    |                                                                                                  |      | ETH1_TXD3_M0   | Default |                       |  |
|    |                                                                                                  |      | SAI2_SDO_M1    |         |                       |  |
|    |                                                                                                  |      | PDM1_SDI0_M0   |         |                       |  |
|    |                                                                                                  |      | UART11_TX_M1   |         |                       |  |
|    |                                                                                                  |      | SPI1_CSN0_M1   |         |                       |  |
|    |                                                                                                  |      | PWM1_CH3_M2    |         |                       |  |
|    |                                                                                                  |      | GPIO2_C4_d     |         |                       |  |
| 26 | VI_CIF_D0/ETH1_TXCLK_M0/SAI2_SDI_M1/PDM1_CLK0_M0/UART11_RX_M1/SPI1_CLK_M1/PWM1_CH4_M2            | 1C15 | VI_CIF_D0      |         | VCCIO4<br>(1.8V/3.3V) |  |
|    |                                                                                                  |      | ETH1_TXCLK_M0  | Default |                       |  |
|    |                                                                                                  |      | SAI2_SDI_M1    |         |                       |  |
|    |                                                                                                  |      | PDM1_CLK0_M0   |         |                       |  |
|    |                                                                                                  |      | UART11_RX_M1   |         |                       |  |
|    |                                                                                                  |      | SPI1_CLK_M1    |         |                       |  |
|    |                                                                                                  |      | PWM1_CH4_M2    |         |                       |  |
|    |                                                                                                  |      |                |         |                       |  |



|    |                                                                                                                  |      |                    |         |                       |  |
|----|------------------------------------------------------------------------------------------------------------------|------|--------------------|---------|-----------------------|--|
|    |                                                                                                                  |      | GPIO2_C5_d         |         |                       |  |
| 27 | GND12                                                                                                            |      | GND12              |         |                       |  |
| 28 | VI_CIF_D4/ETH1_RXD3_M0/ETH0_PPSCCLK_M1/SAI2_MCLK_M1/PDM1_CLK1_M0/UART9_TX_M0/SPI1_CSN1_M1/PWM1_CH1_M2/GPIO2_C1_d | 1A15 | VI_CIF_D4          |         | VCCIO4<br>(1.8V/3.3V) |  |
|    |                                                                                                                  |      | ETH1_RXD3_M0       | Default |                       |  |
|    |                                                                                                                  |      | ETH0_PPSCCLK_M1    |         |                       |  |
|    |                                                                                                                  |      | SAI2_MCLK_M1       |         |                       |  |
|    |                                                                                                                  |      | PDM1_CLK1_M0       |         |                       |  |
|    |                                                                                                                  |      | UART9_TX_M0        |         |                       |  |
|    |                                                                                                                  |      | SPI1_CSN1_M1       |         |                       |  |
|    |                                                                                                                  |      | PWM1_CH1_M2        |         |                       |  |
|    |                                                                                                                  |      | GPIO2_C1_d         |         |                       |  |
| 29 | ETH1_RXCTL_M0/UART6_RX_M1/I3C1_SDA_M0/PWM2_CH3_M2/GPIO2_D3_d                                                     | B18  | ETH1_RXCTL_M0      | Default | VCCIO4<br>(1.8V/3.3V) |  |
|    |                                                                                                                  |      | UART6_RX_M1        |         |                       |  |
|    |                                                                                                                  |      | I3C1_SDA_M0        |         |                       |  |
|    |                                                                                                                  |      | PWM2_CH3_M2        |         |                       |  |
|    |                                                                                                                  |      | GPIO2_D3_d         |         |                       |  |
| 30 | ETH1_RXD0_M0/SAI4_SDO_M3/UART4_RX_M0/I2C6_SDA_M2/PWM2_CH1_M2/GPIO2_D1_d                                          | 1A16 | ETH1_RXD0_M0       | Default | VCCIO4<br>(1.8V/3.3V) |  |
|    |                                                                                                                  |      | SAI4_SDO_M3        |         |                       |  |
|    |                                                                                                                  |      | UART4_RX_M0        |         |                       |  |
|    |                                                                                                                  |      | I2C6_SDA_M2        |         |                       |  |
|    |                                                                                                                  |      | PWM2_CH1_M2        |         |                       |  |
|    |                                                                                                                  |      | GPIO2_D1_d         |         |                       |  |
| 31 | VI_CIF_D5/ETH1_RXD2_M0/ETH0_PTP_REFCLK_M1/PDM1_SDI1_M0/UART9_RX_M0/PWM1_CH0_M2/GPIO2_C0_d                        | A17  | VI_CIF_D5          |         | VCCIO4<br>(1.8V/3.3V) |  |
|    |                                                                                                                  |      | ETH1_RXD2_M0       | Default |                       |  |
|    |                                                                                                                  |      | ETH0_PTP_REFCLK_M1 |         |                       |  |

|    |                                                                                                                      |      |                      |         |                       |  |
|----|----------------------------------------------------------------------------------------------------------------------|------|----------------------|---------|-----------------------|--|
|    |                                                                                                                      |      | PDM1_SDI1_M0         |         |                       |  |
|    |                                                                                                                      |      | UART9_RX_M0          |         |                       |  |
|    |                                                                                                                      |      | PWM1_CH0_M2          |         |                       |  |
|    |                                                                                                                      |      | GPIO2_C0_d           |         |                       |  |
| 32 | CAM_CLK0_OUT_M1/ETH1_RXD1_M0/SAI4_MCLK_M3/UART6_TX_M1/I3C1_SCL_M0/PWM2_CH2_M2/GPIO2_D2_d                             | B17  | CAM_CLK0_OUT_M1      |         | VCCIO4<br>(1.8V/3.3V) |  |
|    |                                                                                                                      |      | ETH1_RXD1_M0         | Default |                       |  |
|    |                                                                                                                      |      | SAI4_MCLK_M3         |         |                       |  |
|    |                                                                                                                      |      | UART6_TX_M1          |         |                       |  |
|    |                                                                                                                      |      | I3C1_SCL_M0          |         |                       |  |
|    |                                                                                                                      |      | PWM2_CH2_M2          |         |                       |  |
|    |                                                                                                                      |      | GPIO2_D2_d           |         |                       |  |
| 33 | VI_CIF_D3/ETH1_RXCLK_M0/ETH0_PPSTRIG_M1/SAI2_SCLK_M1/PDM1_SDI2_M0/UART11_CTSN_M1/SPI1_MOSI_M1/PWM1_CH2_M2/GPIO2_C2_d | 1D15 | VI_CIF_D3            |         | VCCIO4<br>(1.8V/3.3V) |  |
|    |                                                                                                                      |      | ETH1_RXCLK_M0        | Default |                       |  |
|    |                                                                                                                      |      | ETH0_PPSTRIG_M1      |         |                       |  |
|    |                                                                                                                      |      | SAI2_SCLK_M1         |         |                       |  |
|    |                                                                                                                      |      | PDM1_SDI2_M0         |         |                       |  |
|    |                                                                                                                      |      | UART11_CTSN_M1       |         |                       |  |
|    |                                                                                                                      |      | SPI1_MOSI_M1         |         |                       |  |
|    |                                                                                                                      |      | PWM1_CH2_M2          |         |                       |  |
|    |                                                                                                                      |      | GPIO2_C2_d           |         |                       |  |
| 34 | GND13                                                                                                                |      | GND13                |         |                       |  |
| 35 | ISP_PRELIGHT_TRIG_M0/ETH1_MDC_M0/UART6_RTSM_M1/I2C9_SDA_M2                                                           | 1B13 | ISP_PRELIGHT_TRIG_M0 |         | VCCIO4<br>(1.8V/3.3V) |  |
|    |                                                                                                                      |      | ETH1_MDC_M0          | Default |                       |  |
|    |                                                                                                                      |      |                      |         |                       |  |

|    |                                                                                                               |         |                      |         |                    |  |
|----|---------------------------------------------------------------------------------------------------------------|---------|----------------------|---------|--------------------|--|
|    | /PWM2_CH4_M2/GPIO2_D4_d                                                                                       |         | UART6_RTSN_M1        |         |                    |  |
|    |                                                                                                               |         | I2C9_SDA_M2          |         |                    |  |
|    |                                                                                                               |         | PWM2_CH4_M2          |         |                    |  |
|    |                                                                                                               |         | GPIO2_D4_d           |         |                    |  |
| 36 | ISP_FLASH_TRIGOUT_M0/ETH1_MDIO_M0/UART6_CTSN_M1/I2C9_SCL_M2/PWM2_CH5_M2/GPIO2_D5_d                            | 1B15    | ISP_FLASH_TRIGOUT_M0 |         | VCCIO4 (1.8V/3.3V) |  |
|    |                                                                                                               |         | ETH1_MDIO_M0         | Default |                    |  |
|    |                                                                                                               |         | UART6_CTSN_M1        |         |                    |  |
|    |                                                                                                               |         | I2C9_SCL_M2          |         |                    |  |
|    |                                                                                                               |         | PWM2_CH5_M2          |         |                    |  |
|    |                                                                                                               |         | GPIO2_D5_d           |         |                    |  |
| 37 | VCCIO4                                                                                                        | 2A7     | VCCIO4_IN            |         |                    |  |
| 38 | VCCIO5                                                                                                        | 2A4 2A5 | VCCIO5_IN            |         |                    |  |
| 39 | VO_LCDC_D17/VO_EBC_SDCE1/ETH0_RXD0_M0/PDM1_SDI1_M2/DSMC_DATA11/FLEXBUS0_D3/UART9_RX_M1/I2C8_SDA_M3/GPIO3_B2_d | A13     | VO_LCDC_D17          |         | VCCIO5 (1.8V/3.3V) |  |
|    |                                                                                                               |         | VO_EBC_SDCE1         |         |                    |  |
|    |                                                                                                               |         | ETH0_RXD0_M0         | Default |                    |  |
|    |                                                                                                               |         | PDM1_SDI1_M2         |         |                    |  |
|    |                                                                                                               |         | DSMC_DATA11          |         |                    |  |
|    |                                                                                                               |         | FLEXBUS0_D3          |         |                    |  |
|    |                                                                                                               |         | UART9_RX_M1          |         |                    |  |
|    |                                                                                                               |         | I2C8_SDA_M3          |         |                    |  |
|    |                                                                                                               |         | GPIO3_B2_d           |         |                    |  |
| 40 | VO_LCDC_D20/VO_EBC_VCOM/ETH0_RXCTL_M0/PDM1_CLK1_M2/DSMC_DATA13/FLEXBUS0_D5                                    | B13     | VO_LCDC_D20          |         | VCCIO5 (1.8V/3.3V) |  |
|    |                                                                                                               |         | VO_EBC_VCOM          |         |                    |  |
|    |                                                                                                               |         | ETH0_RXCTL_M0        | Default |                    |  |
|    |                                                                                                               |         | PDM1_CLK1_M2         |         |                    |  |

|    |                                                                                                                                                                       |      |                 |         |                    |  |
|----|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------|------|-----------------|---------|--------------------|--|
|    | /UART1_TX_M2/UART10_RTSN_M0/GPIO3_A7_d                                                                                                                                |      | DSMC_DATA13     |         |                    |  |
|    |                                                                                                                                                                       |      | FLEXBUS0_D5     |         |                    |  |
|    |                                                                                                                                                                       |      | UART1_TX_M2     |         |                    |  |
|    |                                                                                                                                                                       |      | UART10_RTSN_M0  |         |                    |  |
|    |                                                                                                                                                                       |      | GPIO3_A7_d      |         |                    |  |
| 41 | VO_LCDC_D1/VO_EBC_SDDO1/ETH0_RXD3_M0/SAI2_SDI_M2/DSMC_CSN3/FLEXBUS0_D12/FLEXBUS1_D15_M0/FLEXBUS0_CSN_M3/UART2_RTSN_M2/SPI4_CSN1_M1/I3C1_SDA_M2/PWM2_CH4_M3/GPIO3_D2_d | 1A10 | VO_LCDC_D1      |         |                    |  |
|    |                                                                                                                                                                       |      | VO_EBC_SDDO1    |         |                    |  |
|    |                                                                                                                                                                       |      | ETH0_RXD3_M0    | Default |                    |  |
|    |                                                                                                                                                                       |      | SAI2_SDI_M2     |         |                    |  |
|    |                                                                                                                                                                       |      | DSMC_CSN3       |         |                    |  |
|    |                                                                                                                                                                       |      | FLEXBUS0_D12    |         |                    |  |
|    |                                                                                                                                                                       |      | FLEXBUS1_D15_M0 |         |                    |  |
|    |                                                                                                                                                                       |      | FLEXBUS0_CSN_M3 |         | VCCIO5 (1.8V/3.3V) |  |
|    |                                                                                                                                                                       |      | UART2_RTSN_M2   |         |                    |  |
|    |                                                                                                                                                                       |      | SPI4_CSN1_M1    |         |                    |  |
|    |                                                                                                                                                                       |      | I3C1_SDA_M2     |         |                    |  |
|    |                                                                                                                                                                       |      | PWM2_CH4_M3     |         |                    |  |
|    |                                                                                                                                                                       |      | GPIO3_D2_d      |         |                    |  |
| 42 | VO_LCDC_D0/VO_EBC_SDDO0/ETH0_RXD2_M0/SAI2_SDO_M2/DSMC_CSN0/FLEXBUS1_D2/UART2_CTSN_M2/I3C1_SCL_M2/PWM2_CH5_M3/GPIO3_D3_d                                               | B12  | VO_LCDC_D0      |         |                    |  |
|    |                                                                                                                                                                       |      | VO_EBC_SDDO0    |         |                    |  |
|    |                                                                                                                                                                       |      | ETH0_RXD2_M0    | Default | VCCIO5 (1.8V/3.3V) |  |
|    |                                                                                                                                                                       |      | SAI2_SDO_M2     |         |                    |  |
|    |                                                                                                                                                                       |      | DSMC_CSN0       |         |                    |  |
|    |                                                                                                                                                                       |      | FLEXBUS1_D2     |         |                    |  |

|    |                                                                                                                                 |      |                 |         |                       |  |
|----|---------------------------------------------------------------------------------------------------------------------------------|------|-----------------|---------|-----------------------|--|
|    |                                                                                                                                 |      | UART2_CTSN_M2   |         |                       |  |
|    |                                                                                                                                 |      | I3C1_SCL_M2     |         |                       |  |
|    |                                                                                                                                 |      | PWM2_CH5_M3     |         |                       |  |
|    |                                                                                                                                 |      | GPIO3_D3_d      |         |                       |  |
| 43 | VO_LCDC_D18/VO_EBC_SDCE2/ETH0_RXD1_M0/PDM1_CLK0_M2/DSMC_DATA12/FLEXBUS0_D4/UART10_TX_M0/SPI4_CS_N0_M1/PWM1_CH3_M3/GPIO3_B1_d    | 1A11 | VO_LCDC_D18     |         | VCCIO5<br>(1.8V/3.3V) |  |
|    |                                                                                                                                 |      | VO_EBC_SDCE2    |         |                       |  |
|    |                                                                                                                                 |      | ETH0_RXD1_M0    | Default |                       |  |
|    |                                                                                                                                 |      | PDM1_CLK0_M2    |         |                       |  |
|    |                                                                                                                                 |      | DSMC_DATA12     |         |                       |  |
|    |                                                                                                                                 |      | FLEXBUS0_D4     |         |                       |  |
|    |                                                                                                                                 |      | UART10_TX_M0    |         |                       |  |
|    |                                                                                                                                 |      | SPI4_CS_N0_M1   |         |                       |  |
|    |                                                                                                                                 |      | PWM1_CH3_M3     |         |                       |  |
|    |                                                                                                                                 |      | GPIO3_B1_d      |         |                       |  |
| 44 | VO_LCDC_D2/VO_EBC_SDDO2/ETH0_RXCLK_M0/SAI2_MCLK_M2/DSMC_CSN2/FLEXBUS0_D11/FLEXBUS1_CSN_M2/SPI4_CLK_M1/I3C1_SDA_PU_M2/GPIO3_D1_d | 1A12 | VO_LCDC_D2      |         | VCCIO5<br>(1.8V/3.3V) |  |
|    |                                                                                                                                 |      | VO_EBC_SDDO2    |         |                       |  |
|    |                                                                                                                                 |      | ETH0_RXCLK_M0   | Default |                       |  |
|    |                                                                                                                                 |      | SAI2_MCLK_M2    |         |                       |  |
|    |                                                                                                                                 |      | DSMC_CSN2       |         |                       |  |
|    |                                                                                                                                 |      | FLEXBUS0_D11    |         |                       |  |
|    |                                                                                                                                 |      | FLEXBUS1_CSN_M2 |         |                       |  |
|    |                                                                                                                                 |      | SPI4_CLK_M1     |         |                       |  |
|    |                                                                                                                                 |      | I3C1_SDA_PU_M2  |         |                       |  |
|    |                                                                                                                                 |      | GPIO3_D1_d      |         |                       |  |
| 45 | GND14                                                                                                                           |      | GND14           |         |                       |  |

|    |                                                                                                                 |     |               |         |                       |  |
|----|-----------------------------------------------------------------------------------------------------------------|-----|---------------|---------|-----------------------|--|
| 46 | VO_LCDC_D16/VO_EBC_SDCE0/ETH0_TXCTL_M0/PDM1_SDI0_M2/DSMC_DATA10/FLEXBUS0_D2/UART9_TX_M1/I2C8_SCL_M3/GPIO3_B3_d  | A11 | VO_LCDC_D16   |         | VCCIO5<br>(1.8V/3.3V) |  |
|    |                                                                                                                 |     | VO_EBC_SDCE0  |         |                       |  |
|    |                                                                                                                 |     | ETH0_TXCTL_M0 | Default |                       |  |
|    |                                                                                                                 |     | PDM1_SDI0_M2  |         |                       |  |
|    |                                                                                                                 |     | DSMC_DATA10   |         |                       |  |
|    |                                                                                                                 |     | FLEXBUS0_D2   |         |                       |  |
|    |                                                                                                                 |     | UART9_TX_M1   |         |                       |  |
|    |                                                                                                                 |     | I2C8_SCL_M3   |         |                       |  |
|    |                                                                                                                 |     | GPIO3_B3_d    |         |                       |  |
| 47 | VO_LCDC_D14/VO_EBC_SDDO14/ETH0_TXD0_M0/SPDIF_TX1_M0/DSMC_DATA8/FLEXBUS0_D0/UART9_CTSN_M1/PWM1_CH5_M3/GPIO3_B5_d | 1A9 | VO_LCDC_D14   |         | VCCIO5<br>(1.8V/3.3V) |  |
|    |                                                                                                                 |     | VO_EBC_SDDO14 |         |                       |  |
|    |                                                                                                                 |     | ETH0_TXD0_M0  | Default |                       |  |
|    |                                                                                                                 |     | SPDIF_TX1_M0  |         |                       |  |
|    |                                                                                                                 |     | DSMC_DATA8    |         |                       |  |
|    |                                                                                                                 |     | FLEXBUS0_D0   |         |                       |  |
|    |                                                                                                                 |     | UART9_CTSN_M1 |         |                       |  |
|    |                                                                                                                 |     | PWM1_CH5_M3   |         |                       |  |
|    |                                                                                                                 |     | GPIO3_B5_d    |         |                       |  |
| 48 | VO_LCDC_D15/VO_EBC_SDDO15/ETH0_TXD1_M0/SPDIF_RX1_M0/DSMC_DATA9/FLEXBUS0_D1/UART9_RTSN_M1/PWM1_CH4_M3/GPIO3_B4_d | B10 | VO_LCDC_D15   |         | VCCIO5<br>(1.8V/3.3V) |  |
|    |                                                                                                                 |     | VO_EBC_SDDO15 |         |                       |  |
|    |                                                                                                                 |     | ETH0_TXD1_M0  | Default |                       |  |
|    |                                                                                                                 |     | SPDIF_RX1_M0  |         |                       |  |
|    |                                                                                                                 |     | DSMC_DATA9    |         |                       |  |
|    |                                                                                                                 |     | FLEXBUS0_D1   |         |                       |  |
|    |                                                                                                                 |     | UART9_RTSN_M1 |         |                       |  |
|    |                                                                                                                 |     |               |         |                       |  |
|    |                                                                                                                 |     |               |         |                       |  |

|    |                                                                                                                                                         |     |                 |         |                       |  |
|----|---------------------------------------------------------------------------------------------------------------------------------------------------------|-----|-----------------|---------|-----------------------|--|
|    |                                                                                                                                                         |     | PWM1_CH4_M3     |         |                       |  |
|    |                                                                                                                                                         |     | GPIO3_B4_d      |         |                       |  |
| 49 | VO_LCDC_D8/VO_EBC_SDDO8/ETH0_TXD2_M0/SAI2_LRCK_M2/DSMC_INT3/FLEXBUS0_D10/FLEXBUS0_CSN_M2/UART11_CTSN_M0/SPI4_MOSI_M1/I2C9_SDA_M3/PWM2_CH1_M3/GPIO3_C3_d | 1A8 | VO_LCDC_D8      |         | VCCIO5<br>(1.8V/3.3V) |  |
|    |                                                                                                                                                         |     | VO_EBC_SDDO8    |         |                       |  |
|    |                                                                                                                                                         |     | ETH0_TXD2_M0    | Default |                       |  |
|    |                                                                                                                                                         |     | SAI2_LRCK_M2    |         |                       |  |
|    |                                                                                                                                                         |     | DSMC_INT3       |         |                       |  |
|    |                                                                                                                                                         |     | FLEXBUS0_D10    |         |                       |  |
|    |                                                                                                                                                         |     | FLEXBUS0_CSN_M2 |         |                       |  |
|    |                                                                                                                                                         |     | UART11_CTSN_M0  |         |                       |  |
|    |                                                                                                                                                         |     | SPI4_MOSI_M1    |         |                       |  |
|    |                                                                                                                                                         |     | I2C9_SDA_M3     |         |                       |  |
|    |                                                                                                                                                         |     | PWM2_CH1_M3     |         |                       |  |
|    |                                                                                                                                                         |     | GPIO3_C3_d      |         |                       |  |
| 50 | VO_LCDC_D9/VO_EBC_SDDO9/ETH0_TXD3_M0/SAI2_SCLK_M2/DSMC_INT1/FLEXBUS0_D9/UART11_RTSN_M0/SPI4_MISO_M1/I2C9_SCL_M3/PWM2_CH0_M3/GPIO3_C2_d                  | B9  | VO_LCDC_D9      |         | VCCIO5<br>(1.8V/3.3V) |  |
|    |                                                                                                                                                         |     | VO_EBC_SDDO9    |         |                       |  |
|    |                                                                                                                                                         |     | ETH0_TXD3_M0    | Default |                       |  |
|    |                                                                                                                                                         |     | SAI2_SCLK_M2    |         |                       |  |
|    |                                                                                                                                                         |     | DSMC_INT1       |         |                       |  |
|    |                                                                                                                                                         |     | FLEXBUS0_D9     |         |                       |  |
|    |                                                                                                                                                         |     | UART11_RTSN_M0  |         |                       |  |
|    |                                                                                                                                                         |     | SPI4_MISO_M1    |         |                       |  |
|    |                                                                                                                                                         |     | I2C9_SCL_M3     |         |                       |  |
|    |                                                                                                                                                         |     | PWM2_CH0_M3     |         |                       |  |
|    |                                                                                                                                                         |     | GPIO3_C2_d      |         |                       |  |



|    |                                                                                                                             |     |               |         |                       |  |
|----|-----------------------------------------------------------------------------------------------------------------------------|-----|---------------|---------|-----------------------|--|
| 51 | VO_LCDC_D13/VO_EBC_SDDO13/ETH0_TXCLK_M0/DSMC_DQS1/FLEXBUS0_CLK/SPI3_CSN0_M1/PWM0_CH1_M3/GPIO3_B6_d                          | B11 | VO_LCDC_D13   |         | VCCIO5<br>(1.8V/3.3V) |  |
|    |                                                                                                                             |     | VO_EBC_SDDO13 |         |                       |  |
|    |                                                                                                                             |     | ETH0_TXCLK_M0 | Default |                       |  |
|    |                                                                                                                             |     | DSMC_DQS1     |         |                       |  |
|    |                                                                                                                             |     | FLEXBUS0_CLK  |         |                       |  |
|    |                                                                                                                             |     | SPI3_CSN0_M1  |         |                       |  |
|    |                                                                                                                             |     | PWM0_CH1_M3   |         |                       |  |
|    |                                                                                                                             |     | GPIO3_B6_d    |         |                       |  |
| 52 | GND15                                                                                                                       |     | GND15         |         |                       |  |
| 53 | VO_LCDC_D22/VO_EBC_GDSP/ETH0_MDIO_M0/PDM1_SDI3_M2/DSMC_DATA15/FLEXBUS0_D7/UART1_RTSN_M2/SPI2_CSN1_M2/PWM1_CH1_M3/GPIO3_A5_d | A9  | VO_LCDC_D22   |         | VCCIO5<br>(1.8V/3.3V) |  |
|    |                                                                                                                             |     | VO_EBC_GDSP   |         |                       |  |
|    |                                                                                                                             |     | ETH0_MDIO_M0  | Default |                       |  |
|    |                                                                                                                             |     | PDM1_SDI3_M2  |         |                       |  |
|    |                                                                                                                             |     | DSMC_DATA15   |         |                       |  |
|    |                                                                                                                             |     | FLEXBUS0_D7   |         |                       |  |
|    |                                                                                                                             |     | UART1_RTSN_M2 |         |                       |  |
|    |                                                                                                                             |     | SPI2_CSN1_M2  |         |                       |  |
|    |                                                                                                                             |     | PWM1_CH1_M3   |         |                       |  |
| 54 | VO_LCDC_D21/VO_EBC_GDOE/ETH0_MDC_M0/PDM1_SDI2_M2/DSMC_DATA14/FLEXBUS0_D6/UART1_RX_M2/UART10_CTSN_M0/PWM1_CH2_M3/GPIO3_A6_d  | 1A7 | GPIO3_A5_d    |         | VCCIO5<br>(1.8V/3.3V) |  |
|    |                                                                                                                             |     | VO_LCDC_D21   |         |                       |  |
|    |                                                                                                                             |     | VO_EBC_GDOE   |         |                       |  |
|    |                                                                                                                             |     | ETH0_MDC_M0   | Default |                       |  |
|    |                                                                                                                             |     | PDM1_SDI2_M2  |         |                       |  |
|    |                                                                                                                             |     | DSMC_DATA14   |         |                       |  |
|    |                                                                                                                             |     | FLEXBUS0_D6   |         |                       |  |

|    |                                                                                                                                                  |      |                     |         |                       |
|----|--------------------------------------------------------------------------------------------------------------------------------------------------|------|---------------------|---------|-----------------------|
|    |                                                                                                                                                  |      | UART1_RX_M2         |         |                       |
|    |                                                                                                                                                  |      | UART10_CTSN_M0      |         |                       |
|    |                                                                                                                                                  |      | PWM1_CH2_M3         |         |                       |
|    |                                                                                                                                                  |      | GPIO3_A6_d          |         |                       |
| 55 | VO_LCDC_D23/VO_EBC_SDSHR/ETH_CLK0_25M_OUT_M0/SAI4_SDI_M1/DSMC_RDYN/FLEXBUS1_D11/FLEXBUS0_CSN_M0/UART1_CTSN_M2/SPI2_CLK_M2/PWM1_CH0_M3/GPIO3_A4_d | 1D13 | VO_LCDC_D23         |         | VCCIO5<br>(1.8V/3.3V) |
|    |                                                                                                                                                  |      | VO_EBC_SDSHR        |         |                       |
|    |                                                                                                                                                  |      | ETH_CLK0_25M_OUT_M0 |         |                       |
|    |                                                                                                                                                  |      | SAI4_SDI_M1         |         |                       |
|    |                                                                                                                                                  |      | DSMC_RDYN           |         |                       |
|    |                                                                                                                                                  |      | FLEXBUS1_D11        |         |                       |
|    |                                                                                                                                                  |      | FLEXBUS0_CSN_M0     |         |                       |
|    |                                                                                                                                                  |      | UART1_CTSN_M2       |         |                       |
|    |                                                                                                                                                  |      | SPI2_CLK_M2         |         |                       |
|    |                                                                                                                                                  |      | PWM1_CH0_M3         |         |                       |
|    |                                                                                                                                                  |      | GPIO3_A4_d          | Default |                       |
| 56 | VO_LCDC_D19/VO_EBC_SDCE3/ETH0_MCLK_M0/SAI4_MCLK_M1/DSMC_CSN1/FLEXBUS0_D8/UART10_RX_M0/SPI2_MOSI_M2/PWM0_CH0_M3/GPIO3_B0_d                        | B14  | VO_LCDC_D19         |         | VCCIO5<br>(1.8V/3.3V) |
|    |                                                                                                                                                  |      | VO_EBC_SDCE3        |         |                       |
|    |                                                                                                                                                  |      | ETH0_MCLK_M0        | Default |                       |
|    |                                                                                                                                                  |      | SAI4_MCLK_M1        |         |                       |
|    |                                                                                                                                                  |      | DSMC_CSN1           |         |                       |
|    |                                                                                                                                                  |      | FLEXBUS0_D8         |         |                       |
|    |                                                                                                                                                  |      | UART10_RX_M0        |         |                       |
|    |                                                                                                                                                  |      | SPI2_MOSI_M2        |         |                       |
|    |                                                                                                                                                  |      | PWM0_CH0_M3         |         |                       |

|    |                                                                                                                                                |      |                 |         |                       |
|----|------------------------------------------------------------------------------------------------------------------------------------------------|------|-----------------|---------|-----------------------|
|    |                                                                                                                                                |      | GPIO3_B0_d      |         |                       |
| 57 | VO_LCDC_D12/VO_EBC_SDDO12/ETH0_PPSTRIG_M0/SAI1_SDI0_M1/DSMC_DQS0/FLEXBUS1_D10/FLEXBUS1_CSN_M0/UART2_RX_M2/UART3_CTSN_M1/I2C4_SDA_M3/GPIO3_B7_d | 1D12 | VO_LCDC_D12     |         | VCCIO5<br>(1.8V/3.3V) |
|    |                                                                                                                                                |      | VO_EBC_SDDO12   |         |                       |
|    |                                                                                                                                                |      | ETH0_PPSTRIG_M0 |         |                       |
|    |                                                                                                                                                |      | SAI1_SDI0_M1    |         |                       |
|    |                                                                                                                                                |      | DSMC_DQS0       |         |                       |
|    |                                                                                                                                                |      | FLEXBUS1_D10    |         |                       |
|    |                                                                                                                                                |      | FLEXBUS1_CSN_M0 |         |                       |
|    |                                                                                                                                                |      | UART2_RX_M2     |         |                       |
|    |                                                                                                                                                |      | UART3_CTSN_M1   |         |                       |
|    |                                                                                                                                                |      | I2C4_SDA_M3     |         |                       |
|    |                                                                                                                                                |      | GPIO3_B7_d      | Default |                       |
| 58 | VO_LCDC_D11/VO_EBC_SDDO11/ETH0_PPCLK_M0/SAI1_SDO3_M1/DSMC_DATA7/FLEXBUS1_D9/UART2_TX_M2/UART3_RTSN_M1/I2C4_SCL_M3/GPIO3_C0_d                   | 1E9  | VO_LCDC_D11     |         | VCCIO5<br>(1.8V/3.3V) |
|    |                                                                                                                                                |      | VO_EBC_SDDO11   |         |                       |
|    |                                                                                                                                                |      | ETH0_PPCLK_M0   |         |                       |
|    |                                                                                                                                                |      | SAI1_SDO3_M1    |         |                       |
|    |                                                                                                                                                |      | DSMC_DATA7      |         |                       |
|    |                                                                                                                                                |      | FLEXBUS1_D9     |         |                       |
|    |                                                                                                                                                |      | UART2_TX_M2     |         |                       |
|    |                                                                                                                                                |      | UART3_RTSN_M1   |         |                       |
|    |                                                                                                                                                |      | I2C4_SCL_M3     |         |                       |
|    |                                                                                                                                                |      | GPIO3_C0_d      | Default |                       |
| 59 | VO_LCDC_D7/VO_EBC_SDDO7/SAI1_SDO1_M1/DS                                                                                                        | 1D9  | VO_LCDC_D7      |         | 1.8V/3.3V             |
|    |                                                                                                                                                |      |                 |         |                       |

|    |                                                                                                                                              |      |                    |         |                       |  |
|----|----------------------------------------------------------------------------------------------------------------------------------------------|------|--------------------|---------|-----------------------|--|
|    | MC_DATA5/FLEXBUS1_D7/UART11_TX_M0/SPI2_CSN0_M2/I2C5_SCL_M3/CAN0_TX_M3/GPIO3_C4_d                                                             |      | VO_EBC_SDDO7       |         |                       |  |
|    |                                                                                                                                              |      | SAI1_SDO1_M1       |         |                       |  |
|    |                                                                                                                                              |      | DSMC_DATA5         |         |                       |  |
|    |                                                                                                                                              |      | FLEXBUS1_D7        |         |                       |  |
|    |                                                                                                                                              |      | UART11_TX_M0       |         |                       |  |
|    |                                                                                                                                              |      | SPI2_CSN0_M2       |         |                       |  |
|    |                                                                                                                                              |      | I2C5_SCL_M3        | Default |                       |  |
|    |                                                                                                                                              |      | CAN0_TX_M3         |         |                       |  |
|    |                                                                                                                                              |      | GPIO3_C4_d         |         |                       |  |
| 60 | VO_LCDC_D10/VO_EBC_SDDO10/ETH0_PTP_REFCLK_M0/SAI1_SDO2_M1/DSMC_DATA6/FLEXBUS1_D8/UART11_RX_M0/SPI2_MISO_M2/I2C5_SDA_M3/CAN0_RX_M3/GPIO3_C1_d | 1B10 | VO_LCDC_D10        |         | VCCIO5<br>(1.8V/3.3V) |  |
|    |                                                                                                                                              |      | VO_EBC_SDDO10      |         |                       |  |
|    |                                                                                                                                              |      | ETH0_PTP_REFCLK_M0 |         |                       |  |
|    |                                                                                                                                              |      | SAI1_SDO2_M1       |         |                       |  |
|    |                                                                                                                                              |      | DSMC_DATA6         |         |                       |  |
|    |                                                                                                                                              |      | FLEXBUS1_D8        |         |                       |  |
|    |                                                                                                                                              |      | UART11_RX_M0       |         |                       |  |
|    |                                                                                                                                              |      | SPI2_MISO_M2       |         |                       |  |
|    |                                                                                                                                              |      | I2C5_SDA_M3        | Default |                       |  |
|    |                                                                                                                                              |      | CAN0_RX_M3         |         |                       |  |
|    |                                                                                                                                              |      | GPIO3_C1_d         |         |                       |  |
| 61 | VO_LCDC_CLK/VO_EBC_SDOE/CAM_CLK0_OUT_M0/SAI4_SCLK_M1/DSMC_RESETN/FLEXBUS0_D15_M0/FLEXBUS1_D12_M0/FLEXBUS1_CSN_M1/UART5_RTSN_M0/SPI3          | 1E7  | VO_LCDC_CLK        |         | VCCIO5<br>(1.8V/3.3V) |  |
|    |                                                                                                                                              |      | VO_EBC_SDOE        |         |                       |  |
|    |                                                                                                                                              |      | CAM_CLK0_OUT_M0    | Default |                       |  |
|    |                                                                                                                                              |      | SAI4_SCLK_M1       |         |                       |  |

|    |                                                                                                                                                                |      |                                                                                                                                                                                                 |         |                       |
|----|----------------------------------------------------------------------------------------------------------------------------------------------------------------|------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|---------|-----------------------|
|    | _CSN1_M1/PWM2_CH7_M3/GPIO3_D7_d                                                                                                                                |      | DSMC_RESETN<br>FLEXBUS0_D15_M0<br>FLEXBUS1_D12_M0<br>FLEXBUS1_CSN_M1<br>UART5_RTSN_M0<br>SPI3_CSN1_M1<br>PWM2_CH7_M3<br>GPIO3_D7_d                                                              |         |                       |
| 62 | SPDIF_RX0_M1/CAM_CLK1_OUT_M0/SAI4_LRCK_M1/DSMC_INT0/FLEXBUS0_D13_M0/FLEXBUS1_D14_M0/FLEXBUS1_CSN_M3/UART3_TX_M1/SPI1_CSN1_M2/I2C7_SCL_M2/MIPI_TE_M2/GPIO4_A0_d | 1B12 | SPDIF_RX0_M1<br>CAM_CLK1_OUT_M0<br>SAI4_LRCK_M1<br>DSMC_INT0<br>FLEXBUS0_D13_M0<br>FLEXBUS1_D14_M0<br>FLEXBUS1_CSN_M3<br>UART3_TX_M1<br>SPI1_CSN1_M2<br>I2C7_SCL_M2<br>MIPI_TE_M2<br>GPIO4_A0_d | Default | VCCIO5<br>(1.8V/3.3V) |
| 63 | VO_POST_EMPTY/SPDIF                                                                                                                                            | 1B7  | VO_POST_EMPTY                                                                                                                                                                                   |         | VCCIO5                |

|    |                                                                                                                                  |     |                                                                                                                                                                  |         |                       |  |
|----|----------------------------------------------------------------------------------------------------------------------------------|-----|------------------------------------------------------------------------------------------------------------------------------------------------------------------|---------|-----------------------|--|
|    | _TX0_M1/CAM_CLK2_OUT_M0/SAI4_SDO_M1/DSMC_INT2/FLEXBUS0_D14_M0/FLEXBUS1_D13_M0/FLEXBUS0_CSN_M1/UART3_RX_M1/I2C7_SDA_M2/GPIO4_A1_d |     | SPDIF_TX0_M1<br>CAM_CLK2_OUT_M0<br>SAI4_SDO_M1<br>DSMC_INT2<br>FLEXBUS0_D14_M0<br>FLEXBUS1_D13_M0<br>FLEXBUS0_CSN_M1<br>UART3_RX_M1<br>I2C7_SDA_M2<br>GPIO4_A1_d |         | (1.8V/3.3V)           |  |
| 64 | SPDIF_TX0_M0/FLEXBUS0_D15_M1/UART2_TX_M1/I2C3_SCL_M0/PCIE0_CLKREQN_M2/CAN1_TX_M2/GPIO4_B5_d                                      | 1A4 | SPDIF_TX0_M0<br>FLEXBUS0_D15_M1<br>UART2_TX_M1<br>I2C3_SCL_M0<br>PCIE0_CLKREQN_M2<br>CAN1_TX_M2<br>GPIO4_B5_d                                                    | Default | VCCIO2<br>(1.8V/3.3V) |  |
| 65 | SPDIF_RX0_M0/FLEXBUS0_CSN_M4/UART2_RX_M1/I2C3_SDA_M0/CAN1_RX_M2/GPIO4_B4_d                                                       | B8  | SPDIF_RX0_M0<br>FLEXBUS0_CSN_M4<br>UART2_RX_M1<br>I2C3_SDA_M0                                                                                                    | Default | VCCIO2<br>(1.8V/3.3V) |  |

|    |                                                                                                                 |     |                      |         |                           |  |
|----|-----------------------------------------------------------------------------------------------------------------|-----|----------------------|---------|---------------------------|--|
|    |                                                                                                                 |     | CAN1_RX_M2           |         |                           |  |
|    |                                                                                                                 |     | GPIO4_B4_d           |         |                           |  |
| 66 | SAI1_LRCK_M0/FLEXBU<br>S1_D12_M1/SPI4_CSN1_<br>M2/UART5_CTSN_M1/I2<br>C2_SDA_M2/PCIE1_CLK<br>REQN_M2/GPIO4_A5_d | 1B6 | SAI1_LRCK_M0         | Default | VCCIO2<br>(1.8V/3.3<br>V) |  |
|    |                                                                                                                 |     | FLEXBUS1_D12_M1      |         |                           |  |
|    |                                                                                                                 |     | SPI4_CSN1_M2         |         |                           |  |
|    |                                                                                                                 |     | UART5_CTSN_M1        |         |                           |  |
|    |                                                                                                                 |     | I2C2_SDA_M2          |         |                           |  |
|    |                                                                                                                 |     | PCIE1_CLKREQN_<br>M2 |         |                           |  |
|    |                                                                                                                 |     | GPIO4_A5_d           |         |                           |  |
| 67 | SAI1_SDO0_M0/SAI4_SDI<br>_M0/SPI3_CLK_M2/PW<br>M2_CH6_M0/GPIO4_A7_<br>d                                         | A7  | SAI1_SDO0_M0         | Default | VCCIO2<br>(1.8V/3.3<br>V) |  |
|    |                                                                                                                 |     | SAI4_SDI_M0          |         |                           |  |
|    |                                                                                                                 |     | SPI3_CLK_M2          |         |                           |  |
|    |                                                                                                                 |     | PWM2_CH6_M0          |         |                           |  |
|    |                                                                                                                 |     | GPIO4_A7_d           |         |                           |  |
| 68 | SAI1_SDI0_M0/SAI4_SDO<br>_M0/PDM1_SDI0_M1/SPI<br>4_CSN0_M2/SPI3_CSN1_<br>M2/PWM2_CH7_M0/GPI<br>O4_B3_d          | 1A6 | SAI1_SDI0_M0         | Default | VCCIO2<br>(1.8V/3.3<br>V) |  |
|    |                                                                                                                 |     | SAI4_SDO_M0          |         |                           |  |
|    |                                                                                                                 |     | PDM1_SDI0_M1         |         |                           |  |
|    |                                                                                                                 |     | SPI4_CSN0_M2         |         |                           |  |
|    |                                                                                                                 |     | SPI3_CSN1_M2         |         |                           |  |
|    |                                                                                                                 |     | PWM2_CH7_M0          |         |                           |  |
|    |                                                                                                                 |     | GPIO4_B3_d           |         |                           |  |
| 69 | SAI1_MCLK_M0/SAI4_M<br>CLK_M0/AUPLL_CLK_IN<br>_M2/PWM2_CH5_M0/GP<br>IO4_A2_d                                    | 1D6 | SAI1_MCLK_M0         | Default | VCCIO2<br>(1.8V/3.3<br>V) |  |
|    |                                                                                                                 |     | SAI4_MCLK_M0         |         |                           |  |
|    |                                                                                                                 |     | AUPLL_CLK_IN_M<br>2  |         |                           |  |
|    |                                                                                                                 |     |                      |         |                           |  |

|    |                                                                                            |      |                            |         |                    |  |
|----|--------------------------------------------------------------------------------------------|------|----------------------------|---------|--------------------|--|
|    |                                                                                            |      | PWM2_CH5_M0                |         |                    |  |
|    |                                                                                            |      | GPIO4_A2_d                 |         |                    |  |
| 70 | SAI1_SCLK_M0/FLEXBUS1_CSN_M4/SPI3_CSN0_M2/UART5_RTSN_M1/I2C2_SCL_M2/PWM2_CH4_M1/GPIO4_A3_d | 1C6  | SAI1_SCLK_M0               | Default | VCCIO2 (1.8V/3.3V) |  |
|    |                                                                                            |      | FLEXBUS1_CSN_M4            |         |                    |  |
|    |                                                                                            |      | SPI3_CSN0_M2               |         |                    |  |
|    |                                                                                            |      | UART5_RTSN_M1              |         |                    |  |
|    |                                                                                            |      | I2C2_SCL_M2                |         |                    |  |
|    |                                                                                            |      | PWM2_CH4_M1                |         |                    |  |
|    |                                                                                            |      | GPIO4_A3_d                 |         |                    |  |
| 71 | VCCIO2                                                                                     | 2A2  | VCCIO2                     |         |                    |  |
| 72 | VCC_1V8_S0                                                                                 | 1J20 | VCC_1V8_S0                 |         |                    |  |
| 73 | VCC_3V3_S0                                                                                 | 2A7  | VCC_3V3_S0                 |         |                    |  |
| 74 | GND30                                                                                      |      | GND30                      |         |                    |  |
| 75 | USB2_HOST1_DM                                                                              | 2T5  | USB2_HOST1_DM              |         |                    |  |
| 77 | USB2_OTG0_DM                                                                               | AL9  | USB2_OTG0_DM               |         |                    |  |
| 78 | USB2_OTG0_DP                                                                               | AK9  | USB2_OTG0_DP               |         |                    |  |
| 79 | USB3_OTG0_SSTX1N/DP_TX_D1N                                                                 | AK11 | USB3_OTG0_SSTX1N/DP_TX_D1N |         |                    |  |
| 80 | USB3_OTG0_SSTX1P/DP_TX_D1P                                                                 | AL11 | USB3_OTG0_SSTX1P/DP_TX_D1P |         |                    |  |



|    |                            |      |                            |  |  |  |
|----|----------------------------|------|----------------------------|--|--|--|
| 81 | USB3_OTG0_SSRX1N/DP_TX_D0N | AL10 | USB3_OTG0_SSRX1N/DP_TX_D0N |  |  |  |
| 82 | USB3_OTG0_SSRX1P/DP_TX_D0P | AK10 | USB3_OTG0_SSRX1P/DP_TX_D0P |  |  |  |
| 83 | USB3_OTG0_SSRX2N/DP_TX_D2N | AL12 | USB3_OTG0_SSRX2N/DP_TX_D2N |  |  |  |
| 84 | USB3_OTG0_SSRX2P/DP_TX_D2P | AK12 | USB3_OTG0_SSRX2P/DP_TX_D2P |  |  |  |
| 85 | USB3_OTG0_SSTX2P/DP_TX_D3P | AL13 | USB3_OTG0_SSTX2P/DP_TX_D3P |  |  |  |
| 86 | USB3_OTG0_SSTX2N/DP_TX_D3N | AK13 | USB3_OTG0_SSTX2N/DP_TX_D3N |  |  |  |
| 87 | GND31                      |      | GND31                      |  |  |  |
| 88 | MIPI_DPHY_DSI_TX_D0P       | AL15 | MIPI_DPHY_DSI_TX_D0P       |  |  |  |
| 89 | MIPI_DPHY_DSI_TX_D0N       | AK15 | MIPI_DPHY_DSI_TX_D0N       |  |  |  |
| 90 | MIPI_DPHY_DSI_TX_D1P       | AL16 | MIPI_DPHY_DSI_TX_D1P       |  |  |  |
| 91 | MIPI_DPHY_DSI_TX_D1N       | AK16 | MIPI_DPHY_DSI_TX_D1N       |  |  |  |
| 92 | MIPI_DPHY_DSI_TX_CLKP      | AL17 | MIPI_DPHY_DSI_TX_CLKP      |  |  |  |
| 93 | MIPI_DPHY_DSI_TX_CLKN      | AK17 | MIPI_DPHY_DSI_TX_CLKN      |  |  |  |
| 94 | MIPI_DPHY_DSI_TX_D2P       | AL18 | MIPI_DPHY_DSI_TX_D2P       |  |  |  |
| 95 | MIPI_DPHY_DSI_TX_D2N       | AK18 | MIPI_DPHY_DSI_TX_D2N       |  |  |  |

|     |                                             |       |                        |  |  |  |
|-----|---------------------------------------------|-------|------------------------|--|--|--|
| 96  | MIPI_DPHY_DSI_TX_D3P                        | AL19  | MIPI_DPHY_DSI_TX_D3P   |  |  |  |
| 97  | MIPI_DPHY_DSI_TX_D3N                        | AK19  | MIPI_DPHY_DSI_TX_D3N   |  |  |  |
| 98  | GND32                                       |       | GND32                  |  |  |  |
| 99  | HDMI_TX_D3N/EDP_TX_D3N                      | AK26  | HDMI_TX_D3N/EDP_TX_D3N |  |  |  |
| 100 | HDMI_TX_D3P/EDP_TX_D3P                      | AL26  | HDMI_TX_D3P/EDP_TX_D3P |  |  |  |
| 101 | GND33                                       |       | GND33                  |  |  |  |
| 102 | HDMI_TX_D0N/EDP_TX_D0N                      | AK27  | HDMI_TX_D0N/EDP_TX_D0N |  |  |  |
| 103 | HDMI_TX_D0P/EDP_TX_D0P                      | 1AE24 | HDMI_TX_D0P/EDP_TX_D0P |  |  |  |
| 104 | HDMI_TX_D1N/EDP_TX_D1N                      | AL28  | HDMI_TX_D1N/EDP_TX_D1N |  |  |  |
| 105 | HDMI_TX_D1P/EDP_TX_D1P                      | AK28  | HDMI_TX_D1P/EDP_TX_D1P |  |  |  |
| 106 | HDMI_TX_D2N/EDP_TX_D2N                      | AK29  | HDMI_TX_D2N/EDP_TX_D2N |  |  |  |
| 107 | HDMI_TX_D2P/EDP_TX_D2P                      | AJ28  | HDMI_TX_D2P/EDP_TX_D2P |  |  |  |
| 108 | GND43                                       |       | GND43                  |  |  |  |
| 109 | MIPI_DPHY_CSI1_RX_CLKP                      | 1AC22 | MIPI_DPHY_CSI1_RX_CLKP |  |  |  |
| 110 | MIPI_DPHY_CSI1_RX_CLKN                      | 1AC23 | MIPI_DPHY_CSI1_RX_CLKN |  |  |  |
| 111 | MIPI_DPHY_CSI1_RX_D3P/MIPI_DPHY_CSI2_RX_D1P | AH29  | MIPI_DPHY_CSI1_RX_D3P  |  |  |  |

|     |                       |      |                 |  |      |  |
|-----|-----------------------|------|-----------------|--|------|--|
|     |                       |      | MIPI_DPHY_CSI2_ |  |      |  |
|     |                       |      | RX_D1P          |  |      |  |
| 112 | MIPI_DPHY_CSI1_RX_D3  | AH28 | MIPI_DPHY_CSI1_ |  |      |  |
|     | N/MIPI_DPHY_CSI2_RX_  |      | RX_D3N          |  |      |  |
|     | D1N                   |      | MIPI_DPHY_CSI2_ |  |      |  |
|     |                       |      | RX_D1N          |  |      |  |
| 113 | MIPI_DPHY_CSI1_RX_D2  | AG29 | MIPI_DPHY_CSI1_ |  |      |  |
|     | P/MIPI_DPHY_CSI2_RX_  |      | RX_D2P          |  |      |  |
|     | D0P                   |      | MIPI_DPHY_CSI2_ |  |      |  |
|     |                       |      | RX_D0P          |  |      |  |
| 114 | MIPI_DPHY_CSI1_RX_D2  | AG28 | MIPI_DPHY_CSI1_ |  |      |  |
|     | N/MIPI_DPHY_CSI2_RX_  |      | RX_D2N          |  |      |  |
|     | D0N                   |      | MIPI_DPHY_CSI2_ |  |      |  |
|     |                       |      | RX_D0N          |  |      |  |
| 115 | MIPI_DPHY_CSI1_RX_D1P | AF29 | MIPI_DPHY_CSI1_ |  |      |  |
|     |                       |      | RX_D1P          |  |      |  |
| 116 | MIPI_DPHY_CSI1_RX_D1  | AF28 | MIPI_DPHY_CSI1_ |  |      |  |
|     | N                     |      | RX_D1N          |  |      |  |
| 117 | MIPI_DPHY_CSI1_RX_D0  | AE29 | MIPI_DPHY_CSI1_ |  |      |  |
|     | P                     |      | RX_D0P          |  |      |  |
| 118 | MIPI_DPHY_CSI1_RX_D0  | AE28 | MIPI_DPHY_CSI1_ |  |      |  |
|     | N                     |      | RX_D0N          |  |      |  |
| 119 | UART0_RX_M0/JTAG_T    | AA28 | UART0_RX_M0     |  | 3.3V |  |
|     | MS_M1/GPIO0_D5_u      |      | JTAG_TMS_M1     |  |      |  |
|     |                       |      | GPIO0_D5_u      |  |      |  |
| 120 | UART0_TX_M0/JTAG_TC   | 1U24 | UART0_TX_M0     |  | 3.3V |  |
|     | K_M1/GPIO0_D4_u       |      | JTAG_TCK_M1     |  |      |  |
|     |                       |      | GPIO0_D4_u      |  |      |  |
| 121 | GND44                 |      | GND44           |  |      |  |

|     |                                          |      |                        |         |  |  |
|-----|------------------------------------------|------|------------------------|---------|--|--|
| 122 | PCIE0_REFCLKN                            | 1N23 | PCIE0_REFCLKN          |         |  |  |
| 123 | PCIE0_REFCLKP                            | 1N22 | PCIE0_REFCLKP          |         |  |  |
| 124 | PCIE0_RXN/SATA0_RXN                      | R29  | PCIE0_RXN              | Default |  |  |
|     |                                          |      | SATA0_RXN              |         |  |  |
| 125 | PCIE0_RXP/SATA0_RXP                      | R28  | PCIE0_RXP              | Default |  |  |
|     |                                          |      | SATA0_RXP              |         |  |  |
| 126 | PCIE0_TXN/SATA0_TXN                      | P28  | PCIE0_TXN              | Default |  |  |
|     |                                          |      | SATA0_TXN              |         |  |  |
| 127 | PCIE0_TXP/SATA0_TXP                      | P29  | PCIE0_TXP              | Default |  |  |
|     |                                          |      | SATA0_TXP              |         |  |  |
| 128 | GND45                                    |      | GND45                  |         |  |  |
| 129 | PCIE1_REFCLKN                            | 1M23 | PCIE1_REFCLKN          |         |  |  |
| 130 | PCIE1_REFCLKP                            | 1L23 | PCIE1_REFCLKP          |         |  |  |
| 131 | USB3_OTG1_SSTXN                          | N29  | USB3_OTG1_SSTX<br>N    |         |  |  |
| 132 | USB3_OTG1_SSTXP                          | N28  | USB3_OTG1_SSTX<br>P    |         |  |  |
| 133 | USB3_OTG1_SSRXN                          | M29  | USB3_OTG1_SSRX<br>N    |         |  |  |
| 134 | USB3_OTG1_SSRXP                          | M28  | USB3_OTG1_SSRX<br>P    |         |  |  |
| 135 | GND46                                    |      | GND46                  |         |  |  |
| 136 | MIPI_DPHY_CSI4_RX_CLKP                   | 1K22 | MIPI_DPHY_CSI4_RX_CLKP |         |  |  |
| 137 | MIPI_DPHY_CSI4_RX_CLKN                   | 1K23 | MIPI_DPHY_CSI4_RX_CLKN |         |  |  |
| 138 | MIPI_DPHY_CSI3_RX_D3P/MIPI_DPHY_CSI4_RX_ | L28  | MIPI_DPHY_CSI3_RX_D3P  |         |  |  |

|     |                                                     |      |                                                        |  |  |  |
|-----|-----------------------------------------------------|------|--------------------------------------------------------|--|--|--|
|     | D1P                                                 |      | MIPI_DPHY_CSI4_<br>RX_D1P                              |  |  |  |
| 139 | MIPI_DPHY_CSI3_RX_D3<br>N/MIPI_DPHY_CSI4_RX_<br>D1N | L29  | MIPI_DPHY_CSI3_<br>RX_D3N<br>MIPI_DPHY_CSI4_<br>RX_D1N |  |  |  |
| 140 | MIPI_DPHY_CSI3_RX_D2<br>P/MIPI_DPHY_CSI4_RX_<br>D0P | K28  | MIPI_DPHY_CSI3_<br>RX_D2P<br>MIPI_DPHY_CSI4_<br>RX_D0P |  |  |  |
| 141 | MIPI_DPHY_CSI3_RX_D2<br>N/MIPI_DPHY_CSI4_RX_<br>D0N | K29  | MIPI_DPHY_CSI3_<br>RX_D2N<br>MIPI_DPHY_CSI4_<br>RX_D0N |  |  |  |
| 142 | MIPI_DPHY_CSI3_RX_CL<br>KP                          | 1H22 | MIPI_DPHY_CSI3_<br>RX_CLKP                             |  |  |  |
| 143 | MIPI_DPHY_CSI3_RX_CL<br>KN                          | 1H23 | MIPI_DPHY_CSI3_<br>RX_CLKN                             |  |  |  |
| 144 | MIPI_DPHY_CSI3_RX_D1<br>P                           | J28  | MIPI_DPHY_CSI3_<br>RX_D1P                              |  |  |  |
| 145 | MIPI_DPHY_CSI3_RX_D1<br>N                           | J29  | MIPI_DPHY_CSI3_<br>RX_D1N                              |  |  |  |
| 146 | MIPI_DPHY_CSI3_RX_D0<br>P                           | H28  | MIPI_DPHY_CSI3_<br>RX_D0P                              |  |  |  |
| 147 | MIPI_DPHY_CSI3_RX_D0<br>N                           | H29  | MIPI_DPHY_CSI3_<br>RX_D0N                              |  |  |  |
| 148 | GND47                                               |      | GND47                                                  |  |  |  |
| 149 | PWRON_L                                             |      | PWRON_L                                                |  |  |  |
| 150 | PMIC_VDC                                            |      | PMIC_VDC                                               |  |  |  |

|     |                                                                                                                           |      |                                                                                                                                                   |         |      |  |
|-----|---------------------------------------------------------------------------------------------------------------------------|------|---------------------------------------------------------------------------------------------------------------------------------------------------|---------|------|--|
| 151 | VCC_1V8_S3                                                                                                                |      | VCC_1V8_S3                                                                                                                                        |         |      |  |
| 152 | VCC_3V3_S3                                                                                                                |      | VCC_3V3_S3                                                                                                                                        |         |      |  |
| A1  | ETH1_PPSCCLK_M1/SDMMC1_PWREN_M0/FSPI1_RTSN_M1/FSPI1_CSN1_M1/UART4_RTSN_M1/I2C6_SCL_M1/SPI2_CSN1_M1/PWM1_CH2_M1/GPIO1_C2_u | B29  | ETH1_PPSCCLK_M1<br>SDMMC1_PWREN_M0<br>FSPI1_RTSN_M1<br>FSPI1_CSN1_M1<br>UART4_RTSN_M1<br>I2C6_SCL_M1<br>SPI2_CSN1_M1<br>PWM1_CH2_M1<br>GPIO1_C2_u | Default | 1.8V |  |
| A2  | ETH1_PPSTRIG_M1/SDMMC1_DET_N0/FSPI1_CSN0_M1/UART4_CTSN_M1/I2C6_SDA_M1/SPI2_CSN0_M1/GPIO1_C3_u                             | 1C23 | ETH1_PPSTRIG_M1<br>SDMMC1_DET_N0<br>FSPI1_CSN0_M1<br>UART4_CTSN_M1<br>I2C6_SDA_M1<br>SPI2_CSN0_M1<br>GPIO1_C3_u                                   | Default | 1.8V |  |
| A3  | ETH1_TXD0_M1/FSPI1_D0_M1/UART4_TX_M1/UART2_RTSN_M0/SPI2_MOSI_M1/PCIE0_BUTTONRS_TN/GPIO1_C4_d                              | 1B23 | ETH1_TXD0_M1<br>FSPI1_D0_M1<br>UART4_TX_M1<br>UART2_RTSN_M0<br>SPI2_MOSI_M1                                                                       | Default | 1.8V |  |

|    |                                                                                                             |      |                      |         |      |  |
|----|-------------------------------------------------------------------------------------------------------------|------|----------------------|---------|------|--|
|    |                                                                                                             |      | PCIE0_BUTTONRS<br>TN |         |      |  |
|    |                                                                                                             |      | GPIO1_C4_d           |         |      |  |
| A4 | ETH1_TXD1_M1/FSPI1_D1<br>_M1/UART4_RX_M1/UAR<br>T2_CTSN_M0/SPI2_MIS<br>O_M1/PCIE1_BUTTONRS<br>TN/GPIO1_C5_d | B28  | ETH1_TXD1_M1         |         | 1.8V |  |
|    |                                                                                                             |      | FSPI1_D1_M1          |         |      |  |
|    |                                                                                                             |      | UART4_RX_M1          |         |      |  |
|    |                                                                                                             |      | UART2_CTSN_M0        | Default |      |  |
|    |                                                                                                             |      | SPI2_MISO_M1         |         |      |  |
|    |                                                                                                             |      | PCIE1_BUTTONRS<br>TN |         |      |  |
|    |                                                                                                             |      | GPIO1_C5_d           |         |      |  |
| A5 | GND4                                                                                                        |      | GND4                 |         |      |  |
| A6 | ETH1_RXD1_M1/SAI2_SD<br>O_M0/UART10_TX_M1/G<br>PIO1_D0_d                                                    | C29  | ETH1_RXD1_M1         |         | 1.8V |  |
|    |                                                                                                             |      | SAI2_SDO_M0          | Default |      |  |
|    |                                                                                                             |      | UART10_TX_M1         |         |      |  |
|    |                                                                                                             |      | GPIO1_D0_d           |         |      |  |
| A7 | ETH1_RXCTL_M1/SAI2_S<br>CLK_M0/UART10_RX_M1<br>/I3C0_SDA_PU_M1/GPIO<br>1_D1_d                               | 1D22 | ETH1_RXCTL_M1        |         | 1.8V |  |
|    |                                                                                                             |      | SAI2_SCLK_M0         | Default |      |  |
|    |                                                                                                             |      | UART10_RX_M1         |         |      |  |
|    |                                                                                                             |      | I3C0_SDA_PU_M1       |         |      |  |
|    |                                                                                                             |      | GPIO1_D1_d           |         |      |  |
| A8 | ETH1_MDC_M1/SAI2_LR<br>CK_M0/I3C0_SCL_M1/P<br>WM1_CH3_M1/GPIO1_D2<br>_d                                     | 1A24 | ETH1_MDC_M1          |         | 1.8V |  |
|    |                                                                                                             |      | SAI2_LRCK_M0         | Default |      |  |
|    |                                                                                                             |      | I3C0_SCL_M1          |         |      |  |
|    |                                                                                                             |      | PWM1_CH3_M1          |         |      |  |
|    |                                                                                                             |      | GPIO1_D2_d           |         |      |  |

|     |                                                                                                                          |      |                     |         |                       |  |
|-----|--------------------------------------------------------------------------------------------------------------------------|------|---------------------|---------|-----------------------|--|
| A9  | ETH1_MDIO_M1/SAI2_SDI_M0/I3C0_SDA_M1/PWM1_CH4_M1/GPIO1_D3_d                                                              | C28  | ETH1_MDIO_M1        |         | 1.8V                  |  |
|     |                                                                                                                          |      | SAI2_SDI_M0         | Default |                       |  |
|     |                                                                                                                          |      | I3C0_SDA_M1         |         |                       |  |
|     |                                                                                                                          |      | PWM1_CH4_M1         |         |                       |  |
|     |                                                                                                                          |      | GPIO1_D3_d          |         |                       |  |
| A10 | ETH_CLK1_25M_OUT_M1/FSPI1_CLK_M1/PDM0_CLK1_M2/SPDIF_TX1_M2/UART10_CTSN_M1/I2C5_SDA_M1/SPI2_CLK_M1/SATA_MPSWIT/GPIO1_D5_d | 1E22 | ETH_CLK1_25M_OUT_M1 |         | 1.8V                  |  |
|     |                                                                                                                          |      | FSPI1_CLK_M1        |         |                       |  |
|     |                                                                                                                          |      | PDM0_CLK1_M2        |         |                       |  |
|     |                                                                                                                          |      | SPDIF_TX1_M2        |         |                       |  |
|     |                                                                                                                          |      | UART10_CTSN_M1      |         |                       |  |
|     |                                                                                                                          |      | I2C5_SDA_M1         |         |                       |  |
|     |                                                                                                                          |      | SPI2_CLK_M1         |         |                       |  |
|     |                                                                                                                          |      | SATA_MPSWIT         |         |                       |  |
|     |                                                                                                                          |      | GPIO1_D5_d          | Default |                       |  |
| A11 | GND5                                                                                                                     |      | GND5                |         |                       |  |
| A12 | VI_CIF_D7/ETH1_PTP_REFCLK_M1/ETH0_RXD3_M1/SAI0_SCLK_M0/UART7_TX_M0/UART8_RTSN_M1/I2C8_SCL_M2/GPIO2_B6_d                  | A21  | VI_CIF_D7           |         | VCCIO4<br>(1.8V/3.3V) |  |
|     |                                                                                                                          |      | ETH1_PTP_REFCLK_M1  |         |                       |  |
|     |                                                                                                                          |      | ETH0_RXD3_M1        |         |                       |  |
|     |                                                                                                                          |      | SAI0_SCLK_M0        |         |                       |  |
|     |                                                                                                                          |      | UART7_TX_M0         | Default |                       |  |
|     |                                                                                                                          |      | UART8_RTSN_M1       |         |                       |  |
|     |                                                                                                                          |      | I2C8_SCL_M2         |         |                       |  |
|     |                                                                                                                          |      | GPIO2_B6_d          |         |                       |  |
| A13 | VI_CIF_D6/ETH0_RXD2_                                                                                                     | B21  | VI_CIF_D6           |         | VCCIO4                |  |



|     |                                                                                                       |      |                 |         |                       |  |
|-----|-------------------------------------------------------------------------------------------------------|------|-----------------|---------|-----------------------|--|
|     | M1/SAI0_LRCK_M0/UART7_RX_M0/UART8_CTSN_M1/I2C8_SDA_M2/GPIO2_B7_d                                      |      | ETH0_RXD2_M1    |         | (1.8V/3.3V)           |  |
|     |                                                                                                       |      | SAI0_LRCK_M0    |         |                       |  |
|     |                                                                                                       |      | UART7_RX_M0     | Default |                       |  |
|     |                                                                                                       |      | UART8_CTSN_M1   |         |                       |  |
|     |                                                                                                       |      | I2C8_SDA_M2     |         |                       |  |
|     |                                                                                                       |      | GPIO2_B7_d      |         |                       |  |
| A14 | VI_CIF_HREF/ETH0_MDIO_M1/SAI3_SCLK_M2/UART3_TX_M0/SPI3_CLK_M0/I2C7_SCL_M1/GPIO3_A0_d                  | 1D16 | VI_CIF_HREF     |         | VCCIO4<br>(1.8V/3.3V) |  |
|     |                                                                                                       |      | ETH0_MDIO_M1    |         |                       |  |
|     |                                                                                                       |      | SAI3_SCLK_M2    |         |                       |  |
|     |                                                                                                       |      | UART3_TX_M0     | Default |                       |  |
|     |                                                                                                       |      | SPI3_CLK_M0     |         |                       |  |
|     |                                                                                                       |      | I2C7_SCL_M1     |         |                       |  |
|     |                                                                                                       |      | GPIO3_A0_d      |         |                       |  |
| A15 | VI_CIF_VSYNC/ETH1_PPSTRIG_M0/ETH0_MDC_M1/SAI3_LRCK_M2/UART3_RX_M0/SPI3_MOSI_M0/I2C7_SDA_M1/GPIO3_A1_d | 1B18 | VI_CIF_VSYNC    |         | VCCIO4<br>(1.8V/3.3V) |  |
|     |                                                                                                       |      | ETH1_PPSTRIG_M0 |         |                       |  |
|     |                                                                                                       |      | ETH0_MDC_M1     |         |                       |  |
|     |                                                                                                       |      | SAI3_LRCK_M2    |         |                       |  |
|     |                                                                                                       |      | UART3_RX_M0     | Default |                       |  |
|     |                                                                                                       |      | SPI3_MOSI_M0    |         |                       |  |
|     |                                                                                                       |      | I2C7_SDA_M1     |         |                       |  |
|     |                                                                                                       |      | GPIO3_A1_d      |         |                       |  |
| A16 | GND6                                                                                                  |      | GND6            |         |                       |  |
| A17 | VI_CIF_CLKO/ETH1_PPSCLK_M0/ETH0_RXCTL_M1/SAI3_SDO_M2/SPDIF_RX1_M1/UART3_CTSN_M1                       | 1A20 | VI_CIF_CLKO     |         | VCCIO4<br>(1.8V/3.3V) |  |
|     |                                                                                                       |      | ETH1_PPSCCLK_M0 |         |                       |  |
|     |                                                                                                       |      | ETH0_RXCTL_M1   |         |                       |  |

|     |                                                                                                                         |      |                    |         |                       |  |
|-----|-------------------------------------------------------------------------------------------------------------------------|------|--------------------|---------|-----------------------|--|
|     | M0/SPI3_MISO_M0/CAN1_TX_M3/MIPI_TE_M1/GPIO3_A2_d                                                                        |      | SAI3_SDO_M2        |         |                       |  |
|     |                                                                                                                         |      | SPDIF_RX1_M1       |         |                       |  |
|     |                                                                                                                         |      | UART3_CTSN_M0      |         |                       |  |
|     |                                                                                                                         |      | SPI3_MISO_M0       |         |                       |  |
|     |                                                                                                                         |      | CAN1_TX_M3         | Default |                       |  |
|     |                                                                                                                         |      | MIPI_TE_M1         |         |                       |  |
|     |                                                                                                                         |      | GPIO3_A2_d         |         |                       |  |
| A18 | VI_CIF_D8/SDMMC1_DET_N_M1/ETH0_RXCLK_M1/SAI0_MCLK_M0/PDM0_CLK0_M3/UART7_RTSN_M0/SPI4_MISO_M3/SATA1_ACTLED_M0/GPIO2_B5_d | 1C18 | VI_CIF_D8          |         | VCCIO4<br>(1.8V/3.3V) |  |
|     |                                                                                                                         |      | SDMMC1_DET_N_M1    |         |                       |  |
|     |                                                                                                                         |      | ETH0_RXCLK_M1      |         |                       |  |
|     |                                                                                                                         |      | SAI0_MCLK_M0       |         |                       |  |
|     |                                                                                                                         |      | PDM0_CLK0_M3       |         |                       |  |
|     |                                                                                                                         |      | UART7_RTSN_M0      | Default |                       |  |
|     |                                                                                                                         |      | SPI4_MISO_M3       |         |                       |  |
|     |                                                                                                                         |      | SATA1_ACTLED_M0    |         |                       |  |
|     |                                                                                                                         |      | GPIO2_B5_d         |         |                       |  |
| A19 | VI_CIF_CLKI/ETH1_PTP_REFCLK_M0/ETH0_RXD1_M1/SAI3_SDI_M2/SPDIF_TX1_M1/UART3_RTSN_M0/SPI3_CSN0_M0/CAN1_RX_M3/GPIO3_A3_d   | 1A19 | VI_CIF_CLKI        |         | VCCIO4<br>(1.8V/3.3V) |  |
|     |                                                                                                                         |      | ETH1_PTP_REFCLK_M0 |         |                       |  |
|     |                                                                                                                         |      | ETH0_RXD1_M1       |         |                       |  |
|     |                                                                                                                         |      | SAI3_SDI_M2        |         |                       |  |
|     |                                                                                                                         |      | SPDIF_TX1_M1       |         |                       |  |
|     |                                                                                                                         |      | UART3_RTSN_M0      |         |                       |  |
|     |                                                                                                                         |      | SPI3_CSN0_M0       |         |                       |  |
|     |                                                                                                                         |      |                    |         |                       |  |

|    |                                                                                                 |      |                 |         |      |  |
|----|-------------------------------------------------------------------------------------------------|------|-----------------|---------|------|--|
|    |                                                                                                 |      | CAN1_RX_M3      | Default |      |  |
|    |                                                                                                 |      | GPIO3_A3_d      |         |      |  |
| B1 | GND7                                                                                            |      | GND7            |         |      |  |
| B2 | PMIC_EXT_EN_OUT                                                                                 |      | PMIC_EXT_EN_OUT |         |      |  |
| B3 | RESET_L                                                                                         |      | RESET_L         |         |      |  |
| B4 | GND8                                                                                            |      | GND8            |         |      |  |
| B5 | ETH1_TXCTL_M1/FSPI1_D2_M1/PDM0_SDI0_M2/UART2_TX_M0/I2C8_SCL_M1/SATA_CPPOD/GPIO1_C6_d            | A26  | ETH1_TXCTL_M1   |         | 1.8V |  |
|    |                                                                                                 |      | FSPI1_D2_M1     |         |      |  |
|    |                                                                                                 |      | PDM0_SDI0_M2    |         |      |  |
|    |                                                                                                 |      | UART2_TX_M0     | Default |      |  |
|    |                                                                                                 |      | I2C8_SCL_M1     |         |      |  |
|    |                                                                                                 |      | SATA_CPPOD      |         |      |  |
|    |                                                                                                 |      | GPIO1_C6_d      |         |      |  |
| B6 | ETH1_RXD0_M1/FSPI1_D3_M1/PDM0_SDI1_M2/UART2_RX_M0/I2C8_SDA_M1/SATA_CPDET/BT_REG_ON_H/GPIO1_C7_d | 1C22 | ETH1_RXD0_M1    |         | 1.8V |  |
|    |                                                                                                 |      | FSPI1_D3_M1     |         |      |  |
|    |                                                                                                 |      | PDM0_SDI1_M2    |         |      |  |
|    |                                                                                                 |      | UART2_RX_M0     | Default |      |  |
|    |                                                                                                 |      | I2C8_SDA_M1     |         |      |  |
|    |                                                                                                 |      | SATA_CPDET      |         |      |  |
|    |                                                                                                 |      | BT_REG_ON_H     |         |      |  |
|    |                                                                                                 |      | GPIO1_C7_d      |         |      |  |
| B7 | ETH1_MCLK_M1/SAI2_MCLK_M0/PDM0_SDI3_M2/SPDIF_RX1_M2/UART10_RTSN_M1/I2C5_SCL_M1/GPIO1_D4_d       | 1E21 | ETH1_MCLK_M1    |         | 1.8V |  |
|    |                                                                                                 |      | SAI2_MCLK_M0    |         |      |  |
|    |                                                                                                 |      | PDM0_SDI3_M2    |         |      |  |
|    |                                                                                                 |      | SPDIF_RX1_M2    |         |      |  |

|     |                                                                                                   |     |                |         |                       |  |
|-----|---------------------------------------------------------------------------------------------------|-----|----------------|---------|-----------------------|--|
|     |                                                                                                   |     | UART10_RTSN_M1 |         |                       |  |
|     |                                                                                                   |     | I2C5_SCL_M1    |         |                       |  |
|     |                                                                                                   |     | GPIO1_D4_d     | Default |                       |  |
| B8  | GND9                                                                                              |     | GND9           |         |                       |  |
| B9  | VI_CIF_D15/SDMMC1_D0_M1/ETH0_RXD0_M1/SAI0_SDO0_M0/UART8_TX_M1/SPI4_CSN1_M3/I2C4_SCL_M2/GPIO2_A6_d | B22 | VI_CIF_D15     |         | VCCIO4<br>(1.8V/3.3V) |  |
|     |                                                                                                   |     | SDMMC1_D0_M1   |         |                       |  |
|     |                                                                                                   |     | ETH0_RXD0_M1   |         |                       |  |
|     |                                                                                                   |     | SAI0_SDO0_M0   |         |                       |  |
|     |                                                                                                   |     | UART8_TX_M1    | Default |                       |  |
|     |                                                                                                   |     | SPI4_CSN1_M3   |         |                       |  |
|     |                                                                                                   |     | I2C4_SCL_M2    |         |                       |  |
|     |                                                                                                   |     | GPIO2_A6_d     |         |                       |  |
| B10 | VI_CIF_D14/SDMMC1_D1_M1/ETH0_TXCTL_M1/SAI0_SDO1_M0/UART8_RX_M1/I2C4_SDA_M2/GPIO2_A7_d             | B20 | VI_CIF_D14     |         | VCCIO4<br>(1.8V/3.3V) |  |
|     |                                                                                                   |     | SDMMC1_D1_M1   |         |                       |  |
|     |                                                                                                   |     | ETH0_TXCTL_M1  |         |                       |  |
|     |                                                                                                   |     | SAI0_SDO1_M0   |         |                       |  |
|     |                                                                                                   |     | UART8_RX_M1    | Default |                       |  |
|     |                                                                                                   |     | I2C4_SDA_M2    |         |                       |  |
| B11 | VI_CIF_D13/SDMMC1_D2_M1/ETH0_TXD1_M1/SAI0_SDI0_M0/PDM0_SDI3_M3/UART1_TX_M1/GPIO2_B0_d             | B19 | VI_CIF_D13     |         | VCCIO4<br>(1.8V/3.3V) |  |
|     |                                                                                                   |     | SDMMC1_D2_M1   |         |                       |  |
|     |                                                                                                   |     | ETH0_TXD1_M1   |         |                       |  |
|     |                                                                                                   |     | SAI0_SDI0_M0   |         |                       |  |
|     |                                                                                                   |     | PDM0_SDI3_M3   |         |                       |  |
|     |                                                                                                   |     | UART1_TX_M1    | Default |                       |  |

|     |                                                                                                                        |      |                  |         |                       |  |
|-----|------------------------------------------------------------------------------------------------------------------------|------|------------------|---------|-----------------------|--|
|     |                                                                                                                        |      | GPIO2_B0_d       |         |                       |  |
| B12 | VI_CIF_D12/SDMMC1_D3_M1/ETH0_TXD0_M1/SAI0_SDI1_M0/PDM0_SDI2_M3/UART1_RX_M1/GPIO2_B1_d                                  | 1A18 | VI_CIF_D12       |         | VCCIO4<br>(1.8V/3.3V) |  |
|     |                                                                                                                        |      | SDMMC1_D3_M1     |         |                       |  |
|     |                                                                                                                        |      | ETH0_TXD0_M1     |         |                       |  |
|     |                                                                                                                        |      | SAI0_SDI1_M0     |         |                       |  |
|     |                                                                                                                        |      | PDM0_SDI2_M3     |         |                       |  |
|     |                                                                                                                        |      | UART1_RX_M1      | Default |                       |  |
|     |                                                                                                                        |      | GPIO2_B1_d       |         |                       |  |
| B13 | GND10                                                                                                                  |      | GND10            |         |                       |  |
| B14 | VI_CIF_D11/SDMMC1_CMD_M1/ETH0_TXD3_M1/SAI0_SDI2_M0/PDM0_SDI1_M3/UART1_CTSN_M1/SPI4_CSN0_M3/PCIE0_CLKREQN_M0/GPIO2_B2_d | 1A17 | VI_CIF_D11       |         | VCCIO4<br>(1.8V/3.3V) |  |
|     |                                                                                                                        |      | SDMMC1_CMD_M1    |         |                       |  |
|     |                                                                                                                        |      | ETH0_TXD3_M1     |         |                       |  |
|     |                                                                                                                        |      | SAI0_SDI2_M0     |         |                       |  |
|     |                                                                                                                        |      | PDM0_SDI1_M3     |         |                       |  |
|     |                                                                                                                        |      | UART1_CTSN_M1    |         |                       |  |
|     |                                                                                                                        |      | SPI4_CSN0_M3     |         |                       |  |
|     |                                                                                                                        |      | PCIE0_CLKREQN_M0 |         |                       |  |
|     |                                                                                                                        |      | GPIO2_B2_d       | Default |                       |  |
| B15 | VI_CIF_D10/SDMMC1_CLK_M1/ETH0_TXCLK_M1/SAI0_SDO2_M0/PDM0_CLK1_M3/UART1_RTSN_M1/SPI4_CLK_M3/PCIE1_CLKREQN_M0/GPIO2_B3_d | 1B16 | VI_CIF_D10       |         | VCCIO4<br>(1.8V/3.3V) |  |
|     |                                                                                                                        |      | SDMMC1_CLK_M1    |         |                       |  |
|     |                                                                                                                        |      | ETH0_TXCLK_M1    |         |                       |  |
|     |                                                                                                                        |      | SAI0_SDO2_M0     |         |                       |  |
|     |                                                                                                                        |      | PDM0_CLK1_M3     |         |                       |  |
|     |                                                                                                                        |      | UART1_RTSN_M1    | Default |                       |  |
|     |                                                                                                                        |      | SPI4_CLK_M3      |         |                       |  |

|     |                                                                                                                                |      |                     |         |                    |  |
|-----|--------------------------------------------------------------------------------------------------------------------------------|------|---------------------|---------|--------------------|--|
|     |                                                                                                                                |      | PCIE1_CLKREQN_M0    |         |                    |  |
|     |                                                                                                                                |      | GPIO2_B3_d          |         |                    |  |
| B16 | VI_CIF_D9/SDMMC1_PWREN_M1/ETH0_TXD2_M1/SAI0_SDI3_M0/PDM0_SDI0_M3/UART7_CTSN_M0/SPI4_MOSI_M3/SATA0_ACTLED_M0/GPIO2_B4_d         | A19  | VI_CIF_D9           |         | VCCIO4 (1.8V/3.3V) |  |
|     |                                                                                                                                |      | SDMMC1_PWREN_M1     |         |                    |  |
|     |                                                                                                                                |      | ETH0_TXD2_M1        |         |                    |  |
|     |                                                                                                                                |      | SAI0_SDI3_M0        |         |                    |  |
|     |                                                                                                                                |      | PDM0_SDI0_M3        |         |                    |  |
|     |                                                                                                                                |      | UART7_CTSN_M0       |         |                    |  |
|     |                                                                                                                                |      | SPI4_MOSI_M3        |         |                    |  |
|     |                                                                                                                                |      | SATA0_ACTLED_M0     |         |                    |  |
|     |                                                                                                                                |      | GPIO2_B4_d          | Default |                    |  |
| B17 | CAM_CLK1_OUT_M1/ETH_CLK1_25M_OUT_M0/ETH0_MCLK_M1/SAI3_MCLK_M2/SPDIF_RX0_M2/UART9_RTSN_M0/I3C1_SDA_PU_M0/PWM2_CH6_M2/GPIO2_D6_d | 1D18 | CAM_CLK1_OUT_M1     |         | VCCIO4 (1.8V/3.3V) |  |
|     |                                                                                                                                |      | ETH_CLK1_25M_OUT_M0 |         |                    |  |
|     |                                                                                                                                |      | ETH0_MCLK_M1        |         |                    |  |
|     |                                                                                                                                |      | SAI3_MCLK_M2        |         |                    |  |
|     |                                                                                                                                |      | SPDIF_RX0_M2        |         |                    |  |
|     |                                                                                                                                |      | UART9_RTSN_M0       |         |                    |  |
|     |                                                                                                                                |      | I3C1_SDA_PU_M0      |         |                    |  |
|     |                                                                                                                                |      | PWM2_CH6_M2         |         |                    |  |
|     |                                                                                                                                |      | GPIO2_D6_d          | Default |                    |  |
| B18 | CAM_CLK2_OUT_M1/ETH1_MCLK_M0/ETH_CLK0_25M_OUT_M1/SAI0_S                                                                        | 1E15 | CAM_CLK2_OUT_M1     |         | VCCIO4 (1.8V/3.3V) |  |
|     |                                                                                                                                |      |                     |         |                    |  |

|    |                                                                                                                  |      |                         |         |                       |  |
|----|------------------------------------------------------------------------------------------------------------------|------|-------------------------|---------|-----------------------|--|
|    | DO3_M0/SPDIF_TX0_M2<br>/UART9_CTSN_M0/SPI3_CSN1_M0/PWM2_CH7_M2/GPIO2_D7_d                                        |      | ETH1_MCLK_M0            |         |                       |  |
|    |                                                                                                                  |      | ETH_CLK0_25M_O<br>UT_M1 | Default |                       |  |
|    |                                                                                                                  |      | SAI0_SDO3_M0            |         |                       |  |
|    |                                                                                                                  |      | SPDIF_TX0_M2            |         |                       |  |
|    |                                                                                                                  |      | UART9_CTSN_M0           |         |                       |  |
|    |                                                                                                                  |      | SPI3_CSN1_M0            |         |                       |  |
|    |                                                                                                                  |      | PWM2_CH7_M2             |         |                       |  |
|    |                                                                                                                  |      | GPIO2_D7_d              |         |                       |  |
| C1 | GND16                                                                                                            |      | GND16                   |         |                       |  |
| C2 | VO_LCDC_D4/VO_EBC_SDDO4/SAI1_SCLK_M1/DSMC_DATA2/FLEXBUS1_D4/UART8_RTSN_M0/SPI1_CLK_M2/GPIO3_C7_d                 | 1C7  | VO_LCDC_D4              |         | VCCIO5<br>(1.8V/3.3V) |  |
|    |                                                                                                                  |      | VO_EBC_SDDO4            |         |                       |  |
|    |                                                                                                                  |      | SAI1_SCLK_M1            | Default |                       |  |
|    |                                                                                                                  |      | DSMC_DATA2              |         |                       |  |
|    |                                                                                                                  |      | FLEXBUS1_D4             |         |                       |  |
|    |                                                                                                                  |      | UART8_RTSN_M0           |         |                       |  |
|    |                                                                                                                  |      | SPI1_CLK_M2             |         |                       |  |
|    |                                                                                                                  |      | GPIO3_C7_d              |         |                       |  |
| C3 | VO_LCDC_VSYNC/VO_EBC_SDCLK/SAI1_SDI3_M1/DSMC_CLKN/FLEXBUS1_CLK/UART5_CTSN_M0/SPI3_MOSI_M1/PWM2_CH6_M3/GPIO3_D6_d | 1C10 | VO_LCDC_VSYNC           |         | VCCIO5<br>(1.8V/3.3V) |  |
|    |                                                                                                                  |      | VO_EBC_SDCLK            |         |                       |  |
|    |                                                                                                                  |      | SAI1_SDI3_M1            |         |                       |  |
|    |                                                                                                                  |      | DSMC_CLKN               |         |                       |  |
|    |                                                                                                                  |      | FLEXBUS1_CLK            |         |                       |  |
|    |                                                                                                                  |      | UART5_CTSN_M0           |         |                       |  |
|    |                                                                                                                  |      | SPI3_MOSI_M1            |         |                       |  |
|    |                                                                                                                  |      | PWM2_CH6_M3             |         |                       |  |

|    |                                                                                                                        |     |                 |         |                       |  |
|----|------------------------------------------------------------------------------------------------------------------------|-----|-----------------|---------|-----------------------|--|
|    |                                                                                                                        |     | GPIO3_D6_d      | Default |                       |  |
| C4 | GND17                                                                                                                  |     | GND17           |         |                       |  |
| C5 | SAI1_SDO1_M0/SAI1_SDI3_M0/PDM1_CLK1_M1/FLEXBUS1_D13_M1/SPI4_CLK_M2/UART5_TX_M1/UART6_RTSN_M0/UART2_RTSN_M1/GPIO4_B0_d  | 1A5 | SAI1_SDO1_M0    |         | VCCIO2<br>(1.8V/3.3V) |  |
|    |                                                                                                                        |     | SAI1_SDI3_M0    |         |                       |  |
|    |                                                                                                                        |     | PDM1_CLK1_M1    | Default |                       |  |
|    |                                                                                                                        |     | FLEXBUS1_D13_M1 |         |                       |  |
|    |                                                                                                                        |     | SPI4_CLK_M2     |         |                       |  |
|    |                                                                                                                        |     | UART5_TX_M1     |         |                       |  |
|    |                                                                                                                        |     | UART6_RTSN_M0   |         |                       |  |
|    |                                                                                                                        |     | UART2_RTSN_M1   |         |                       |  |
|    |                                                                                                                        |     | GPIO4_B0_d      |         |                       |  |
| C6 | SAI1_SDO2_M0/SAI1_SDI2_M0/PDM1_SDI2_M1/FLEXBUS1_D14_M1/SPI4_MOSI_M2/UART5_RX_M1/UART6_CTSN_M0/UART2_CTSN_M1/GPIO4_B1_d | B7  | SAI1_SDO2_M0    |         | VCCIO2<br>(1.8V/3.3V) |  |
|    |                                                                                                                        |     | SAI1_SDI2_M0    |         |                       |  |
|    |                                                                                                                        |     | PDM1_SDI2_M1    | Default |                       |  |
|    |                                                                                                                        |     | FLEXBUS1_D14_M1 |         |                       |  |
|    |                                                                                                                        |     | SPI4_MOSI_M2    |         |                       |  |
|    |                                                                                                                        |     | UART5_RX_M1     |         |                       |  |
|    |                                                                                                                        |     | UART6_CTSN_M0   |         |                       |  |
|    |                                                                                                                        |     | UART2_CTSN_M1   |         |                       |  |
|    |                                                                                                                        |     | GPIO4_B1_d      |         |                       |  |
| C7 | SAI1_SDO3_M0/SAI1_SDI1_M0/PDM1_SDI1_M1/FLEXBUS1_D15_M1/SPI4_MISO_M2/MIPI_TE_M0/GPIO4_B2_d                              | B6  | SAI1_SDO3_M0    |         | VCCIO2<br>(1.8V/3.3V) |  |
|    |                                                                                                                        |     | SAI1_SDI1_M0    |         |                       |  |
|    |                                                                                                                        |     | PDM1_SDI1_M1    | Default |                       |  |
|    |                                                                                                                        |     | FLEXBUS1_D15_M1 |         |                       |  |
|    |                                                                                                                        |     | SPI4_MISO_M2    |         |                       |  |
|    |                                                                                                                        |     |                 |         |                       |  |



|     |                                                                                                                          |      |                     |         |                           |  |
|-----|--------------------------------------------------------------------------------------------------------------------------|------|---------------------|---------|---------------------------|--|
|     |                                                                                                                          |      | MIPI_TE_M0          |         |                           |  |
|     |                                                                                                                          |      | GPIO4_B2_d          |         |                           |  |
| C8  | SAI4_SCLK_M0/PDM1_S<br>DI3_M1/FLEXBUS0_D13_<br>M1/SPI3_MOSI_M2/UAR<br>T6_TX_M0/I2C4_SCL_M1<br>/CAN0_TX_M2/GPIO4_A<br>4_d | 1C5  | SAI4_SCLK_M0        |         | VCCIO2<br>(1.8V/3.3<br>V) |  |
|     |                                                                                                                          |      | PDM1_SDI3_M1        |         |                           |  |
|     |                                                                                                                          |      | FLEXBUS0_D13_M<br>1 |         |                           |  |
|     |                                                                                                                          |      | SPI3_MOSI_M2        |         |                           |  |
|     |                                                                                                                          |      | UART6_TX_M0         |         |                           |  |
|     |                                                                                                                          |      | I2C4_SCL_M1         |         |                           |  |
|     |                                                                                                                          |      | CAN0_TX_M2          | Default |                           |  |
|     |                                                                                                                          |      | GPIO4_A4_d          |         |                           |  |
| C9  | SAI4_LRCK_M0/PDM1_C<br>LK0_M1/FLEXBUS0_D14_<br>M1/SPI3_MISO_M2/UAR<br>T6_RX_M0/I2C4_SDA_M1<br>/CAN0_RX_M2/GPIO4_A<br>6_d | 1B5  | SAI4_LRCK_M0        |         | VCCIO2<br>(1.8V/3.3<br>V) |  |
|     |                                                                                                                          |      | PDM1_CLK0_M1        |         |                           |  |
|     |                                                                                                                          |      | FLEXBUS0_D14_M<br>1 |         |                           |  |
|     |                                                                                                                          |      | SPI3_MISO_M2        |         |                           |  |
|     |                                                                                                                          |      | UART6_RX_M0         |         |                           |  |
|     |                                                                                                                          |      | I2C4_SDA_M1         |         |                           |  |
|     |                                                                                                                          |      | CAN0_RX_M2          | Default |                           |  |
|     |                                                                                                                          |      | GPIO4_A6_d          |         |                           |  |
| C10 | GND18                                                                                                                    |      | GND18               |         |                           |  |
| C11 | UFS_REFCLK/GPIO4_D1_<br>d                                                                                                | AK4  | UFS_REFCLK          | Default | 1.2V                      |  |
|     |                                                                                                                          |      | GPIO4_D1_d          |         |                           |  |
| C12 | UFS_TX_D0P                                                                                                               | 1AD6 | UFS_TX_D0P          |         |                           |  |
| C13 | UFS_TX_D0N                                                                                                               | 1AC6 | UFS_TX_D0N          |         |                           |  |
| C14 | UFS_RX_D0P                                                                                                               | AK8  | UFS_RX_D0P          |         |                           |  |

|     |                                                                                                               |      |               |         |                       |  |
|-----|---------------------------------------------------------------------------------------------------------------|------|---------------|---------|-----------------------|--|
| C15 | UFS_RX_D0N                                                                                                    | AL7  | UFS_RX_D0N    |         |                       |  |
| C16 | UFS_TX_D1P                                                                                                    | 1AD4 | UFS_TX_D1P    |         |                       |  |
| C17 | UFS_TX_D1N                                                                                                    | 1AD5 | UFS_TX_D1N    |         |                       |  |
| C18 | UFS_RX_D1P                                                                                                    | AK7  | UFS_RX_D1P    |         |                       |  |
| C19 | UFS_RX_D1N                                                                                                    | AL6  | UFS_RX_D1N    |         |                       |  |
| D1  | GND19                                                                                                         |      | GND19         |         |                       |  |
| D2  | VO_LCDC_D3/VO_EBC_SDDO3/SAI1_MCLK_M1/DSMC_DATA1/FLEXBUS1_D3/UART8_CTSN_M0/SPI1_CSN0_M2/PWM2_CH3_M3/GPIO3_D0_d | 1C12 | VO_LCDC_D3    |         | VCCIO5<br>(1.8V/3.3V) |  |
|     |                                                                                                               |      | VO_EBC_SDDO3  |         |                       |  |
|     |                                                                                                               |      | SAI1_MCLK_M1  |         |                       |  |
|     |                                                                                                               |      | DSMC_DATA1    |         |                       |  |
|     |                                                                                                               |      | FLEXBUS1_D3   |         |                       |  |
|     |                                                                                                               |      | UART8_CTSN_M0 |         |                       |  |
|     |                                                                                                               |      | SPI1_CSN0_M2  | Default |                       |  |
|     |                                                                                                               |      | PWM2_CH3_M3   |         |                       |  |
| D3  | VO_LCDC_DEN/VO_EBC_SDLE/SAI1_SDI1_M1/DSMC_DATA0/FLEXBUS1_D1/UART5_RX_M0/SPI3_CLK_M1/I2C3_SCL_M2/GPIO3_D4_d    | 1E12 | GPIO3_D0_d    |         | VCCIO5<br>(1.8V/3.3V) |  |
|     |                                                                                                               |      | VO_LCDC_DEN   |         |                       |  |
|     |                                                                                                               |      | VO_EBC_SDLE   |         |                       |  |
|     |                                                                                                               |      | SAI1_SDI1_M1  |         |                       |  |
|     |                                                                                                               |      | DSMC_DATA0    |         |                       |  |
|     |                                                                                                               |      | FLEXBUS1_D1   |         |                       |  |
|     |                                                                                                               |      | UART5_RX_M0   |         |                       |  |
|     |                                                                                                               |      | SPI3_CLK_M1   |         |                       |  |
|     |                                                                                                               |      | I2C3_SCL_M2   |         |                       |  |
| D4  | VO_LCDC_HSYNC/VO_EBC_GDCLK/SAI1_SDI2_M                                                                        | 1D10 | GPIO3_D4_d    | Default | VCCIO5<br>(1.8V/3.3V) |  |
|     |                                                                                                               |      | VO_LCDC_HSYNC |         |                       |  |

|    |                                                                                                             |     |              |         |                       |  |
|----|-------------------------------------------------------------------------------------------------------------|-----|--------------|---------|-----------------------|--|
|    | 1/DSMC_CLKP/FLEXBUS1_D0/UART5_TX_M0/SPI3_MISO_M1/I2C3_SDA_M2/GPIO3_D5_d                                     |     | VO_EBC_GDCLK |         | V)                    |  |
|    |                                                                                                             |     | SAI1_SDI2_M1 |         |                       |  |
|    |                                                                                                             |     | DSMC_CLKP    |         |                       |  |
|    |                                                                                                             |     | FLEXBUS1_D0  |         |                       |  |
|    |                                                                                                             |     | UART5_TX_M0  |         |                       |  |
|    |                                                                                                             |     | SPI3_MISO_M1 |         |                       |  |
|    |                                                                                                             |     | I2C3_SDA_M2  |         |                       |  |
|    |                                                                                                             |     | GPIO3_D5_d   | Default |                       |  |
| D5 | VO_LCDC_D6/VO_EBC_SDDO6/SAI1_SDO0_M1/DSMC_DATA4/FLEXBUS1_D6/UART8_RX_M0/SPI1_MISO_M2/PWM2_CH2_M3/GPIO3_C5_d | 1B9 | VO_LCDC_D6   |         | VCCIO5<br>(1.8V/3.3V) |  |
|    |                                                                                                             |     | VO_EBC_SDDO6 |         |                       |  |
|    |                                                                                                             |     | SAI1_SDO0_M1 |         |                       |  |
|    |                                                                                                             |     | DSMC_DATA4   |         |                       |  |
|    |                                                                                                             |     | FLEXBUS1_D6  |         |                       |  |
|    |                                                                                                             |     | UART8_RX_M0  |         |                       |  |
|    |                                                                                                             |     | SPI1_MISO_M2 | Default |                       |  |
|    |                                                                                                             |     | PWM2_CH2_M3  |         |                       |  |
|    |                                                                                                             |     | GPIO3_C5_d   |         |                       |  |
| D6 | VO_LCDC_D5/VO_EBC_SDDO5/SAI1_LRCK_M1/DSMC_DATA3/FLEXBUS1_D5/UART8_TX_M0/SPI1_MOSI_M2/GPIO3_C6_d             | 1D7 | VO_LCDC_D5   |         | VCCIO5<br>(1.8V/3.3V) |  |
|    |                                                                                                             |     | VO_EBC_SDDO5 |         |                       |  |
|    |                                                                                                             |     | SAI1_LRCK_M1 |         |                       |  |
|    |                                                                                                             |     | DSMC_DATA3   |         |                       |  |
|    |                                                                                                             |     | FLEXBUS1_D5  |         |                       |  |
|    |                                                                                                             |     | UART8_TX_M0  |         |                       |  |
|    |                                                                                                             |     | SPI1_MOSI_M2 | Default |                       |  |
|    |                                                                                                             |     | GPIO3_C6_d   |         |                       |  |

|     |                                                                                     |      |               |         |      |  |
|-----|-------------------------------------------------------------------------------------|------|---------------|---------|------|--|
| D7  | GND20                                                                               |      | GND20         |         |      |  |
| D8  | UFS_1V8_EN                                                                          |      | UFS_1V8_EN    |         | 1.8V |  |
| D9  | UFS_RSTn                                                                            | 1AD2 | UFS_RSTn      |         | 1.2V |  |
| D10 | GND21                                                                               |      | GND21         |         |      |  |
| D11 | GND22                                                                               |      | GND22         |         |      |  |
| D12 | GND23                                                                               |      | GND23         |         |      |  |
| D13 | GND24                                                                               |      | GND24         |         |      |  |
| D14 | GND25                                                                               |      | GND25         |         |      |  |
| D15 | GND26                                                                               |      | GND26         |         |      |  |
| D16 | GND27                                                                               |      | GND27         |         |      |  |
| D17 | GND28                                                                               |      | GND28         |         |      |  |
| D18 | GND29                                                                               |      | GND29         |         |      |  |
| E1  | DSM_AUD_RN_M1/HDMI_TX_SDA/I2C2_SDA_M3/CAN0_RX_M1/UART9_RX_M2/PWM2_CH1_M1/GPIO4_C3_d | 1AE2 | DSM_AUD_RN_M1 |         | 3.3V |  |
|     |                                                                                     |      | HDMI_TX_SDA   | Default |      |  |
|     |                                                                                     |      | I2C2_SDA_M3   |         |      |  |
|     |                                                                                     |      | CAN0_RX_M1    |         |      |  |
|     |                                                                                     |      | UART9_RX_M2   |         |      |  |
|     |                                                                                     |      | PWM2_CH1_M1   |         |      |  |
|     |                                                                                     |      | GPIO4_C3_d    |         |      |  |
| E2  | DSM_AUD_RP_M1/HDMI_TX_SCL/I2C2_SCL_M3/CAN0_TX_M1/UART9_TX_M2/PWM2_CH0_M1/GPIO4_C2_d | AL2  | DSM_AUD_RP_M1 |         | 3.3V |  |
|     |                                                                                     |      | HDMI_TX_SCL   | Default |      |  |
|     |                                                                                     |      | I2C2_SCL_M3   |         |      |  |
|     |                                                                                     |      | CAN0_TX_M1    |         |      |  |
|     |                                                                                     |      | UART9_TX_M2   |         |      |  |
|     |                                                                                     |      | PWM2_CH0_M1   |         |      |  |
|     |                                                                                     |      |               |         |      |  |

|    |                                                                                                                 |      |                  |         |      |  |
|----|-----------------------------------------------------------------------------------------------------------------|------|------------------|---------|------|--|
|    |                                                                                                                 |      | GPIO4_C2_d       |         |      |  |
| E3 | DSM_AUD_LN_M1/HDMI_TX_HPDIN_M0/PCIE1_CLKREQN_M3/I2C7_SDA_M3/EDP_TX_HPDIN_M0/UART11_RX_M2/PWM0_CH1_M1/GPIO4_C1_d | AK2  | DSM_AUD_LN_M1    | Default | 3.3V |  |
|    |                                                                                                                 |      | HDMI_TX_HPDIN_M0 |         |      |  |
|    |                                                                                                                 |      | PCIE1_CLKREQN_M3 |         |      |  |
|    |                                                                                                                 |      | I2C7_SDA_M3      |         |      |  |
|    |                                                                                                                 |      | EDP_TX_HPDIN_M0  |         |      |  |
|    |                                                                                                                 |      | UART11_RX_M2     |         |      |  |
|    |                                                                                                                 |      | PWM0_CH1_M1      |         |      |  |
|    |                                                                                                                 |      | GPIO4_C1_d       |         |      |  |
| E4 | DSM_AUD_LP_M1/SAI4_MCLK_M2/HDMI_TX_CEC_M0/I2C7_SCL_M3/SPI4_CSN1_M0/UART11_TX_M2/PWM1_CH5_M1/GPIO4_C0_d          | AK3  | DSM_AUD_LP_M1    | Default | 3.3V |  |
|    |                                                                                                                 |      | SAI4_MCLK_M2     |         |      |  |
|    |                                                                                                                 |      | HDMI_TX_CEC_M0   |         |      |  |
|    |                                                                                                                 |      | I2C7_SCL_M3      |         |      |  |
|    |                                                                                                                 |      | SPI4_CSN1_M0     |         |      |  |
|    |                                                                                                                 |      | UART11_TX_M2     |         |      |  |
|    |                                                                                                                 |      | PWM1_CH5_M1      |         |      |  |
|    |                                                                                                                 |      | GPIO4_C0_d       |         |      |  |
| E5 | GND34                                                                                                           |      | GND34            |         |      |  |
| E6 | DP_TX_AUXN                                                                                                      | 2T3  | DP_TX_AUXN       |         |      |  |
| E7 | DP_TX_AUXP                                                                                                      | 2T2  | DP_TX_AUXP       |         |      |  |
| E8 | GND35                                                                                                           |      | GND35            |         |      |  |
| E9 | HDMI_TX_SBDN/EDP_TX_AUXN                                                                                        | 2U12 | HDMI_TX_SBDN     | Default |      |  |
|    |                                                                                                                 |      | EDP_TX_AUXN      |         |      |  |

|     |                                                                                     |       |                          |         |      |  |
|-----|-------------------------------------------------------------------------------------|-------|--------------------------|---------|------|--|
| E10 | HDMI_TX_SBDP/EDP_TX_AUXP                                                            | 2T12  | HDMI_TX_SBDP             | Default |      |  |
|     |                                                                                     |       | EDP_TX_AUXP              |         |      |  |
| E11 | GND36                                                                               |       | GND36                    |         |      |  |
| E12 | MIPI_DPHY_CSI0_RX_D3P/NO_USE                                                        | AK24  | MIPI_DPHY_CSI0_RX_D3P    | Default |      |  |
|     |                                                                                     |       | /                        |         |      |  |
| E13 | MIPI_DPHY_CSI0_RX_D3N/MIPI_CPHY_CSI_RX_TRIO2_C                                      | AL24  | MIPI_DPHY_CSI0_RX_D3N    | Default |      |  |
|     |                                                                                     |       | MIPI_CPHY_CSI_RX_TRIO2_C |         |      |  |
| E14 | GND37                                                                               |       | GND37                    |         |      |  |
| E15 | MIPI_DPHY_CSI0_RX_D2P/MIPI_CPHY_CSI_RX_TRIO2_B                                      | AK23  | MIPI_DPHY_CSI0_RX_D2P    | Default |      |  |
|     |                                                                                     |       | MIPI_CPHY_CSI_RX_TRIO2_B |         |      |  |
| E16 | MIPI_DPHY_CSI0_RX_D2N/MIPI_CPHY_CSI_RX_TRIO2_A                                      | AL23  | MIPI_DPHY_CSI0_RX_D2N    | Default |      |  |
|     |                                                                                     |       | MIPI_CPHY_CSI_RX_TRIO2_A |         |      |  |
| E17 | GND38                                                                               |       | GND38                    |         |      |  |
| E18 | MIPI_DPHY_CSI2_RX_CLKP                                                              | 1AD21 | MIPI_DPHY_CSI2_RX_CLKP   |         |      |  |
| E19 | MIPI_DPHY_CSI2_RX_CLKN                                                              | 1AD22 | MIPI_DPHY_CSI2_RX_CLKN   |         |      |  |
| F1  | SAI4_SCLK_M2/VP2_SYNC_OUT/I2C6_SDA_M3/SPI4_CLK_M0/CAN1_RX_M1/PWM2_CH3_M1/GPIO4_C7_d | AJ1   | SAI4_SCLK_M2             | Default | 3.3V |  |
|     |                                                                                     |       | VP2_SYNC_OUT             |         |      |  |
|     |                                                                                     |       | I2C6_SDA_M3              |         |      |  |
|     |                                                                                     |       | SPI4_CLK_M0              |         |      |  |
|     |                                                                                     |       |                          |         |      |  |

|    |                                                                                                                           |      |                      |         |      |  |
|----|---------------------------------------------------------------------------------------------------------------------------|------|----------------------|---------|------|--|
|    |                                                                                                                           |      | CAN1_RX_M1           |         |      |  |
|    |                                                                                                                           |      | PWM2_CH3_M1          |         |      |  |
|    |                                                                                                                           |      | GPIO4_C7_d           |         |      |  |
| F2 | SAI4_SDI_M2/VP1_SYNC_OUT/PCIE0_CLKREQN_M3/SATA0_ACTLED_M1/I2C6_SCL_M3/SPI4_MISO_M0/CAN1_TX_M1/PWM2_CH2_M1/GPIO4_C6_d      | 1AE1 | SAI4_SDI_M2          | Default | 3.3V |  |
|    |                                                                                                                           |      | VP1_SYNC_OUT         |         |      |  |
|    |                                                                                                                           |      | PCIE0_CLKREQN_M3     |         |      |  |
|    |                                                                                                                           |      | SATA0_ACTLED_M1      |         |      |  |
|    |                                                                                                                           |      | I2C6_SCL_M3          |         |      |  |
|    |                                                                                                                           |      | SPI4_MISO_M0         |         |      |  |
|    |                                                                                                                           |      | CAN1_TX_M1           |         |      |  |
|    |                                                                                                                           |      | PWM2_CH2_M1          |         |      |  |
|    |                                                                                                                           |      | GPIO4_C6_d           |         |      |  |
| F3 | ISP_FLASH_TRIGOUT_M1/SAI4_SDO_M2/VP0_SYNC_OUT/SATA1_ACTLED_M1/I2C3_SDA_M3/SPI4_MOSI_M0/UART6_RX_M3/PWM2_CH5_M1/GPIO4_C5_d | AK1  | ISP_FLASH_TRIGOUT_M1 |         | 3.3V |  |
|    |                                                                                                                           |      | SAI4_SDO_M2          |         |      |  |
|    |                                                                                                                           |      | VP0_SYNC_OUT         |         |      |  |
|    |                                                                                                                           |      | SATA1_ACTLED_M1      |         |      |  |
|    |                                                                                                                           |      | I2C3_SDA_M3          |         |      |  |
|    |                                                                                                                           |      | SPI4_MOSI_M0         |         |      |  |
|    |                                                                                                                           |      | UART6_RX_M3          |         |      |  |
|    |                                                                                                                           |      | PWM2_CH5_M1          | Default |      |  |
|    |                                                                                                                           |      | GPIO4_C5_d           |         |      |  |
| F4 | ISP_PRELIGHT_TRIG_M1/SAI4_LRCK_M2/DP_HP                                                                                   | AL3  | ISP_PRELIGHT_TRIG_M1 |         | 3.3V |  |
|    |                                                                                                                           |      |                      |         |      |  |

|     |                                                                                |      |                                                            |         |  |  |
|-----|--------------------------------------------------------------------------------|------|------------------------------------------------------------|---------|--|--|
|     | DIN_M0/I2C3_SCL_M3/S<br>PI4_CSN0_M0/UART6_T<br>X_M3/PWM2_CH6_M1/G<br>PIO4_C4_d |      | SAI4_LRCK_M2                                               | Default |  |  |
|     |                                                                                |      | DP_HPDI0_M0                                                |         |  |  |
|     |                                                                                |      | I2C3_SCL_M3                                                |         |  |  |
|     |                                                                                |      | SPI4_CSN0_M0                                               |         |  |  |
|     |                                                                                |      | UART6_TX_M3                                                |         |  |  |
|     |                                                                                |      | PWM2_CH6_M1                                                |         |  |  |
|     |                                                                                |      | GPIO4_C4_d                                                 |         |  |  |
| F5  | GND39                                                                          |      | GND39                                                      |         |  |  |
| F6  | USB2_OTG1_VBUSDET                                                              | 2T10 | USB2_OTG1_VBUS<br>DET                                      |         |  |  |
| F7  | USB2_OTG1_ID                                                                   | 2T9  | USB2_OTG1_ID                                               |         |  |  |
| F8  | USB2_OTG0_VBUSDET                                                              | 2P3  | USB2_OTG0_VBU<br>SDET                                      |         |  |  |
| F9  | USB2_OTG0_ID                                                                   | 2R6  | USB2_OTG0_ID                                               |         |  |  |
| F10 | GND40                                                                          |      | GND40                                                      |         |  |  |
| F11 | MIPI_DPHY_CSI0_RX_CL<br>KP/MIPI_CPHY_CSI_RX_<br>TRIO1_C                        | AK22 | MIPI_DPHY_CSI0_<br>RX_CLKP<br>MIPI_CPHY_CSI_R<br>X_TRIO1_C | Default |  |  |
| F12 | MIPI_DPHY_CSI0_RX_CL<br>KN/MIPI_CPHY_CSI_RX_<br>TRIO1_B                        | AL22 | MIPI_DPHY_CSI0_<br>RX_CLKN<br>MIPI_CPHY_CSI_R<br>X_TRIO1_B | Default |  |  |
| F13 | GND41                                                                          |      | GND41                                                      |         |  |  |
| F14 | MIPI_DPHY_CSI0_RX_D1<br>P/MIPI_CPHY_CSI_RX_T<br>RIO1_A                         | AK21 | MIPI_DPHY_CSI0_<br>RX_D1P<br>MIPI_CPHY_CSI_R<br>X_TRIO1_A  | Default |  |  |



|     |                                                                                                    |       |                          |         |      |  |
|-----|----------------------------------------------------------------------------------------------------|-------|--------------------------|---------|------|--|
| F15 | MIPI_DPHY_CSI0_RX_D1N/MIPI_CPHY_CSI_RX_TRIO0_C                                                     | AL21  | MIPI_DPHY_CSI0_RX_D1N    | Default |      |  |
|     |                                                                                                    |       | MIPI_CPHY_CSI_RX_TRIO0_C |         |      |  |
| F16 | GND42                                                                                              |       | GND42                    |         |      |  |
| F17 | MIPI_DPHY_CSI0_RX_D0P/MIPI_CPHY_CSI_RX_TRIO0_B                                                     | AK20  | MIPI_DPHY_CSI0_RX_D0P    | Default |      |  |
|     |                                                                                                    |       | MIPI_CPHY_CSI_RX_TRIO0_B |         |      |  |
| F18 | MIPI_DPHY_CSI0_RX_D0N/MIPI_CPHY_CSI_RX_TRIO0_A                                                     | AL20  | MIPI_DPHY_CSI0_RX_D0N    | Default |      |  |
|     |                                                                                                    |       | MIPI_CPHY_CSI_RX_TRIO0_A |         |      |  |
| G1  | GND48                                                                                              |       | GND48                    |         |      |  |
| G2  | SAI0_SDI3_M1/SAI0_SDO1_M1/PDM0_SDI3_M0/I2C4_SDA_M0/GPU_AVS/PWM2_CH0_M0/UART1_RTSN_M0/GPIO0_D3_d    | 1AA23 | SAI0_SDI3_M1             |         | 3.3V |  |
|     |                                                                                                    |       | SAI0_SDO1_M1             |         |      |  |
|     |                                                                                                    |       | PDM0_SDI3_M0             |         |      |  |
|     |                                                                                                    |       | I2C4_SDA_M0              |         |      |  |
|     |                                                                                                    |       | GPU_AVS                  |         |      |  |
|     |                                                                                                    |       | PWM2_CH0_M0              |         |      |  |
|     |                                                                                                    |       | UART1_RTSN_M0            |         |      |  |
|     |                                                                                                    |       | GPIO0_D3_d               | Default |      |  |
| G3  | SAI0_SDI2_M1/SAI0_SDO2_M1/PDM0_SDI2_M0/I2C4_SCL_M0/CPUBIG_AVS/PWM1_CH5_M0/UART1_CTSN_M0/GPIO0_D2_d | 1Y22  | SAI0_SDI2_M1             |         | 3.3V |  |
|     |                                                                                                    |       | SAI0_SDO2_M1             |         |      |  |
|     |                                                                                                    |       | PDM0_SDI2_M0             |         |      |  |
|     |                                                                                                    |       | I2C4_SCL_M0              |         |      |  |
|     |                                                                                                    |       | CPUBIG_AVS               |         |      |  |
|     |                                                                                                    |       |                          |         |      |  |

|    |                                                                  |       |                |         |      |  |
|----|------------------------------------------------------------------|-------|----------------|---------|------|--|
|    |                                                                  |       | PWM1_CH5_M0    |         |      |  |
|    |                                                                  |       | UART1_CTSN_M0  |         |      |  |
|    |                                                                  |       | GPIO0_D2_d     | Default |      |  |
| G4 | SAI0_SDI1_M1/SAI0_SDO3_M1/PDM0_SDI1_M0/SPI0_MISO_M0/GPIO0_D1_d   | AC28  | SAI0_SDI1_M1   |         | 3.3V |  |
|    |                                                                  |       | SAI0_SDO3_M1   |         |      |  |
|    |                                                                  |       | PDM0_SDI1_M0   |         |      |  |
|    |                                                                  |       | SPI0_MISO_M0   |         |      |  |
|    |                                                                  |       | GPIO0_D1_d     | Default |      |  |
| G5 | SAI0_SDI0_M1/PDM0_SDI0_M0/SPI0_MOSI_M0/GPIO0_D0_d                | 1W23  | SAI0_SDI0_M1   |         | 3.3V |  |
|    |                                                                  |       | PDM0_SDI0_M0   |         |      |  |
|    |                                                                  |       | SPI0_MOSI_M0   |         |      |  |
|    |                                                                  |       | GPIO0_D0_d     | Default |      |  |
| G6 | SAI0_LRCK_M1/I2C3_SDA_M1/SPI0_CLK_M0/GPIO0_C7_d                  | 1Y23  | SAI0_LRCK_M1   |         | 3.3V |  |
|    |                                                                  |       | I2C3_SDA_M1    |         |      |  |
|    |                                                                  |       | SPI0_CLK_M0    |         |      |  |
|    |                                                                  |       | GPIO0_C7_d     | Default |      |  |
| G7 | SAI0_SCLK_M1/I2C3_SCL_M1/SPI0_CSN0_M0/GPIO0_C6_d                 | 1Y21  | SAI0_SCLK_M1   |         | 3.3V |  |
|    |                                                                  |       | I2C3_SCL_M1    |         |      |  |
|    |                                                                  |       | SPI0_CSN0_M0   |         |      |  |
|    |                                                                  |       | GPIO0_C6_d     | Default |      |  |
| G8 | SAI0_SDO0_M1/DP_HPDI_N_M1/UART10_RX_M2/I3C0_SDA_PU_M0/GPIO0_C5_d | 1AA22 | SAI0_SDO0_M1   |         | 3.3V |  |
|    |                                                                  |       | DP_HPDI_N_M1   |         |      |  |
|    |                                                                  |       | UART10_RX_M2   | Default |      |  |
|    |                                                                  |       | I3C0_SDA_PU_M0 |         |      |  |
|    |                                                                  |       | GPIO0_C5_d     |         |      |  |

|     |                                                               |      |                     |         |      |  |
|-----|---------------------------------------------------------------|------|---------------------|---------|------|--|
| G9  | SAI0_MCLK_M1/PDM0_CLK0_M0/UART10_TX_M2/PWM0_CH0_M0/GPIO0_C4_d | 1W22 | SAI0_MCLK_M1        |         | 3.3V |  |
|     |                                                               |      | PDM0_CLK0_M0        |         |      |  |
|     |                                                               |      | UART10_TX_M2        | Default |      |  |
|     |                                                               |      | PWM0_CH0_M0         |         |      |  |
|     |                                                               |      | GPIO0_C4_d          |         |      |  |
| G10 | GND49                                                         |      | GND49               |         |      |  |
| G11 | SPI2_MISO_M0/I2C0_SDA_M0/GPIO0_B1_z                           | 1P23 | SPI2_MISO_M0        |         | 1.8V |  |
|     |                                                               |      | I2C0_SDA_M0         |         |      |  |
|     |                                                               |      | GPIO0_B1_z          | Default |      |  |
| G12 | AUPLL_CLK_IN_M1/SPI2_CSN0_M0/I2C0_SCL_M0/GPIO0_B0_z           | 1U22 | AUPLL_CLK_IN_M1     |         | 1.8V |  |
|     |                                                               |      | SPI2_CSN0_M0        |         |      |  |
|     |                                                               |      | I2C0_SCL_M0         |         |      |  |
|     |                                                               |      | GPIO0_B0_z          | Default |      |  |
| G13 | SDMMC0_DET_N/SPI2_CSN1_M0/GPIO0_A7_u                          | 1U21 | SDMMC0_DET_N        |         | 1.8V |  |
|     |                                                               |      | SPI2_CSN1_M0        |         |      |  |
|     |                                                               |      | GPIO0_A7_u          | Default |      |  |
| G14 | GND50                                                         |      | GND50               |         |      |  |
| G15 | SARADC_VIN4                                                   | 1E18 | SARADC_VIN4         |         |      |  |
| G16 | SARADC_VIN3_HP_HOOK                                           | 1C19 | SARADC_VIN3_HP_HOOK |         |      |  |
| G17 | SARADC_VIN2_HW_ID                                             | 1B19 | SARADC_VIN2_HW_ID   |         |      |  |
| G18 | SARADC_VIN0_BOOT                                              | A25  | SARADC_VIN0_BOOT    |         |      |  |
| G19 | GND51                                                         |      | GND51               |         |      |  |
| H1  | GND52                                                         |      | GND52               |         |      |  |

|    |                                                                 |      |                |         |      |  |
|----|-----------------------------------------------------------------|------|----------------|---------|------|--|
| H2 | PDM0_CLK1_M0/HDMI_TX_CEC_M1/SPI0_CSN1_M0/PWM0_CH1_M0/GPIO0_C3_d | 1W21 | PDM0_CLK1_M0   |         | 3.3V |  |
|    |                                                                 |      | HDMI_TX_CEC_M1 |         |      |  |
|    |                                                                 |      | SPI0_CSN1_M0   |         |      |  |
|    |                                                                 |      | PWM0_CH1_M0    |         |      |  |
|    |                                                                 |      | GPIO0_C3_d     | Default |      |  |
| H3 | I2C0_SDA_M1/UART8_RX_M2/I3C0_SDA_M0/GPIO0_C2_d                  | 1V24 | I2C0_SDA_M1    | Default | 3.3V |  |
|    |                                                                 |      | UART8_RX_M2    |         |      |  |
|    |                                                                 |      | I3C0_SDA_M0    |         |      |  |
|    |                                                                 |      | GPIO0_C2_d     |         |      |  |
| H4 | I2C0_SCL_M1/UART8_TX_M2/I3C0_SCL_M0/GPIO0_C1_d                  | AB28 | I2C0_SCL_M1    | Default | 3.3V |  |
|    |                                                                 |      | UART8_TX_M2    |         |      |  |
|    |                                                                 |      | I3C0_SCL_M0    |         |      |  |
|    |                                                                 |      | GPIO0_C1_d     |         |      |  |
| H5 | I2C2_SDA_M0/UART1_RX_M0/CPULIT_AVS/PWM1_CH3_M0/GPIO0_C0_d       | AB29 | I2C2_SDA_M0    | Default | 3.3V |  |
|    |                                                                 |      | UART1_RX_M0    |         |      |  |
|    |                                                                 |      | CPULIT_AVS     |         |      |  |
|    |                                                                 |      | PWM1_CH3_M0    |         |      |  |
|    |                                                                 |      | GPIO0_C0_d     |         |      |  |
| H6 | I2C2_SCL_M0/UART1_TX_M0/NPU_AVS/PWM1_CH4_M0/GPIO0_B7_d          | 1W24 | I2C2_SCL_M0    | Default | 3.3V |  |
|    |                                                                 |      | UART1_TX_M0    |         |      |  |
|    |                                                                 |      | NPU_AVS        |         |      |  |
|    |                                                                 |      | PWM1_CH4_M0    |         |      |  |
|    |                                                                 |      | GPIO0_B7_d     |         |      |  |
| H7 | SDMMC0_PWREN/SDMMC1_DET_N2/HDMI_TX_HPDIN_M1/EDP_TX_H            | 1Y24 | SDMMC0_PWREN   | Default | 3.3V |  |
|    |                                                                 |      | SDMMC1_DET_N2  |         |      |  |

|     |                                                             |      |                 |         |      |  |
|-----|-------------------------------------------------------------|------|-----------------|---------|------|--|
|     | PDIN_M1/PWM1_CH2_M0/GPIO0_B6_d                              |      | HDMI_TX_HPDI_M1 |         |      |  |
|     |                                                             |      | EDP_TX_HPDI_M1  |         |      |  |
|     |                                                             |      | PWM1_CH2_M0     |         |      |  |
|     |                                                             |      | GPIO0_B6_d      |         |      |  |
| H8  | REF_CLK2_OUT/I2C1_SDA_M1/UART4_RX_M2/PWM1_CH1_M0/GPIO0_B5_d | AD29 | REF_CLK2_OUT    |         | 3.3V |  |
|     |                                                             |      | I2C1_SDA_M1     |         |      |  |
|     |                                                             |      | UART4_RX_M2     | Default |      |  |
|     |                                                             |      | PWM1_CH1_M0     |         |      |  |
|     |                                                             |      | GPIO0_B5_d      |         |      |  |
| H9  | REF_CLK1_OUT/I2C1_SCL_M1/UART4_TX_M2/PWM1_CH0_M0/GPIO0_B4_d | AD28 | REF_CLK1_OUT    |         | 3.3V |  |
|     |                                                             |      | I2C1_SCL_M1     |         |      |  |
|     |                                                             |      | UART4_TX_M2     | Default |      |  |
|     |                                                             |      | PWM1_CH0_M0     |         |      |  |
|     |                                                             |      | GPIO0_B4_d      |         |      |  |
| H10 | GND53                                                       |      | GND53           |         |      |  |
| H11 | PWR_CTRL3/I2C6_SDA_M0/GPIO0_A5_d                            | Y29  | PWR_CTRL3       |         | 1.8V |  |
|     |                                                             |      | I2C6_SDA_M0     |         |      |  |
|     |                                                             |      | GPIO0_A5_d      | Default |      |  |
| H12 | CLK_32K_IN/CLK0_32K_OUT/I2C6_SCL_M0/GPIO0_A2_d              | 1U23 | CLK_32K_IN      |         | 1.8V |  |
|     |                                                             |      | CLK0_32K_OUT    |         |      |  |
|     |                                                             |      | I2C6_SCL_M0     |         |      |  |
|     |                                                             |      | GPIO0_A2_d      | Default |      |  |
| H13 | REF_CLK0_OUT/AUPLL_CLK_IN_M0/GPIO0_A0_d                     | V29  | REF_CLK0_OUT    |         | 1.8V |  |
|     |                                                             |      |                 |         |      |  |

|     |                    |      |                        |         |  |  |
|-----|--------------------|------|------------------------|---------|--|--|
|     |                    |      | AUPLL_CLK_IN_M<br>0    |         |  |  |
|     |                    |      | GPIO0_A0_d             | Default |  |  |
| H14 | GND54              |      | GND54                  |         |  |  |
| H15 | SARADC_VIN7_LCD_ID | 1E19 | SARADC_VIN7_LC<br>D_ID |         |  |  |
| H16 | SARADC_VIN6        | 1D21 | SARADC_VIN6            |         |  |  |
| H17 | SARADC_VIN5        | 1D19 | SARADC_VIN5            |         |  |  |
| H18 | GND55              |      | GND55                  |         |  |  |

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